### 5th Semester (CBCS)

**B.E. Electronics and Instrumentation Engineering (EI)**  
Choice Based Credit System (CBCS)  
Semester - V

#### Management and Entrepreneurship Development  
(Common to EC/TC/EI/BM/ML)

<table>
<thead>
<tr>
<th>Subject Code</th>
<th>IA Marks</th>
<th>Exam Marks</th>
<th>Exam Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>15ES51</td>
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</table>

**Number of Lecture Hours /Week:** 04  
**Total Number of Lecture Hours:** 50

**Credits – 4** (Each module – 10 Hours)

**Module -1**  
**Management:** Nature and Functions of Management – Importance, Definition, Management Functions, Levels of Management, Roles of Manager, Managerial Skills, Management & Administration, Management as a Science, Art & Profession (Selected topics of Chapter 1, Text 1).  
**Planning:** Planning-Nature, Importance, Types, Steps and Limitations of Planning; Decision Making – Meaning, Types and Steps in Decision Making.  
(Selected topics from Chapters 4 & 5, Text 1).

**Module -2**  
**Organizing and Staffing:** Organization-Meaning, Characteristics, Process of Organizing, Principles of Organizing, Span of Management (meaning and importance only), Departmentalisation, Committees– Meaning, Types of Committees; Centralization Vs Decentralization of Authority and Responsibility;  
**Staffing:** Need and Importance, Recruitment and Selection Process  
(Selected topics from Chapters 7, 8 & 11, Text 1).  
**Directing and Controlling:** Meaning and Requirements of Effective Direction, Giving Orders; Motivation-Nature of Motivation, Motivation Theories (Maslow’s Need-Hierarchy Theory and Herzberg’s Two Factor Theory); Communication – Meaning, Importance and Purposes of Communication; Leadership-Meaning, Characteristics, Behavioural Approach of Leadership; Coordination-Meaning, Types, Techniques of Coordination; Controlling – Meaning, Need for Control System, Benefits of Control, Essentials of Effective Control System, Steps in Control Process  
(Selected topics from Chapters 15 to 18 and 9, Text 1).

**Module -3**  
**Social Responsibilities of Business:** Meaning of Social Responsibility, Social Responsibilities of Business towards Different Groups, Social Audit, Business Ethics and Corporate Governance (Selected topics from Chapter 3, Text 1).  
**Entrepreneurship:** Definition of Entrepreneur, Importance of Entrepreneurship, concepts of Entrepreneurship, Characteristics of successful Entrepreneur, Classification of Entrepreneurs, Myths of Entrepreneurship, Entrepreneurial Development models, Entrepreneurial development cycle, Problems faced by Entrepreneurs and capacity building for Entrepreneurship  
(Selected topics from Chapter 2, Text 2).
Module -4
Institutional Support for Business Enterprises: Introduction, Policies & Schemes of Central Level Institutions, State Level Institutions.(Selected topics from Chapter 4, Text 2).

Module -5
(Selected topics from Chapters 16 to 20 of Unit 3, Text 3).

Course Outcomes: After studying this course, students will be able to:
1. Learn and explain basic is management and acquire basic managerial skills.
2. Analyze the nature, purpose & objectives of Planning, Organizing & Staffing.
3. Develop the factual leadership qualities for development of organizations
4. Learn and build the qualities and characteristics of business ethics and entrepreneurs.
5. Describe the importance of small scale industries in economic development and institutional support to start a small scale industry and implement.
6. Demonstrate the project management, product planning, project design and network analysis.

Question paper pattern
- The question paper will have TEN questions
- Each full question carries 16 marks
- There will be two full questions (with a maximum of THREE sub questions) from each module
- Each full question will have sub questions covering all topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

Reference Book:
Module -1
Introduction to Signals and Systems:
Basic elements of a DSP System, Classification of Signals, Sampling Theorem (statement and problems on Nyquist rate), Discrete Time Signals (Representation, Standard Signals, Classification, and Operations), Discrete Time Systems, Convolution Sum, Cross correlation and Auto correlation of sequences.
Text 1: 1.1.1, 1.2, 1.4.2, 2.1, 2.2, 2.3.3, 2.3.7, 2.6.1.

Module -2
Z- Transform and its Application to analysis of LTI Systems:
Direct Z-Transform, Properties of the Z-Transform, Examples, Inverse Z- Transform by Partial- Fraction Expansion method only, System Function of a LTI System, Causality and Stability (from H (z)).
Realization of Digital System: Direct Form I, Direct form II, cascade form and parallel form
Text 1: 3.1.1, 3.2, 3.4.3, 3.3.3, 3.5.3.
Text 2: 9.2, 9.3

Module -3
DFT: Properties and Applications:
Text 1: 7.1.3, 7.2, 7.3.1, 8.1.3.

Module -4
IIR & FIR Filters:
IIR Filters: Low-pass filter specifications, IIR filter Design by Impulse Invariance & Bilinear Techniques, Design of Digital IIR filter by Butterworth approach, Examples. Magnitude response of lowpass Chebyshev Type I, II filter (Theoretical concept only)
FIR Filters: Design of FIR filters – Symmetric and Antisymmetric FIR filters, Design of Linear phase FIR filters by Rectangular Hamming & Hanning windows. Summary of window function characteristics (window shape, transition bandwidth, stop band attenuation, etc.). Implementation of FIR filters by direct form and Single-stage lattice structure only.
Text 1: 10.3.2, 10.3.3, 10.3.4, 9.3.1, 9.3.3, 9.3.4, 10.2.1, 10.2.2, 10.2.7, 9.2.1, 9.2.4

Module -5
Multirate Digital Signal Processing & Adaptive Filters:
adaptive algorithm, Applications, Features & Architectural of TMS320C54XX processor.

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<th>Course Outcomes:</th>
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<td>1. Visualize, Classify and perform computation on discrete time signals, systems and properties</td>
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<tr>
<td>2. Perform the transformation techniques from time domain to other and vice versa, and analyze the system and properties (Z-Transform, DFT etc.)</td>
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<td>3. Realize / implement the Direct/ cascade/ parallel/ lattice forms of the given digital system (IIR/ FIR)</td>
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<td>4. Compute DFT by FFT algorithms</td>
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<td>5. Develop transformation from analog system to digital system and design and implement IIR and FIR filters</td>
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<td>6. Demonstrate the advanced concepts of signal processing (Multirate and Adaptive filtering) and architecture of DSP processor</td>
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<td>Each full question will have sub questions covering all the topics under a module.</td>
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<td>The students will have to answer FIVE full questions, selecting ONE full question from each module.</td>
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B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - V

Process Control Systems

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<td>20</td>
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</table>

Credits – 4 (Each module – 10 Hours)

Module -1
Introduction to Process Control and Final Control Operations:
Introduction, Process control principles, Process control block diagram, Control system evaluation, Analog and Digital Processing, Analog data representation.
Final Control: Introduction, Final control operation, Signal conversions, Actuators, Control elements.
(Numerical problems on all topics)

Module -2
Controller Principles: Introduction, Process characteristics, Control system parameters, Discontinuous controller modes: Two position, multiposition, floating control modes. Continuous controller modes: Proportional (P), Integral (I), Derivative (D) control modes, Composite controller modes: PI, PD, PID modes. (Problems on all types of controller modes).

Module -3
Analog Controllers: Introduction, General features, Electronic controllers, Error detector, Single mode, Composite controller modes, Pneumatic controllers, Design considerations. (Numerical problems on all topics).
Digital Controllers: Digital electronic methods, Simple alarms, Two position control, Multivariable alarms, Data logging, Supervisory computer control (SDC) and Direct digital control. Sampled data systems, Input data operations. Controller Modes Software-Error, P, I, D, & PID control mode software.

Module -4
Basic Instrumentation symbols, Process instrumentation & drawing (P & ID) symbols.

Module - 5
Modeling and Simulation for Plant Automation: Introduction, definition of terms, Need of system modeling, Uses of system simulation, how to build the mathematical model of a plant, Model evaluation and improvement, modern tools for modeling and simulation of systems, application examples, future perspectives.
Multivariable & Intelligent Controllers:

Course Outcomes: After studying this course, students will able to:
1. Discuss the principles of process control, evaluation, data representation and the elements of final control operation.
2. Analyze the principle and working of continuous and discontinuous controller modes.
3. Design analog controllers based on op-amps and pneumatic systems.
4. Discuss the principle and working digital controllers and implementation of controller mode software, concepts and applications of modelling and simulation of process plant.
5. Analyze control loop characteristics, control system quality and process loop tuning, and sketch the basic process instrumentation symbols.
6. Describe the fundamental concepts of multivariable and intelligent controllers.

**Question Paper Pattern:**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**
2. Computer Based Industrial Control by Krishna Kant, PHI, New Delhi 1997. (Module 5)

**Reference Books:**
4. Lessons in Industrial Instrumentation by Tony R. Kuphaldt, Creative Commons Attribution License (open source textbook), Sept. 2008. (for basic instrumentation symbols, 6.5.1, 6.5.2, 6.5.3, 6.5.4, 6.5.9).
### Biomedical Instrumentation

<table>
<thead>
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<td>: 80</td>
<td>: 03</td>
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#### Module -1
**Fundamentals of Biomedical Instrumentation:** Sources of biomedical signals, Basic Medical Instrumentation system, Performance requirements of medical instrumentation systems. PC based medical instruments, General constraints in design of biomedical instrumentation systems.

**Bioelectric Signals and Electrodes:** Origin of Bioelectric signals, Types of bioelectric signals- ECG, EEG, EMG, Recording electrodes: Electrode – Tissue interface, polarization, skin contact- impedance, Silver-silver chloride electrodes, Electrodes for ECG, EEG, EMG, Microelectrodes.

#### Module -2
**Electrocardiograph:** Physiology of the heart, Electrical activity of the heart and Electrocardiogram (ECG), Normal & Abnormal cardiac Rhythms, Block diagram-description of an Electrocardiograph, ECG leads, Effects of artifacts on ECG Recordings, Multi- channel ECG machine.

**Electroencephalograph:** Block diagram description of an Electroencephalograph, 10-20 electrode systems, computerized analysis of EEG. Electromyograph, Biofeedback instrumentation.

#### Module -3
**Patient Monitoring System:** Bedside patient monitoring systems, Central monitors, Measurement of heart rate – Average heart rate meter, Instantaneous heart rate meter, Measurement of pulse rate, Definition of oximeter & Pulse oximeter.

**Blood Pressure Measurement:** Introduction, Indirect methods of blood pressure measurement: Korotkoff’s method, Rheographic method, differential auscultatory technique, Oscillometric technique.

**Measurement of Respiration Rate:** Impedance pneumography, CO₂ method of respiration rate measurement, Apnoea detectors.

#### Module -4
**Blood Flow Measurement:** Electromagnetic blood flow meter- Principle and Square wave electromagnetic flowmeter. Doppler shift blood flow velocity meter, Blood flow measurement by Doppler imaging.

**Cardiac Output Measurement:** Measurement of continuous cardiac output derived from the aortic pressure waveform, ultrasound method.

**Cardiac Pacemakers and Defibrillators:** Need for cardiac pacemaker, External pacemaker, Implantable pacemaker, Types of Implantable pacemakers, Programmable pacemakers, Power sources for Implantable pacemaker.

Cardiac Defibrillator: Need for a Defibrillator, DC defibrillator, Pacer-Cardioverter-Defibrillator.

#### Module -5
**Therapeutic Instruments:**
Cardiac-assist devices, Pump oxygenators, Total artificial heart, Hemodialysis, Lithotripsy, Ventilators, Infant incubators, Drug infusion pumps, Ambulatory and Implantable Infusion systems, Anesthesia Machines, Electrosurgical unit.

**Patient Safety:** Electric shock hazards, Leakage currents, Electrical safety analyzer, Testing of Biomedical equipment

<table>
<thead>
<tr>
<th>Course Outcome:</th>
<th>After studying this course, students will able to:</th>
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<tbody>
<tr>
<td>1.</td>
<td>Acquire knowledge bout origin of bio-potential, bio-signals and their measurement</td>
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<tr>
<td>2.</td>
<td>Describe the problem, identify and formulate solution in the field of Bio-Medical Engineering for current and future issues</td>
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<td>3.</td>
<td>Describe the cardiac, brain and muscular physiological systems with the related diagnostic measurement methods.</td>
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<td>4.</td>
<td>Recognize the therapeutic methods of treatment and the associated instrumentation.</td>
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<td>5.</td>
<td>Identify and judge patient safety issues related to biomedical instrumentation.</td>
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<tr>
<td>6.</td>
<td>Describe the principle and working of cardiac pacemakers, defibrillators, BP measurement, blood flow meters, CO measurement, respiration measurements and their implementation.</td>
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**Question Paper Pattern:**
- The question paper will have TEN questions
- Each full question carry16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Book:**
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - V

VLSI Design
(Common to EI, BM & ML)

<table>
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<th>Subject Code</th>
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Credits – 3 (Each module – 8 Hours)

Module -1
Moore’s law, speed power performance, nMOS fabrication, CMOS fabrication: n-well, p-well processes, BiCMOS. Comparison of bipolar and CMOS.

Basic Electrical Properties of MOS And BiCMOS Circuits: Drain to source current versus voltage characteristics, threshold voltage, transconductance.

Module -2
Basic Electrical Properties of MOS And BiCMOS Circuits: nMOS inverter, Determination of pull up to pull down ratio, nMOS inverter driven through one or more pass transistors, alternative forms of pull up, CMOS inverter, BiCMOS inverters, latch up.

Basic Circuit Concepts: Sheet resistance, area capacitance calculation, Delay unit, inverter delay, estimation of CMOS inverter delay, driving of large capacitance loads, super buffers, BiCMOS drivers.

Module -3
MOS and BiCMOS Circuit Design Processes: MOS layers, stick diagrams, nMOS design style, CMOS design style, design rules and layout, \( \lambda \) - based design.

Scaling of MOS Circuits: scaling factors for device parameters, limitations of scaling.

Module -4
Subsystem Design and Layout-1 : Switch logic pass transistor, Gate logic inverter, NAND gates, NOR gates, pseudo nMOS, Dynamic CMOS, example of structured design, Parity generator, Bus arbitration, multiplexers, logic function block, code converter.

Subsystem Design and Layout-2 : Clocked sequential circuits, dynamic shift registers, bus lines, subsystem design processes, General considerations, 4-bit arithmetic processes, 4-bit shifter.

Module -5

Memory, Register and Aspects of Timing: Three Transistor Dynamic RAM cell, Dynamic memory cell, Pseudo- Static RAM, JK Flip-flop, D Flip-flop circuits, RAM arrays, practical aspects and testability: Some thoughts of performance, optimization and CAD tools for design and simulation.

Course Outcomes: After studying this course, students will able to:
1. Identify the CMOS layout levels, and the design layers used in the process sequence.
2. Describe the general steps required for processing of CMOS integrated circuits.
3. Design static CMOS combinational and sequential logic at the transistor level.
4. Demonstrate different logic styles such as complementary CMOS logic, pass-transistor Logic, dynamic logic, etc.

5. Interpret the need for testability and testing methods in VLSI.

**Question Paper Pattern:**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester - V

### Aeronautical Instrumentation

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<tr>
<th>Credits – 3 (Each module – 8 Hours)</th>
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**Module -1**  
**Aircraft Instruments:** Introduction-Qualitative and quantitative displays, basic T grouping of instruments, basics of Altitude Director Indicator (ADI) & Horizontal Situation Indicator.

**Air Data Instruments:** Pneumatic type and air data computers, International Standard Atmosphere (ISA), combined pitot-static probe, separate static probe, air speed indicator, instantaneous vertical speed indicator.

**Module -2**  
Altimeters, **Air Data Warning System:** Mach warning system, altitude alerts system, airspeed warning system.

**Module -3**  
**Directional Systems:** Earth’s total magnetic field, horizontal and vertical components of total field direct reading compass and its limitations, fluxgate detector units. gyro stabilized direction indicating systems.

**Module -4**  
**Gyroscopic Flight Instruments:** types of gyros-mechanical, ring laser gyros, fiber optic gyros and their limitations, basic mechanical gyro and its properties namely rigidity and precision, gyro horizon, direction indicator, turn and bank indicator.

**Module -5**  
**Engine Instruments:** pressure measurement (EPR), Temperature measurement (EGT), capacitance type volumetric fuel quantity indicator, densitometer, fuel quantity indicator by weight. Engine speed measurement, torque measurement, integrated impellor type flow meter.

**Course Outcomes:** After studying this course, students will able to

1. Outline the scope and extent of avionics and identify the types of flight instruments and display panels.
2. Describe the fundamentals of flight, basics of aircraft structures, propulsion and materials used in the development of an aircraft.
3. Comprehend the complexities involved during development of flight vehicles.
4. Recognize the fundamental applications of gyroscopic flight instruments in aircraft and analyses the performance of aircraft control system and interpret the results.
5. Evaluate the performance characteristics of engine instruments of aircraft and give better view and ways to improve efficiency.

**Question Paper Pattern:**

- The question paper will have TEN questions.
- Each full question carry 16 marks.
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
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<tbody>
<tr>
<td>1. Aircraft Instrumentation and Systems - S. Nagabhushana &amp; L.K. Sudha, IK International</td>
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B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - V

Remote Sensing and Telemetry

<table>
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Credits – 3 (Each module – 8 Hours)

Module -1
Introduction: Sun and Atmosphere, concept of signatures, remote sensing system, why observe earth from space? remote sensing – A Historic perspective, Indian remote sensing programme, the earth observation evolution – the paradigm shift legal and ethical aspects, electromagnetic radiation, velocity of EM radiation polarization coherent radiation, propagation of EM waves from one medium to another, attenuation, quantum of EM radiation, thermal radiation, source of EM Radiation for remote sensing.

Module -2
Remote Sensors – Classification of remote sensors, optical – infrared sensors, photographic camera, television cameras, opto - mechanical scanners, opto-mechanical scanners operated from satellites, pushbroom cameras, hyper-spectral imager, measuring the third dimension, image quality aspects.
Microwave Sensors: Antenna, passive microwave sensors, active microwave sensors, side looking radar, scatterometer, platforms, principles of satellite motion, locating a satellite in space, types of Orbit, Orbital perturbations, the spacecraft, global positioning system(GPS).

Module -3
Data Reception and Data Products: Data formats, ground segment organization, data product generation, referencing scheme, data products output medium, IRS data products, special processing, data analysis, visual image analysis, digital classification, classification accuracy.

Module -4
Telemetry System: Introduction, fundamental of RF telemetry, basic telemetry, system components of coding resolution, pulse code modulation, PCM multiplexing and conversion, PCM data transmission, PCMP PSD system. Theoretical comparison of telemetry systems.

Module -5
Applications of Remote Sensing for Earth Resources Management: Agriculture, forestry application, land cover/land use mapping water resources, snow and glacier, wetland management, marine fisheries, remote sensing for earth system science studies, geographical information system(GIS), date model, data entry data analysis example – urban land use suitability, spatial data infrastructure.

Course Outcomes: After studying this course, students will able to;
1. Acquire the different concepts associated with EM radiation.
2. Identify and classify the different remote and microwave sensors.
3. Illustrate the concepts of data acquisition for remote sensing.
4. Describe the telemetry system employed in remote sensing.
5. Discuss the Applications of Remote Sensing for Earth Resources Management.

Question Paper Pattern:
- The question paper will have TEN questions.
- Each full question carry 16 marks.
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

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### B.E. Electronics and Instrumentation Engineering (EI)
#### Choice Based Credit System (CBCS)
#### Semester - V

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<th>Product Design Technology and Ergonomics</th>
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**Credits – 3 (Each module – 8 Hours)**

- **Module -1**

- **Module -2**
  **Product Design Practice and Industry:** Introduction, Product Strategies, Time to Market, Analysis of the Product, The three S’s, Standardization, Renard Series (Preferred Numbers), Simplification,
  **The Designer:** The designer and His Role, Myth and Reality, The Industrial Design Organization, Basic Design Consideration, Problems faced by Industrial Designer, Procedure adopted by Industrial Designers,

- **Module -3**
  **Productivity:** Definition of productivity, individual enterprises, task of management Productivity of materials, land, building, machine and power.
  Measurement of productivity, factors affecting the productivity, productivity improvement programs.

- **Module -4**
  **Work Study:** Definition, objective and scope of work study. Human factor in work study, Work study and management, work study and supervision, work study and worker.
  **Introduction to Work Measurement:** Definition, objective and benefit of work measurement, Work measurement techniques

- **Module -5**
  Study of Development of Stress in Human body and their consequences, Computer based ergonomics.

**Course Outcomes:** After studying this course, students will able to;
1. Define product design and describe process of product design.
2. Analyze the strategies in the development of product from industry perspective.
3. Identify the role and responsibility of designer and the challenges in industrial design.
4. Evaluate the work study and work measurement.
5. Apply the concepts of ergonomics in product design and development.
6. Develop engineering, scientific and mathematical skills to execute a design from concept to finished product.

**Question Paper Pattern:**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
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Choice Based Credit System (CBCS)
Semester - V

Computer Organization
(Common to EI, BM & ML)

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Credits – 3 (Each module – 8 Hours)

Module -1

Module -2

Module -3

Module -4

Module -5

Course Outcomes: After studying this course, students will able to
1. Describe basic structure of computers, machine instructions and programs
2. Describe different addressing modes, output operations, Stacks and Queues, Subroutines and Additional Instructions, IEEE standard for Floating point Numbers
3. Apply the techniques accessing of I/O Devices, Interrupts, Direct Memory Access, Buses, Interface Circuits, and Standard I/O Devices.
4. Evaluate the concepts of Semiconductor RAM Memories, Read Only Memories, Cache, Memories, Performance Considerations and Virtual Memories.
5. Execute a Complete Instruction, Multiple Bus Organization, and Microprogrammed Control and Hardwired Control.
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<tbody>
<tr>
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</table>

<table>
<thead>
<tr>
<th>Text Books:</th>
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<table>
<thead>
<tr>
<th>Reference Books:</th>
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### Material Science

<table>
<thead>
<tr>
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<th>Exam Marks</th>
<th>Exam Hours</th>
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<tbody>
<tr>
<td>15EI562</td>
<td>20</td>
<td>80</td>
<td>03</td>
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</table>

- **Number of Lecture Hours/Week**: 03
- **Total Number of Lecture Hours**: 40
- **Credits**: 3 (Each module – 8 Hours)

#### Module -1
**Crystallography**: Forces between atoms, bonding in solids, ionic, covalent and metallic bonding; Fundamental concepts of crystals, lattice points and space lattice, crystal systems, Bravais lattices, directions, planes and Miller indices, atomic packing fraction, structure of simple cubic, body centered cubic (CsCl), face centered cubic (NaCl), hexagonal closed packed (HCP), diamond structure; X-ray diffraction, Bragg's law.

#### Module -2
**Characterization Techniques**: X-ray diffraction, powder X – ray diffractometer, construction and working, crystalline phase analysis, fundamentals of transmission electron microscopy and scanning electron microscopy(SEM), study of crystal structure using TEM, study of microstructure using SEM, scanning electron microscopy with EDS, construction and working, grain size and chemical analysis atomic force microscopy, construction and working, scanning tunneling microscope, construction and working.

#### Module -3
**Crystal Imperfections**: Point defects, vacancies and self-interstitials, impurities in solids, dislocations, linear defects, interfacial defects, bulk or volume defects edge and screw dislocation.
**Mechanical Behaviour**: Elastic behaviour of metals, stress-strain relation, Hooke's law, atomic model of elastic behaviour, plasticity, ductile and brittle materials, tensile strength, hardness, fatigue, creep, fracture, types of fracture.

#### Module -4
**Diffusion and Phase Transformation in Solids**: Fick's laws of diffusion, experimental determination of diffusion coefficient, Kirkendall effect, atomic model of diffusion. Time scale of phase changes, nucleation and growth, nucleation kinetics, applications, solidification and crystallization, glass transition.

#### Module -5
**Nanoscience**: Overview of nanotechnology, quantum effect, nanotechnology in nature, energy levels in nano-films, nano-wires and nano-dots. Growth techniques, physical vapor deposition, ball milling, lithography techniques, properties at nanoscale, size dependence, structural, chemical, optical, mechanical, electrical and magnetic properties. Applications of Nanomaterials: Sensors and actuators, catalysis, biomedical, advanced electronic materials, current challenges and future trends, safety and societal implications.

#### Course Outcomes: After studying this course, students will able to:
1. Explain fundamental concepts of crystal and crystal structure with examples.
2. Describe the principle and working of different instrumentation techniques for the characterization of crystals and materials.
3. Analyze the crystal Imperfections and mechanical behaviour of materials.
4. Discuss the concepts and experimentations in the areas of diffusion and phase transformation in solids
5. Discuss the basic concepts of Nanoscience and nano-materials with emphasis on applications in different fields, challenges, safety and societal implications

**Question Paper Pattern:**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
# Operating Systems
(Common to EI & BM)

<table>
<thead>
<tr>
<th>Subject Code</th>
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<th>Exam Marks</th>
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<tr>
<td>( \text{Total Number of Lecture Hours} = 40 )</td>
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</table>

**Credits – 3 (Each module – 8 Hours)**

**Module -1**


**Operating system structures**: OS Services, User-OS Interface, System calls, System programs, OS structure, System Boot.

Text: 1.1, 1.2, 1.3, 1.5, 1.6, 1.7, 1.8, 1.9, 1.11, 2.1, 2.2, 2.3, 2.5, 2.7, 2.10.

**Module -2**

**Process Management**

**Processes**: Process concept, Process scheduling, Operation on processes, Inter process communication.

**Threads** – Overview, Multithreading models, Threading issues.

**CPU scheduling** – Basic concepts, Scheduling criteria, Scheduling algorithms, real time scheduling.

Text: 3.1, 3.2, 3.3, 3.4, 4.1, 4.3, 4.6, 6.1, 6.2, 6.3, 6.6

**Module -3**

**Process Synchronization**: Background, The critical section problem, Peterson’s Solution, Synchronization hardware, Mutex Locks, Semaphores, Classical problems of synchronization, Monitors.

**Deadlock** – System model, Deadlock characterization, Methods for handling deadlocks, deadlock prevention, deadlock avoidance, deadlock detection and recovery from deadlock.

Text: 5.1, 5.2, 5.3, 5.4, 5.5, 5.6, 5.7, 5.8, 7.1, 7.2, 7.3, 7.4, 7.5, 7.6, 7.7

**Module -4**

**Memory management**:

**Main Memory**: Background, Swapping, Contiguous, allocation, Paging.

**Virtual memory**: Background, Demand paging, Copy-on-write, Page replacement.

Text: 8.1, 8.2, 8.3, 8.5, 9.1, 9.2, 9.3, 9.4

**Module -5**

**Storage Management**:

**Mass storage structure**: Overview of mass storage structure, Disk structure, Disk scheduling, Disk management, Swap space management.

**File System Interface**: File concept, Access methods, Directory and Disk structure, File system mounting, Protection.

**File System Structure**: File system structure, File system implementation, Directory implementation, Allocation methods, and free space management.

Text: 10.1, 10.2, 10.4, 10.5, 10.6, 11.1, 11.2, 11.3, 11.4, 11.6, 12.1, 12.2, 12.3, 12.4, 12.5
**Course Outcomes:** After studying this course, students will able to:

1. Define OS and explain organization of computer system, and components, computing environments, & typical structure of OS.
2. Analyze the process management, process scheduling and threads.
3. Describe the concepts of process synchronization and analyze the problems of synchronization.
4. Evaluate, prevent and avoid the deadlocks.
5. Develop the techniques of memory allocation and paging.
6. Apply appropriate disk scheduling algorithms.
7. Describe the interfaces to file systems, file structure and implement file systems and directory structure.

**Question Paper Pattern:**
- The question paper will have TEN questions.
- Each full question carry 16 marks.
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - V

Fundamentals of Nanotechnology

<table>
<thead>
<tr>
<th>Subject Code</th>
<th>IA Marks</th>
<th>Exam Marks</th>
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<table>
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<tr>
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</table>

Credits – 3 (Each module – 8 Hours)

Module -1

Module -2

Module -3

Module -4

Module -5
Nanotech Applications and Recent Breakthroughs: Introduction, Significant Impact of Nanotechnology and Nanomaterial, Medicine and Healthcare Applications, Biological and Biochemical Applications (Nano biotechnology), Energy Applications, Electronic Applications (Nano electronics), Computing Applications (Nano computers), Chemical Applications (Nano chemistry), Optical Applications (Nano photonics), Agriculture and Food Applications, Recent Major Breakthroughs in Nanotechnology.

Course Outcomes: After studying this course, students will able to;
1. Explain the fundamental concepts Nanotechnology.
2. Discuss different classes of carbon nano-materials and their characteristics
3. Describe different types of nano-materials and their characteristics.
4. Analyze and characterize nanomaterial using scientific and analytical instrumentation
5. Apply nanotechnology for multidisciplinary fields for the real life problems for better solutions.

Question Paper Pattern:
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

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<th>Text Books:</th>
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<table>
<thead>
<tr>
<th>Reference Books:</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Charles Poole, Frank Owens, “Introduction to Nanotechnology”, Wiley India, Student Edition</td>
</tr>
<tr>
<td>Laboratory Experiments:</td>
</tr>
<tr>
<td>----------------------------------------</td>
</tr>
<tr>
<td><strong>Signal Conditioning Circuits and Data Acquisition Lab</strong></td>
</tr>
<tr>
<td>(Common to EI, BM &amp; ML)</td>
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<tr>
<td><strong>Subject Code</strong>: 15 EI/BM/ML L57</td>
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<tr>
<td><strong>Number of Practical Hours/Week</strong>: 03</td>
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<td><strong>Credits</strong>: 2</td>
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<tr>
<td><strong>Revised Bloom’s Taxonomy Levels</strong>:</td>
</tr>
<tr>
<td>L1 – Remembering, L2 – Understanding,</td>
</tr>
<tr>
<td>L3 – Applying, L4 – Analyzing, L5 –</td>
</tr>
<tr>
<td>Evaluating, and L6 - Creating</td>
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</tbody>
</table>

**Laboratory Experiments:**

- **Note:** Standard design procedure to be adopted
- Students should build the circuit using discrete components and ICs (models are not to be used)

<table>
<thead>
<tr>
<th>Laboratory Experiments:</th>
<th>Revised Bloom’s Taxonomy (RBT)Level</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. To design and implement</td>
<td></td>
</tr>
<tr>
<td>• Inverting Amplifier and Inverting</td>
<td></td>
</tr>
<tr>
<td>Attenuator</td>
<td>L3, L4</td>
</tr>
<tr>
<td>• Non-Inverting Amplifier and Voltage</td>
<td></td>
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<tr>
<td>Follower</td>
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<tr>
<td>2. To realize</td>
<td></td>
</tr>
<tr>
<td>• Full wave Precision rectifier</td>
<td>L3, L4</td>
</tr>
<tr>
<td>3. To design and implement</td>
<td></td>
</tr>
<tr>
<td>• Butterworth I order Low-pass filter</td>
<td>L3, L4</td>
</tr>
<tr>
<td>• Butterworth II order High-pass filter</td>
<td></td>
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<tr>
<td>4. To design and implement</td>
<td></td>
</tr>
<tr>
<td>• RC Phase shift oscillator</td>
<td>L3, L4</td>
</tr>
<tr>
<td>• Wein Bridge oscillator</td>
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<tr>
<td>5. To realize</td>
<td></td>
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<tr>
<td>• ZCD</td>
<td>L3, L4</td>
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<tr>
<td>• Positive and Negative Voltage level</td>
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<tr>
<td>level detectors</td>
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<tr>
<td>6. To design and implement</td>
<td></td>
</tr>
<tr>
<td>• Astable Multivibrator using 555 timer</td>
<td>L3, L4</td>
</tr>
<tr>
<td>• Mono-stable Multivibrator using 555</td>
<td></td>
</tr>
<tr>
<td>timer</td>
<td></td>
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<tr>
<td>7. To realize</td>
<td></td>
</tr>
<tr>
<td>• Sample and Hold circuit using discrete</td>
<td></td>
</tr>
<tr>
<td>components</td>
<td>L3, L4</td>
</tr>
<tr>
<td>8. To realize</td>
<td></td>
</tr>
<tr>
<td>• Programmable Gain Amplifier using Analog Mux</td>
<td>L3, L4</td>
</tr>
<tr>
<td>9. To design and implement</td>
<td></td>
</tr>
<tr>
<td>• 4 bit R-2R DAC using discrete</td>
<td>L3, L4</td>
</tr>
<tr>
<td>components</td>
<td></td>
</tr>
<tr>
<td>10. To design and implement</td>
<td></td>
</tr>
<tr>
<td>• 8-bit DAC using IC (DAC 0800)</td>
<td>L3, L4</td>
</tr>
<tr>
<td>11. To design and implement</td>
<td></td>
</tr>
<tr>
<td>• 8-bit ADC using IC (ADC 0809)</td>
<td>L3, L4</td>
</tr>
<tr>
<td>12. To design and implement</td>
<td></td>
</tr>
<tr>
<td>• 3 bit Flash ADC using ICs</td>
<td>L3, L4</td>
</tr>
</tbody>
</table>
**Course Outcomes:** After studying this course, students will able to:

1. Sketch/draw circuit schematics, construct circuits on breadboards, analyze and troubleshoot circuits containing Op-amps, resistors, diodes, capacitors and independent sources.
2. Memorize and reproduce the manufacturer's data sheets of IC 555 timer, IC μA741 op-amp and data converters like IC ADC 0800 and IC DAC 0809.
3. Design and evaluate analog integrated circuits like Amplifiers, Oscillators, Active filters, Precision Rectifiers and Voltage level detectors, and compare the experimental results with theoretical values.
4. Demonstrate and analyze the working of Sample-Hold, Programmable gain amplifier and Analog Multiplexer circuits in data acquisition system.
5. Design and evaluate different resolution data converters using discrete components and ICs.

**Conduct of Practical Examination:**

1. All laboratory experiments are to be included for practical examination.
2. Students are allowed to pick one experiment from the lot.
3. Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.

**Reference Books:**

# B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - V

## Digital Signal Processing Lab

<table>
<thead>
<tr>
<th>Subject Code</th>
<th>IA Marks</th>
<th>Number of Hours/Week</th>
<th>Exam Marks</th>
<th>Total Number of Lecture Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>15EIL58</td>
<td>20</td>
<td>03</td>
<td>80</td>
<td>I+2P</td>
</tr>
</tbody>
</table>

### Credits -2

**USING ONLY MATLAB / SCILAB/OCTAVE**

1. Verify the Sampling theorem.
2. Determine linear convolution, Circular convolution and Correlation of two given sequences. Verify the result using theoretical computations.
3. Determine the linear convolution of two given point sequences using FFT algorithm.
4. Determine the correlation using FFT algorithm.
5. Determine the spectrum of the given sequence using FFT.
6. Design and test FIR filter using Windowing method (Hamming, Hanning and Rectangular window) for the given order and cut-off frequency.
7. Design and test Butterworth 1st and 2nd order low & high pass filter.
8. Design and test Chebyshev 1st and 2nd order low & high pass filter.

**USING DSP KIT / EMULATORS FROM TI/ ADSPSHARC/ MOTOROLA**

9. Linear convolution of two given sequences.
10. Circular convolution of two given sequences
11. Computation of N-point FFT of a given sequence.
12. Implementation of an FIR filters to meet given specifications.
13. Implementation of an IIR filters to meet given specifications.

### Course Outcomes: After studying this course, students will able to;

1. Write programs using Matlab / Scilab/Octave to demonstrate the DSP concepts on sampling, convolution and correlation, and implementation of the same using DSP kit.
2. Write programs using Matlab / Scilab/Octave for generation and computation of discrete signals.
3. Write program using Matlab / Scilab/Octave to apply FFT/DFT algorithm to determine spectrum of a given signal, and implementation of the same using DSP kit.
4. Write programs using Matlab / Scilab/Octave to design and evaluate different types of low and high pass filters.
5. Design and demonstrate IIR and FIR filters using Matlab / Scilab/Octave programs and DSP Kit.
6. Design and demonstrate DSP system applications in noise cancellation, communication, biomedical signal processing.

### Reference Books:

6th SEMESTER (CBCS)

B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VI

Analog and Digital Communication Systems
(Common to EI, BM & ML)

Subject Code : 15EI/BM/ML61
IA Marks : 20

Number of Lecture Hours / Week : 04
Exam Marks : 80

Total Number of Lecture Hours : 50
Exam Hours : 03

Credits – 4 (Each module – 10 Hours)

Module -1
Introduction to analog and Digital Communication, Historical Background and Applications.

Module -2
Angle Modulation: Basic Definitions, Properties of Angle-Modulated Waves, Relationship between PM and FM Waves, NBFM, WBFM, Transmission Bandwidth of FM Waves, Generation of FM waves, Demodulation of FM Signals, Theme Example.(Text 1:Chapter 4 )

Module -3
Pulse Modulation: Transition from Analog to Digital Communications: Sampling Process, PAM, Completing the Transition from Analog to Digital, Quantization Process, PCM, Delta Modulation, Theme Examples.(Text 1: 5.1, 5.2, 5.4, 5.5, 5.6, 5.7, 5.10)

Module -4

[Note: Excluding Computer Experiments in all the above Modules]

Module -5
Wireless Personal Area Networks (WPAN):Network Architecture, WPAN Components, WPAN Technologies and protocols (Bluetooth & Zigbee), WPAN Applications.(Text2: 4.1, 4.2, 4.3, 4.4, 4.5)
Wireless Wide Area Networks: Cellular Networks: Principles, GSM, CDMA, Handover in Cellular Networks.( Text 2: 7.1 (Excluding GPRS and CDPD))

Course Outcomes: After studying this course, students will be able to:
1. Explain the basics concepts of analog modulation techniques.
2. Discuss the basic concepts of digital modulation techniques.
3. Describe the basic concepts of digital data and pulse communication.
4. Explain and analyze different digital modulation techniques.
5. Describe different wireless area networks and their applications.
### Question Paper Pattern

- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

### Text Book:


### Reference Books:

B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VI

**Power and Industrial Electronics**

<table>
<thead>
<tr>
<th>Subject Code</th>
<th>IA Marks</th>
<th>Exam Marks</th>
<th>Total Number of Lecture Hours</th>
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<tbody>
<tr>
<td>15EI62</td>
<td>20</td>
<td>80</td>
<td>50</td>
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</tbody>
</table>

Number of Lecture Hours / Week: 04
Exam Hours: 03

Credits – 4 (Each module – 10 Hours)

**Module -1**
**Introduction:** Applications of power electronics, power semiconductor devices, control characteristics, types of power electronic circuits, peripheral effects.
Power BJT's, switching characteristics, switching limits, base-drive control, introduction to IGBT, and MOSFET, isolation of gate and base drives.

**Module -2**
**Thyristsors:** Introduction, static characteristics of SCR, two transistor model, turn-on and turn off methods, di/dt and dv/dt protection of SCR, series and parallel operation of thyristors, thyristor firing circuit using UJT.
**Commutation Techniques:** Introduction, natural commutation, forced commutation: self-commutation, impulse commutation.

**Module -3**
**Controlled Rectifiers:** Introduction, principle of phase controlled converter operation, single-phase semi converters, full converters and dual converters.
**AC Voltage Controllers:** Introduction, principle of ON-OFF and phase control, single-phase bidirectional controllers with resistive and inductive loads.

**Module -4**
**DC Choppers:** Introduction, principle of step-down operation, step-down chopper with R- L loads, Principle of step-up operation, Classification of DC Choppers.
**DC Drives:** Introduction, basic characteristics of DC Motors, operating modes single phase Full-converter drives, Introduction to stepper motor

**Module -5**
**Inverters:** Introduction, principle of operation, single phase bridge inverters, three phase inverters, voltage control of single phase inverters, current source inverter
**Applications of Power electronics:** Power supplies, switched- mode DC power supplies and configurations, AC power supplies (UPS only), Industrial applications.

**Course Outcomes:** After studying the course, students will be able to:
1. Explain static and dynamic characteristics of power semiconductor devices.
2. Analyze operation, model, characteristics, and turn-on and turn-off methods of devices.
3. Describe the converter circuits and their operation using Thyristors, BJT, MOSFET etc.
4. Analyze, evaluate and apply the power converter circuits in Industry
5. Apply the knowledge in the Domestic/Industrial control system applications.
**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Book:**

**Reference Books:**
B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester – VI  

**OOPS with C++**  
(Common to EI, BM & ML)  

<table>
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<th>Number of Lecture Hours/Week</th>
<th>Exam Marks</th>
<th>Total Number of Lecture Hours</th>
<th>Exam Hours</th>
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<td>20</td>
<td>04</td>
<td>80</td>
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Credits – 4 (Each module – 10 Hours)  

**Module -1**  
**C++ Programming Basic**: Need for object oriented programming, Characteristics of OOPS, Preprocessor directives, Data types, Manipulators, Boolean types, Functions : Functions Declaration, Calling the function, function definition, Passing Arguments to functions, Returning values from functions, Reference arguments, Overloaded functions, inline functions. (Text Book 1)  

**Module -2**  
**Classes and Objects**: Classes & Objects, Specifying the class, Defining objects, calling members functions, objects as data types, constructors, Destructors, overloaded constructors. Arrays: arrays as class member data types, passing arrays, arrays as objects, friend function & friend classes. (Text Book 1)  

**Module -3**  
**Strings and Pointers**: Strings: String variable & string constants, coping a string, array of a string, string as class members, user defined string type. Pointers : Pointers and arrays, pointers & functions, pointers & strings, pointers to objects,’this’ pointer, Array of pointers to objects, memory management using keywords new & delete (Text Book 1)  

**Module -4**  
**Operator Overloading and Inheritance**: Unary operators, binary operators.  
**INHERITANCE**: Derived class and base class, overriding member functions, scope resolution, Public & private inheritance, Public & private inheritance, levels of inheritance, multiple inheritance. (Text Book 1)  

**Module -5**  
**Virtual Functions and Polymorphism**: Virtual function, calling a virtual function through a base class reference, virtual attribute is inherited, virtual functions are hierarchical, Pure virtual functions, Abstract classes, using virtual function, early Vs late binding. (Text Book 2)  
**Templates**: Generic functions, A function with two generic types, explicitly overloading a generic functions, generic classes. Applying template classes, A generic classes using default arguments with template classes. Exception handling: Exception handling fundamentals, catching class types, using multiple catch statement. (Text Book 2)  

**Course Outcomes**: After studying this course, students will able to:  
1. Explain the basic concepts of OOPS.  
2. Apply the concept of OOPS to realize the existing algorithms.  
3. Analyse the given program for debugging to obtain correct output  
4. Create suitable application programs to solve real world problems.

**Question Paper Pattern**
- The question paper will have TEN questions
- Each full question carries 16 marks
- There will be TWO full questions (with maximum of FOUR sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**
1. Object oriented programming in TURBO C++ - Robert Lafore, Galgotia Publications, 2002 (Module 1, 2, 3, 4)

**Reference Books:**
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VI

Robots and Automation

<table>
<thead>
<tr>
<th>Subject Code</th>
<th>IA Marks</th>
<th>Exam Marks</th>
<th>Total Number of Lecture Hours</th>
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<td>15EI64</td>
<td>20</td>
<td>80</td>
<td>50</td>
<td>03</td>
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</table>

Credits – 4 (Each module – 10 Hours)

Module -1


Automation Concepts: SCADA, introduction and brief history of SCADA, SCADA systems software, distributed control system (DCS), introduction to the PLC, considerations and benefits of SCADA system. [Textbook-2]

Module -2

Robot Motion Analysis, Sensors and Control: Introduction to manipulator kinematics, homogeneous transformations and robot kinematics, manipulator path control, robot dynamics, configuration of a robot controller, types of end effectors, mechanical grippers, other types of grippers, tools as end effectors, robot/end effector interface, consideration in gripper selection and design, problems.

Sensors in Robotics: Transducers and sensors, sensors in robotics, tactile sensors, proximity and range sensors, uses of sensors in robotics, problems. [Textbook-1]

Module -3

Machine Vision, Robot Programming & Artificial Intelligence: Introduction to machine vision, sensing and digitizing function in machine vision, image processing and analysis, training the vision system, robotic applications, problems. Robot Programming: Methods of robot programming, lead - through programming methods, a robot program as a path in space, motion interpolation, wait, signal and delay commands, branching, capabilities and limitations of lead-through methods, problems.

Artificial Intelligence (AI): Introduction & goals of AI in research, AI techniques, LISP programming, AI & robotics, LISP in factory, robotic paradigms, and problems. [Textbook-1]

Module -4

Robotics in Manufacturing/Automation, Material Transfer, Machine Loading/Unloading: Robot cell layouts, multiple robots and machine interference, considerations in work-cell design, work-cell control, interlocks, error detection and recovery, work-cell controller, robot cycle time analysis, graphic simulation of robotic work-cells, problems.

Material Transfer, Machine Loading/Unloading: General considerations in robot material handling, material transfer applications, machine loading and unloading. [Textbook-1]

Module -5

Robots in Automatic Processing Operations, Assembly & Inspection: Introduction, spot welding, continuous arc welding, spray coating, other processing operations. Assembly and robotic assembly automation, parts presentation methods, assembly operations, compliance and remote center compliance
(RCC) device, assembly system configurations, adaptable programmable assembly system, designing for robotic assembly, inspection automation. [Textbook-1]

**Autonomous Mobile Robots: Introduction, Planning & Navigation:** Introduction, basic control scheme for mobile robots (only basic understanding of perception, localization, path planning & motion control). [Textbook-3]

---

**Course Outcomes:** After studying this course, students will be able to:

1. Identify basic components of robot system and its functionality
2. Identify DH representation of robot and homogenous transformation for various arm configurations.
3. Analyze the functions of sensors in the robot.
4. Solve forward and inverse kinematic problems.
5. Evaluate and compare the use Robots in different applications.
6. Recognize material-handling applications, processing operations, assembly and inspection operations to increase product quality and uniformity in minimize cycle times and effort.

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**Question Paper Pattern**

- The question paper will have TEN questions.
- Each full question carry 16 marks.
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

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**Text Books:**


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**Reference Books:**

**B.E. Electronics and Instrumentation Engineering (EI)**
Choice Based Credit System (CBCS)
Semester - VI

### Mechatronics

<table>
<thead>
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**Number of Teaching Hours/Week**: 03  
**Total Number of Lecture Hours**: 40  
**Credits – 3 (Each module – 8 Hours)**

### Module 1
**Introduction**: What is Mechatronics?, Design process, Systems, Measurement systems, Control systems, Examples of mechatronic systems: Digital camera with autofocus, Engine management system.  
**Sensors and Transducers** (only selected topics): Smart sensors, Pneumatic sensors, Proximity switches, Pyroelectric sensors, Piezoelectric sensors, Tactile sensor. [Textbook-1]

### Module 2
**Pneumatic And Hydraulic Actuation Systems**: Actuation systems, Pneumatic and hydraulic systems, Directional control valves, Pressure control valves, Servo and proportional control valves, Process control valves, Rotary actuators.  
**Mechanical Actuation Systems**: Mechanical systems, Types of motion, Kinematic chains, Cams, Gears, Belt and chain drives, Bearings. [Textbook-1]

### Module 3
**Electrical Actuation Systems**: Electrical systems, Mechanical switches, Solenoids, D.C. motors, A.C. motors, Stepper motors.  
**Fault Finding**: Fault-detection techniques, Watchdog timer, Parity and error coding checks, Common hardware faults, Microprocessor systems, Emulation and simulation. [Textbook-1]

### Module 4
**Interfacing Microcontrollers with Actuators**: Introduction, Interfacing with general purpose three state transistors, Interfacing relays, Interfacing solenoids, Interfacing stepper motors, interfacing permanent magnet motors, Interfacing sensors, Interfacing with DAC, interfacing power supplies, Compatibility at an interface.  
**Reliability**: Meaning of reliability, The life curve, Repairable and non-repairable systems, Failure or hazard rate models, Reliability systems, Response surface modeling. [Textbook-2]

### Module 5
**Components Based Modular Design and System Validation**: Introduction, Components based modular design view, System validation, Validation methodology, Validation scheme, Fusion technique- An example with vision system.  
**Integration**: Introduction, Background, Advanced actuators, Industrial robot, Autonomous guided vehicle (AGV), Drilling machine for PCB board. [Textbook-3]

Course Outcomes: After studying this course, students will be able to:
1. Describe and analyze the mechatronic systems and their associated systems
2. Describe and select sensors and transducers for different mechatronic applications.
3. Discuss and illustrate different types of actuation systems that can be employed in a mechatronic
4. Demonstrate the integration of mechatronic systems.
5. Identify and solve the faults in mechatronic systems and assess the reliability.
6. Design and develop microcontroller and actuator based mechatronic system.
7. Design modular system and perform validation.

**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
# Power Plant Instrumentation

<table>
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**Module -1**

**Coal Based Thermal Power Plants:** Rankine cycle - improvisations, Layout of modern coal power plant, Super Critical Boilers, FBC Boilers, Turbines, Condensers, Steam & Heat rate, Subsystems of thermal power plants – Fuel and ash handling, Draught system, Feed water treatment. Binary Cycles and Cogeneration systems.

**Module -2**


**Module -3**


**Module -4**

**Power From Renewable Energy:** Hydro Electric Power Plants – Classification, Typical Layout and associated components including Turbines. Principle, Construction and working of Wind, Tidal, Solar Photo Voltaic (SPV), Solar Thermal, Geo Thermal, Biogas and Fuel Cell power systems.

**Module -5**

**Energy, Economic And Environmental Issues Of Power Plants:** Power tariff types, Load distribution parameters, load curve, Comparison of site selection criteria, relative merits & demerits, Capital & Operating Cost of different power plants. Pollution control technologies including Waste Disposal Options for Coal and Nuclear Power Plants.

**Course Outcomes:** After studying this course, students will be able to:

1. Identify the resources of power generation and implementation
2. Describe the installation, operation, maintenance and control of coal based power plant
3. Recognize various analyzer for monitoring impurity, feed water and flue gas etc.
4. Evaluate the safety and boiler control system in power plants
5. Apply the knowledge to design, install, control and monitor the power plants as per the natural resources.

**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

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<tbody>
<tr>
<td>2. Thermal Engineering, R. K. Rajput, Laxmi Publication</td>
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</table>
### B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VI

#### Advanced Control Systems

<table>
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**Credits – 3 (Each module – 8 Hours)**

**Module -1**
**Design of Lag/Lead/Lag-lead compensators using Frequency domain (Bode plot) technique:** Lead, lag, lead lag network and compensator design using Bode techniques. Numerical problems
**Text 1:** 9.1, 9.2, 9.3, 9.4.

**Module-2**
**Design of Lag/Lead/Lag-lead compensators using Root Locus technique:** Lead, lag, lead lag network and compensator design using Root locus techniques. Numerical problems
**Text 1:** 7.1, 7.2, 7.3, 7.4, 7.5.

**Module -3**
**Nonlinear Systems:** Introduction, Common physical nonlinearities, phase-plane Method: Basic concepts, singular points, Stability of non –linear system, Construction of phase trajectories (by analytical method only). Describing function Method: Basic Concepts, Derivation of describing functions, Stability Analysis by Describing function Method,
**Text 3:** 15.1, 15.2, 15.7, 15.8, 15.9

**Module -4**
**Text 2:** 1.1, 1.2, 3.2, 3.4, 3.5, 4.3.

**Module -5**
**State space Analysis:** State-Space representations of discrete-Time systems, Solving Discrete-time state space equations, Controllability, Observability. Numerical problems
**Text 2:** 5.1, 5.2, 5.3, 6.2, 6.3.

**Course Outcomes:** After studying this course, students will be able to:
1. Explain concepts of Lag, Lead and lag-Lead networks and their design and implementation in controls systems
2. Design of control system for given time domain and frequency domain specifications
3. Describe the concept of nonlinearity and linearity of systems and stability analysis
4. Describe the concept of discrete control system, transform function and solutions and stability analysis
5. Develop model of physical process in state space form and solve state space equations.
6. Test the controllability and observability of a system.
**Question Paper Pattern**

- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**


**Reference Books:**

B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester – VI

Electrical Machines and Drives

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Module -1

Electrical Circuits and Transformers: Ohms law, series and parallel circuits Kirchhoff’s law mesh analysis, A.C. voltage – sinusoidal waves, power factor complex power Basic operation of transformers, EMF equation, Turns ratio, Losses and efficiency – simple problems

Module -2

Electrical Motors: Constructional details, principle of operation and performance characteristics of D.C. motors, single phase induction motor, three phase induction motor, synchronous motors, universal motors, stepper motors and reluctance motor

Module -3


Module -4

Electrical Drives: Type of Electrical Drives – Selection & factors influencing the selection – heating and cooling curves – loading condition and classes of duty – determination of power rating – simple problems

Module -5

Solid State Drives (Qualitative Treatment Only): Advantages of solid state drives – D.C. motor control using rectifiers and choppers – control of induction motor by V, V/f and slip power recovery scheme using inverters and A.C. power regulators

Course Outcomes: After studying this course, students will be able to:
1. Formulate and analyze the basic construction, working principle and characteristics of various electrical machines.
2. Evaluate the performance characteristics of motor drives for mechanical load requirements to design torque, speed and position controller in an energy efficient manner.
3. Describe the various electrical drives with basic structure, operation and characteristics.
4. Analyze the structure of electric drive systems and their role in various applications and requirements placed by mechanical systems on electric drives.
5. Illustrate the basic concepts of solid-state drives in controlling of DC motor and induction motors.
6. Design and install electrical machines and drives in an industrial environment.

Question Paper Pattern
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be **TWO** full questions (with maximum of **THREE** sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer **FIVE** full questions, selecting **ONE** full question from each module.

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B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VI

Mobile Communication
(Common to EI, BM & ML)

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Credits – 3 (Each module – 8 Hours)

Module -1
Wireless Transmission: Frequencies for radio transmission, signals, antennas, signal propagation, multiplexing, spread spectrum.
Medium Access Control: Motivation for a specialized MAC, SDMA, FDMA, TDMA, fixed TDM, Reservation TDMA, multiple access with collision avoidance, polling, inhibit sense multiple access, CDMA.

Module -2
Telecommunications Systems: GSM, mobile services, system architecture, radio interface, protocols, localization and calling, handover, security, new data services, DECT, system architecture TETRA, UMTS system architecture, UTRAN, core network.

Module -3
Broadcast Systems: Cyclic repetition of data, digital audio broadcasting, digital video broadcasting, convergence of broadcasting and mobile communications.

Module -4

Module -5
Mobile Network Layer: Mobile IP, Goals, assumptions and requirements, entities and terminology, IP packet delivery, agent discovery, registration, tunneling and encapsulation, optimizations, reverse tunneling, PIV6 343, IP micro-mobility support.

Course Outcomes: After studying this course, students will be able to:
1. Explain the basics of radio frequencies and their transmission, and distinguish the process of accessing the medium for wireless communication
2. Recognize and analyze various telecommunication systems such as DECT, 2G and 3G technologies.
3. Describe the working of satellites and broadcasting system.
4. Discuss the issues involved in different ad-hoc routing and networking.
5. Explain wireless communication standards, network devices and their functions within a network.
6. Apply the techniques for transmitting the data packet through Network/TCP layer of ISO/OSI model.
7. Demonstrate the implementation of simple mobile communication system.

**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
MEMS and NEMS

<table>
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Credits – 3 (Each module – 08Hours)

Module -1
Introduction: Design of MEMS & NEMS, Biological & Biosystems analogies, overview & applications of nano& micro electromechanical systems, systems, devices & structures definition, microfabrication, synergetic paradigms in MEMS, architectures of MEMS & NEMS.

Module -2
Fundamentals of MEMS Fabrication: Basic processes, microfabrication& micromachining of ICs, microstructures & microdevices, MEMS Fabrication Technologies – bulk, surface, LIGA.

Module -3
Devising and Synthesis of MEMS & NEMS: MEMS Motion microdevices classifier & synthesis, nano-electromechanical systems.

Module -4

Module -5

Course Outcomes: After studying this course, students will be able to:
1. Explain the operation of micro devices, micro systems and their applications
2. Discuss in-depth the MEMS and NEMS architectures.
3. Apply the knowledge in microfabrication processes
4. Recognize the basic approaches involved in the various sensor and actuator designs
5. Model, simulate and analyze micro-transducers and micro-motors of a typical MEMS/NEMS.

Question Paper Pattern
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:
1. Sergey Edward Lyshevski, “MEMS and NEMS: Systems, Devices, and Structures (Nano- and
Reference Books:

B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester - VI  

Embedded System Design and Programming  
(Common to EI, BM & ML)  

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| Number of Lecture Hours /Week | : 03 |
| Total Number of Lecture Hours | : 40 |

Credits – 3 (Each module – 08Hours)

Module -1  
**Introduction:** An Embedded System, Characteristics and Constraints of an Embedded system, Processor embedded into a system, Embedded Hardware units and devices in a system, Software embedded in a system, Design metrics, examples of embedded system.

Module -2  
**Embedded Microcontroller Core and Architecture:** 8051 micro controller; Architecture; Instruction sets; Assembly language programming. Timer / counter programming, Serial Communication; Interrupts.

Module -3  
**Processor and Memory Organization:** Structural Units in a Processor; Memory Devices, Memory selection for an embedded system, Processor selection, Direct Memory Access, DMA controllers.

Module -4  
**Interrupt Servicing (Handling) Mechanism:** Context and the periods for context switching; Deadline and interrupt latency. Language Features: Parameter passing, Recursion, Dynamic allocation, Typing, exception handling, abstract data typing, modularity.

Module -5  
**Real Time Kernels:** Real Time and Embedded Operating Systems; Interrupt Routines in RTOS environment; co routines, Interrupt driven systems, Foreground/background systems, Full-featured Real Time Operating Systems.  
**Real Time Specifications and Design Technique:** Mathematical specifications, flow charts, structure charts, Finite state automata, data flow diagrams, Petri Nets, WarnierQrr Notation.

**Course Outcomes:** After studying this course, students will be able to:  
1. Explain different embedded systems and their design metrics.  
2. Discuss the 8051 microcontroller architecture and instruction set  
3. Write ALP for implementation of mathematical and logical operations.  
4. Illustrate accessing I/O devices, direct memory access, buses, and interface circuits.  
5. Evaluate interrupt latency, context switching and different interrupt handling mechanisms.  
6. Design an embedded system based on real-time specifications.

**Question Paper Pattern**  
- The question paper will have TEN questions.  
- Each full question carry 16 marks  
- There will be TWO full questions (with maximum of THREE sub questions) from each module.  
- Each full question will have sub questions covering all the topics under a module.
The students will have to answer FIVE full questions, selecting ONE full question from each module.

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B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VI

Statistics and Numerical Methods
(Common to EI & BM)

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</table>

Credits – 3 (Each module – 08 Hours)

Module -1:
Testing of Hypothesis- Large sample test based on Normal distribution for single mean and difference of means - Tests based on t, Students t-test, statistical significance ‘p’ value, lambda^2 and F distributions for testing means and variances – Contingency table (Test for Independency) – Goodness of fit.

Module -2:
Design of Experiments- One way and two way classifications - Completely randomized design – Randomized block design – Latin square design - 22 factorial design.

Module -3:

Module -4:

Module -5:

Course Outcomes: After studying this course, students will be able to:
1. Perform hypothesis testing.
2. Apply completely randomized design for the experimental data classification.
3. Develop and implement iterative methods to solve algebraic and transcendental equations, simultaneous linear equations, and ordinary differential equations.
4. Apply the knowledge of finite differences, interpolation, numerical differentiation and numerical integration to different area.
5. Apply statistical and numerical methods for the analysis of simple and complex signals/data related to projects, research and real world problems.

Question Paper Pattern
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

### Text Books:

### Reference Books:
### Power Electronics, Controls and Communication Lab

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Credits:02

1. Static VI characteristics of SCR.
2. Static VI characteristics of Triac. (First and Third mode only).
3. Controlled half wave rectifier using R & RC triggering.
4. AC voltage controller using Triac & Diac.
5. Full wave controlled rectifier using one SCR and four diodes.
6. To determine the step response of 1st order system using RC circuit and to measure time constant.
7. To determine the step response of 2nd order system using RLC circuit and to determine Time domain specifications for under damped and critically damped conditions. Verification using theoretically calculated values.
8. To study the frequency response of Lag, Lead and Lag-lead Network.
10. Stability analysis of a given Transfer Function based on Bode plot / Root locus / Nyquist plots using Matlab / LabVIEW codes.
11. To design and test tuned amplifier using BJT/FET/ MOSFET.
12. Amplitude modulation using transistor/FET/ MOSFET(Generation and detection).
13. Frequency modulation using IC 8038/2206 and demodulation.
14. ASK &FSK: Generation and Detection.

**Course Outcomes:** After the completion of this Laboratory course, students will be able to:

1. Recognize and demonstrate functioning of semiconductor power devices.
2. Evaluate the characteristics, switching, power conversion and control by semiconductor power devices.
3. Design and analyze 1st and 2nd order control system, compensators and controllers, and evaluate stability of a system.
4. Design and verify the frequency tuning, AM and FM circuits used in communication systems.
5. Apply the concepts to build power electronic circuits, control and communication systems.

**Conduct of Practical Examination:**

- All laboratory experiments are to be included for practical examination.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
- Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VI

OOPs with C++ Lab
(Common to EI, BM & ML)

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Credit - 2

1) Write a C++ program to swap two Variables or Numbers by both call by value & call by reference.
2) Write a program to find largest, smallest & second largest of three numbers using inline functions MAX & Min.
3) Write a C++ program to calculate the volume of different geometric shapes like cube, cylinder and sphere using function overloading concept.
4) Design, develop and execute a program in C++ based on the following requirements:
   - An EMPLOYEE class containing data members & member functions:
     i) Data members: employee number (an integer), Employee_Name (a string of characters),
        Basic_Salary (in integer), All_Allowances (an integer), Net_Salary (an integer).
     ii) Member functions: To read the data of an employee, to calculate Net_Salary & to print the
        values of all the data members. (All_Allowances = 123% of Basic, Income Tax (IT) = 30% of gross salary (=Basic_Salary_All_Allowances_IT)).
5) Define a STUDENT class with USN, Name & Marks in 3 tests of a subject. Declare an array of 10 STUDENT objects. Using appropriate functions, find the average of the two better marks for each student. Print the USN, Name & the average marks of all the students.
6) Create a class for counting the numbers of objects created and destroyed within various block using constructors & destructors.
7) Write a C++ program to create class called MATRIX using two-dimensional array of integers, by
   overloading the operator == which checks the compatibility of two matrices to be added and
   subtracted. Perform the addition and subtraction by overloading + and – operators respectively.
   Display the results by overloading the operator <<. If (m1 == m2) then m3 = m1 + m2 and m4 = m1 – m2 else display error.
8) Demonstrate simple inheritance concept by creating a base class FATHER with data members:
    First Name, Sur name, DOB & bank Balance & creating a derived class SON, which inherits:
    Surname & Bank Balance feature from base class but provides its own feature: First Name & DOB.
    Create & initialize F1 & S1 objects with appropriate constructors & display the Father & Son details.
9) Write a C++ program with different class related through multiple inheritance & demonstrate the
    use of different access specified by means of members variables & members functions.
10) Write a C++ program to create three objects for a class named count object with data members
    such as roll_no & Name. Create a members function set_data( ) for setting the data values &
    display( ) member function to display which object has invoked it using ‘this’ pointer.
11) Write a C++ program to explain virtual function (Polymorphism) by creating a base class
    polygon which has virtual function areas Two classes rectangle & triangle derived from polygon
    & they have area to calculate & return the area of rectangle & Triangle respectively.
12) Write a program to define class name FATHER& SON that holds the income respectively
    calculate & display total income of a family using Friend function.
13) Write a program to accept the student detail such as name & 3 different marks by get_data ( ) method & display the name & average of marks using display ( ) method. Define a friend function for calculating the average marks using the method mark_avg ( ).

14) Write a programme to implement exception handling with minimum 5 exceptions classes including two built in exceptions.

Note: Additional C++ programs depicting / demonstrating high-end concepts and applications may be given as assignments.

<table>
<thead>
<tr>
<th>Course Outcome: After the completion of this Laboratory course, students will be able to:</th>
</tr>
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<tbody>
<tr>
<td>1. Write C++ program to solve simple and complex problems</td>
</tr>
<tr>
<td>2. Apply and implement major object oriented concepts like message passing, function overloading, operator overloading and inheritance to solve real-world problems</td>
</tr>
<tr>
<td>3. Use major C++ features such as Templates for data type independent designs and File I/O to deal with large data set.</td>
</tr>
<tr>
<td>4. Analyze, design and develop solutions to real-world problems applying OOP concepts of C++</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Conduct of Practical Examination:</th>
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</thead>
<tbody>
<tr>
<td>• All laboratory experiments are to be included for practical examination.</td>
</tr>
<tr>
<td>• Students are allowed to pick one experiment from the lot.</td>
</tr>
<tr>
<td>• Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.</td>
</tr>
<tr>
<td>• Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.</td>
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7th SEMESTER (CBCS)

B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VII

Automation in Process Control

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Credits – 4 (Each module – 10 Hours)

Module -1
Introduction to Programmable Logic Controllers (PLC): The digital concept, Analog signals, The input status file, the output status file, input and output status file, sixteen point I/O modules, PLC addressing, PLC memory.
Input modules: Discrete input modules, Discrete AC and DC input modules
Output modules: Discrete output modules, solid-state output module switching, relay output modules

Module-2
PLC Instructions: What is logic?, PLC programming languages, ladder programming- Conventional ladder Vs PLC ladder, the basic relay instructions: Normally open and normally closed, output and latching instructions, series and parallel function of AND, OR, NOT, XOR logic, Analysis of rung. Understanding relay instructions and the PLC input modules, interfacing start stop pushbutton and motor to PLC, developing ladder diagrams with analytical problems.

Module -3
Timers and Counter Instructions: Timer addressing, On delay, off delay and retentive timer instructions and associated status bits. Counter addressing, PLC counter up and down instructions and associated status bits.
Data Handling Instructions: Data handling instructions-MOVE, Masked Move, COPY. Sequencer instructions: Programming sequence output instructions, developing ladder diagram with analytical.

Module -4
Text 2: Ch.7; 7.1 To 7.8

Module -5
Supervisory Control and data Acquisition System: Basic Functions: Channel Scanning, conversion to Engineering units, Data Processing, Distributed SCADA System, Remote Terminal Unit, Reliable System Development Strategy.
Modeling and Simulation for Plant Automation: Introduction, Overview of Process Models, Model Based Automatic Control, System Modeling, uses of systems simulation, How to build the mathematical model of a plant, Model evaluation & improvement, Modern tools for modeling and simulation of systems.
Text 2: Ch.3; 3.6,3.7, 3.8 (3.8.1- 3.8.7), and Ch.11; 11.1,11.2,11.3,11.5,11.6,11.7,11.8,11.9

Course Outcomes: After studying this course, students will be able to:
1. Describe architecture, functioning and applications of PLC in automation.
2. Recognize various I/O modules of PLC and apply programming concepts to interface peripherals.
3. Write ladder diagram program using different PLC instruction sets.
4. Develop an automation system based on PLC ladder diagram program.
5. Analyze the basics of distributed control system and communication protocols used in automation industries.
6. Develop process automation system using SCADA and DSC.
7. Develop models of process automation using modern tools.

**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks.
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**
1. Introduction to Programmable Logic Controllers, Garry Dunning, 3rd edition, Centage Learning. (Modules: 1, 2 & 3)
2. Computer based Industrial Control, Krishna Kant, 2nd edition, PHI, 2015 (Modules: 4&5)

**Reference Books:**
2. T.A. Hughes, Programmable Controllers, Fourth edition, ISA press, 2005
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VII

Digital Image Processing

<table>
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<tr>
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</table>

Credits – 4 (Each module – 10 Hours)

Module -1
Introduction: Background, Examples of fields that use DIP, Fundamental steps in Digital Image Processing (DIP), Components of DIP system, Image sensing and acquisition. A simple image formation model, Image sampling and quantization. Basic relationship between pixels, Color image processing fundamentals and models.

Text: Chapter 1, 2.3, 2.4, 2.5

Module -2

Text: 3.1, 3.2, 3.3, 2.6.1, 2.6.2, 2.6.3, 2.6.4, 3.4, 3.5, 3.6

Module -3
Image Enhancement In Frequency Domain: Background, 2D-Discrete Fourier Transform and its inverse, Basic properties of the 2D-Discrete Fourier Transform, Basics of filtering in the frequency domain.

Image smoothing using frequency domain filters: Ideal low pass filters, Butterworth low-pass filters, Gaussian low-pass filters; Image sharpening using frequency domain filters, Ideal high-pass filters, Butterworth high-pass filters, Gaussian high-pass filters, Homomorphic filtering.

Text: 4.1, 4.2, 4.5.5, 4.6, 4.7, 4.8, 4.9

Module -4


Text: 5.1, 5.2, 5.3.1, 5.3.2, 8.1, 8.2.1, 8.2.3, 8.2.4, 8.2.5

Module -5
Image Segmentation: Fundamentals, Point detection, Line detection, Edge models, Edge detection, Canny edge detector.

Thresholding, Region based segmentation.

Text: 10.1, 10.2.1 – 10.2.6, 10.3, 10.4

Course Outcomes: After studying this course, students will be able to:
1. Describe the fundamentals of Image Processing and Image transform techniques.
2. Apply image enhancement technique in frequency and spatial domain
3. Analyze and implement restoration and color models.
4. Develop and analyze image compression techniques.
5. Apply segmentation algorithms for general image.
6. Develop image processing algorithms for real-world problems.

**Question Paper Pattern**
- The question paper will have TEN questions
- Each full question carry 16 marks.
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester - VII

**ARM Processor**  
(Common to EI, BM & ML)

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</table>

**Credits – 4 (Each module – 10 Hours)**

**Module -1**  
**ARM Embedded Systems**
Introduction, RISC design philosophy, ARM design philosophy, Embedded system hardware - AMBA bus protocol, ARM bus technology, Memory, Peripherals, Embedded system software – Initialization (BOOT) code, Operating System, Applications.

**ARM Processor Fundamentals**
ARM core dataflow model, registers, current program status register, Pipeline, Exceptions, Interrupts and Vector Table, Core extensions.

**Module -2**  
**Introduction to the ARM Instruction set:**
Introduction, Data processing instructions, Load - Store instruction, Software interrupt instructions, Program status register instructions, Loading constants, ARMv5E extensions, Conditional Execution.

**Module -3**
**Introduction to the THUMB instruction set:**
Introduction, THUMB register usage, ARM – THUMB interworking, Other branch instructions, Data processing instructions, Stack instructions, Software interrupt instructions.

**Efficient C Programming:**
Overview of C Compilers and optimization, Basic C Data types, C looping structures.

**Module -4**
**Exception and Interrupt Handling:**
Exception Handling-ARM Processor Exceptions and Modes, Vector Table, Exception Priorities, Link Register Offset, Interrupts- Interrupt Latency, Basic Interrupt Stack design and implementation, Interrupt Handling Scheme- Non nested Interrupt Handler, Nested Interrupt Handler, Reentrant Interrupt Handler, Prioritized Simple Interrupt Handler, Prioritized Standard Interrupt Handler, Prioritized Direct Interrupt Handler, Prioritized Grouped Interrupt Handler.

**Embedded Operating Systems:**
Fundamental Components, SLOS Directory Layout, Memory Interrupts and Exceptions handling, scheduler, Context Switch, Device Driver Framework.

**Module -5**
**CACHES:**
The memory Hierarchy and caches memory-caches and memory management units, Cache Architecture- basic architecture of caches memory, basic operation of cache controller, the relationship between cache and main memory.

**Memory Management Units:**
Moving from an MPU to an MMU, Virtual memory Working-Defining regions using pagers.
multitasking and the MMU, Memory organization in a virtual memory system, page tables Translational look aside buffer.

**Note:** Two or four tutorial classes need to be conducted (in a semester) to discuss the Embedded ARM Applications, such as GSM Chip and Bluetooth controller & assignment should be based on applications only.

**Course Outcomes:** After studying this course, students will be able to:

1. Depict the organization, architecture, bus technology, memory and operation of the ARM microprocessors
2. Employ the knowledge of Instruction set of ARM processors to develop basic Assembly Language Programs
3. Recognize the importance of the Thumb mode of operation of ARM processors and develop C programs for ARM processors
4. Describe the techniques involved in Exception and Interrupt handling in ARM Processors and understand the fundamental concepts of Embedded Operating Systems
5. Develop embedded C programs to interact with Built in Peripherals
6. Design, analyze and write programs using RTOS (Micro C/OS) on ARM based development boards.

**Question Paper Pattern**

- The Question paper will have TEN questions
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**


**Reference Books:**

B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)

Semester - VII

Smart Sensors and Intelligent Instrumentation

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<td>40</td>
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</table>

Credits – 3 (Each module – 08 Hours)

Module -1
Basics of smart sensors and micromachining: Introduction, Mechanical-Electronic transitions in sensing, nature of sensors, overview of smart sensing and control systems, integration of micromachining and microelectronics, introduction to micromachining, bulk micromachining, wafer bonding, surface micromachining, other micromachining techniques.

Module -2
MCUs and DSPs for sensor: Introduction, MCU control, MCUs for sensor interface, DSP control, Software, tools and support, sensor integration.

Module -3

Module -4

Module -5
Implications of Smart Sensor Standards and Recent Trends: Introduction, sensor plug-and-play, communicating sensor data via existing wiring, automated/remote sensing and web, process control over the internet, alternative standards, HVAC sensor chip, MCU with integrated pressure sensors, alternative views of smart sensing, smart loop.

Course Outcomes: After studying this course, students will be able to:

1. Describe the principle of smart sensors and process of micromachining in development of smart sensors.
2. Develop intelligent systems by interfacing the smart sensors to MCUs and DSPs.
3. Analyze the use of smart sensors in communication, MEMS and automation.
4. Evaluate the standards of smart sensors by the assessment of reliability testing and packaging.
5. Discuss the applications of smart sensors in different fields and recent development.
6. Develop/sketch the simple models of intelligent instrumentation.


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B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester - VII

Biomedical Signal Processing

<table>
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<th>Subject Code</th>
<th>IA Marks</th>
<th>Number of Lecture Hours /Week</th>
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<td>03</td>
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</table>

Credits – 3 (Each module – 08 Hours)

**Module -1**

**Introduction:** The nature of biomedical signals, objectives of biomedical signal analysis, difficulties encountered in biomedical signal analysis, Computer aided diagnosis. **Text-1: 1.1, 1.3, 1.4, 1.5**


Text-2: 4.1 to 4.9

**Module -2**

Filtering for Artifacts Removal: Random noise, structured noise and physiological interference, stationary versus non-stationary processes, typical case study, time domain filters with application: Synchronized averaging, moving-average filters.

Frequency domain filters with examples, removal of high frequency noise by Butterworth low pass filters, removal of low frequency noise by Butterworth high pass filter, removal of periodic artifacts by notch and comb filters. Weiner filter

Text-1: 3.1, 3.1.1, 3.1.2, 3.3, 3.3.1, 3.3.2, 3.3.3, 3.4, 3.4.1, 3.4.2, 3.4.3, 3.5.

**Module-3**

Basics of signal averaging, Signal averaging as a digital filter, A typical average, Software for signal averaging, Limitations of signal averaging.

Text-3: 9.1 to 9.5

Data Acquisition and classification of sleep stages, The Markov model and Markov chains, Dynamics of Sleep-wave Transitions, Hypnogram Model Parameters.

Text-2: 5.1 to 5.4

**Module -4**

ECG Parameters and their estimation, A review of wiener filtering problem, Principle of an adaptive filter, the steepest descent algorithm, Adoptive noise canceller, Cancellation 60Hz Interference in ECG, Cancelling Donor heart Interference in Heart-transplant ECG, Cancellation of Electrocardiographic signals from the electrical activity of chest muscles, Cancelling of maternal ECG in Fetal ECG, Cancellation of higher frequency noise in electro-surgery.

Text-2: 7.4, 6.1, 6.2, 6.3, 6.5, 6.6.

**Module -5**

Direct data compression techniques, Direct ECG data compression techniques, Transformation compression techniques, Other data compression techniques, Data compression techniques comparison.

Text-2: 8.1 to 8.5

**Course Outcomes:** After studying this course, students will be able to:
1. Discuss the origin, nature and characteristics of biomedical signals.
2. Identify the noise and artifacts in biomedical signals and apply suitable filters remove.
3. Apply the signal averaging technique.
4. Evaluate various event detection techniques for the analysis of the EEG and ECG.
5. Apply different data compression techniques on biomedical Signals.
6. Develop algorithms to process and analyze biomedical signals for better diagnosis.

**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**
3. Biomedical Digital Signal Processing-Willis J. Tompkins, PHI

**Reference Books:**
## Computer Communication Networks

<table>
<thead>
<tr>
<th>Subject Code</th>
<th>IA Marks</th>
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### Module -1
**Introduction:** Uses of Computer Networks, Network Hardware, Network Software, Reference Models, Example Networks, Network Standardization

**The Physical Layer:** The Theoretical Basis for Data Communication, Guided Transmission Media, Wireless Transmission, Communication Satellites, The Public Switched Telephone Network,

### Module -2
**The Data Link Layer:** Data Link Layer Design Issues, Error Detection and Correction, Elementary Data Link Protocols, Sliding Window Protocols, Protocol Verification, Data Link Protocols

### Module -3
**The Medium Access Control Sub Layer:** The Channel Allocation Problem, Multiple Access Protocols, Ethernet, Wireless LANS Broadband Wireless, Bluetooth

### Module -4

### Module -5

### Course Outcomes:
After completion of this course the student is able to:
1. Describe the basic computer network technology.
2. Identify and analyze the different network topologies and protocols.
3. Analyze the different network devices and their functions within a network.
4. Apply the knowledge in the establishing computer based networks in real world problems.

### Question Paper Pattern
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.
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Credits – 3 (Each module – 08 Hours)

**Module -1**

**Basic Real Time Concepts** - Basic computer architecture: Bus transfer Mechanism, input and output, memory, CPU operation, software concepts, system concepts, real time definition, Event and Determinism, Synchronous and asynchronous events, real time design issues, Examples of real time systems.

**Computer Hardware**: CPU, addressing modes: implied mode, immediate mode, Direct mode, Indirect mode, register indirect mode, Pipelining.

**Module -2**


**Module -3**

**Real-Time Kernels**: Introduction, Polled loop with interrupts, Coroutines, Interrupt Driven systems, context switching, Round-Robin system, preemptive priority systems, Major and minor cycles, hybrid systems, foreground /background systems, Real time operation, Full-Featured real-Time operating systems, Task control block model, task management, posix.

**Module -4**

**Inter-task Communication and Synchronization**: Buffering Data ring buffers, Mailboxes, mailbox implementation, Queues, critical regions, semaphores, mailboxes and semaphores, counting semaphores, binary semaphores deadlock, avoidance, detect and recover.

**Module -5**

**Real-time Memory management**: process stack management, task control block model, managing the stack, multiple stack arrangements, Dynamic allocation, swapping, overlay, MFT, MVT.

**System performance analysis and optimization**: Response time calculation, polled loops, co routines / phase driven code, interrupt systems, interrupt latency.

**Course Outcome**: After completion of this course the student is able to:

1. Describe theoretical and practical concepts, and functioning of operating system.
2. Distinguish a real-time system from other systems.
3. Discuss the specifications, design requirements and kernel techniques in development of RTOS.
4. Evaluate real time operating systems based on real time applications with different models.
5. Implement the real-time operating system principles.

**Question Paper Pattern**
- The question paper will have TEN questions
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

### Text Books:
2. Embedded systems architecture, Programming and design-3rd Edition, Raj Kamal. 2015

### Reference Books:
### Distributed Sensor Networks

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**Credits – 3 (Each module – 08 Hours)**

#### Module -1

#### Module -2

#### Module -3
**Energy-Aware Target Localization:** Detection, Probability Table, Score-Based Ranking Selection of Sensors to Query, 3Energy Evaluation Model, Primitive Energy Evaluation Model, Refined Energy Evaluation Model, Procedural Description, Simulation Results, Case Study.

#### Module -4
**Energy-Efficient Self-Organization:** Introduction, Relevant Prior Outline of SCARE Basic Scheme, Network Partitioning Problem, Details of SCARE.
**Energy-Aware Information Dissemination:** Introduction, Related Prior Work Location-Aided Flooding, Modified Flooding, Location Information, Virtual Grids, Packet Header Format, LAF Node Types.

#### Module -5
**Data-Centric Routing and Directed Diffusion:** Energy Equivalence Approach, Basics, Neighbor Switching Path, Rerouting EER Algorithms, and Assumptions. Procedures and Functions Formats of Packets.

#### Course Outcomes:
After studying this course, students will be able to:
1. Describe sensor network technologies from three different perspectives: sensing, communication, and computing.
2. Evaluate the concepts on sensor deployment, energy consumption, dissemination and optimization.
3. Analyze the wireless sensor networks (WSN) based on structure, energy and routing techniques.
4. Apply different strategies to choose best packet routing methodology.
5. Formulate and solve problems creatively in the area of WSN.

#### Question Paper Pattern
- The question paper will have TEN questions.
- Each full question carry 16 marks
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</table>

<table>
<thead>
<tr>
<th>Reference Books:</th>
</tr>
</thead>
</table>
Module-1
**X-Ray Imaging:** Definition of x-ray, Interactions between X-rays and matter, Intensity of X-ray beam, Attenuation, Generation and Detection of X-rays – X-ray generation, X-ray generators, Filters, Beam restrictors and grids, Intensifying screens, fluorescent screens, and image intensifiers, X-ray detectors.

**X-Ray Diagnostic Methods:** Conventional X-ray radiography, Fluoroscopy, Angiography, Mammography.

**Computed Tomography:** Conventional tomography, Computed tomography – Projection function, CT number. Recent developments – Digital radiography, Digital subtraction angiography (DSA). Biological effects of ionizing radiation.

Module-2
**Ultrasound Imaging:** Definition of ultrasound, Fundamentals of acoustic propagation (only theoretical concepts, no derivations) - Reflection and refraction, Attenuation, absorption & scattering, Doppler effect, Generation and detection of Ultrasound-Piezoelectric effect, Ultrasonic transducers, Axial and Lateral resolution.

**Ultrasonic Diagnostic Methods:** Pulse echo systems- Amplitude mode (A-mode), Brightness mode (B-mode), Motion mode (M-mode). Doppler methods, Duplex imaging, Colour Doppler flow imaging, Biological effects of ultrasound.

Module-3
**Radionuclide Imaging:** Introduction, Fundamentals of Radioactivity: Nuclear particles, Nuclear activity and half-life, Units of measuring nuclear activity, Specific activity, Interaction of nuclear particles and matter, Attenuation of Gamma radiation, Radionuclides, Generation & Detection of Nuclear Emission – Nuclear sources, Radionuclide generators, nuclear radiation detectors, Collimators,

**Diagnostic Methods using Radiation Detector Probes:** Thyroid function test, Renal function test, Blood volume measurement, Radionuclide imaging systems- Rectilinear scanner, Scintillation camera, SPECT: Principle and working. PET: Principle and working.

Module-4
**Basics of Magnetic Resonance Imaging:** Fundamentals of nuclear magnetic resonance- Angular momentum, magnetic dipole moment, magnetization, Larmor frequency, Free induction decay (FID), Fourier spectrum of the NMR signal, Relaxation times, Pulse sequences.

**Generation and Detection of NMR Signal:** Introduction (block diagram and working), Magnet, Imaging Methods- Introduction, slice selection, frequency encoding, phase encoding, Spin-Echo imaging- Gradient echo imaging. Biological effects of magnetic fields-Brief summary of all types of effects.

Module-5
**Thermal Imaging & Advances in Medical Imaging**
**Thermal Imaging:** Medical thermography, Physics of thermography, Infrared detectors, Thermographic equipment, Quantitative medical thermography, Pyroelectric vidicon camera. Applications of thermal imaging medicine.

**Image Guided Intervention:** Introduction, Stereotactic neurosurgery, Stereotactic neurosurgery based on digital image volumes- image acquisition, planning and transfer, Intraoperative Imaging- Intraoperative diagnostic imaging.

**Course Outcomes:** After studying this course, students will be able to:
1. Describe the fundamentals of x-ray radiography and computed tomography, and analyze the system requirements.
2. Explain principles of ultrasound imaging and diagnostic methods and analyze the system requirements.
3. Discuss the fundamentals of radionuclide imaging, MRI, thermal imaging and analyze the system requirements.
4. Describe the concepts of image Guided Intervention and image guided surgery.
5. Design and develop prototype of simple medical imaging system.

**Question Paper Pattern:**
- The question paper will have TEN questions
- Each full question carry 16 marks
- TWO full questions will be set (with maximum of THREE sub questions) from each module
- Covering all the topics under that module.
- The students will have to answer FIVE full questions, selecting one full question from each module.

**Text Books:**
**B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VII**

<table>
<thead>
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<th>Virtual Instrumentation</th>
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<tr>
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<tr>
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<tr>
<td>Total Number of Lecture Hours : 40</td>
</tr>
<tr>
<td>Credits – 3 (Each module – 08 Hours)</td>
</tr>
</tbody>
</table>

**Module -1**
**Graphical System Design**: Introduction, Graphical system design model, Design flow with GSD, Virtual Instrumentation, Virtual instrument and traditional instrument, Hardware in virtual instrumentation, Virtual instrumentation for Test, control & design, virtual instrumentation in Engineering process, virtual instruments beyond personal computer, Graphical system design using LABVIEW, Graphical programming & textual programming.

**Module -2**
**Introduction To LabView**: Introduction, advantages of LABVIEW software environment, palettes, front panel controls & indicators, Block diagram, Data flow program, arranging objects, colour coding, creating sub-VI’s.

**Repetition And Loops**: For loops, while loops, structure tunnels, terminals inside or outside loops, shift registers, feed-back nodes, control timing, communicating among multiple-loops, local variables, Global variables, case structure, formula node.

**Module -3**
**Arrays**: Introduction, arrays in LABVIEW, creating one-dimensional array controls, indicators and constants, creating two dimensional arrays, creating multidimensional arrays, initializing array, deleting, inserting, and replacing elements, rows, columns, and pages with in arrays, arrays functions, auto indexing, creating 2-dimensional array using loops.

**Module -4**
**Plotting Data**: Types of waveforms, waveform graphs, waveform charts, XY graphs, Intensity graphs & charts, Digital waveform graphs, 3D graphs, customizing graphs & charts, configuring a graph or chart, Displaying special planners on the XY graph.

**Module -5**
**File Input/ Output**: File formats, fill I/O functions, path function sample VI’s to demonstrate file write &read, generating filenames automatically, String handling: string functions, LABVIEW string formats, examples, parsing of strings.

**Instrument Control**: Introduction, GPIB communication, Hardware specification, software architecture, Instrument I/O assistant, VISA, Instrument drivers, serial port communications, using other interfaces.

**Course Outcomes**: After studying this course, students will be able to:
1. Recognize the Graphical system design model and develop programs using the modern tools of Graphical programming & textual programming
2. Develop a virtual instrumentation model using the front panel controls & indicators, block diagram, data flow programs.
3. Analyze, design, create, and delete different array controls, indicators constants with various
functions
4. Evaluate and Design Digital waveform graphs, 3 D graphs, customizing graphs & charts
5. Demonstrate file write & read, generating filenames automatically, String handling: string functions, LABVIEW string formats, examples, parsing of strings
6. Demonstrate Instrument Control, GPIB communication, Hardware specification, software architecture, Instrument I/O assistant, VISA, Instrument drivers, serial port communications, using other interfaces.

**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester - VII

Big Data and Cloud Computing  
(Common to EI, BM & ML)

<table>
<thead>
<tr>
<th>Subject Code</th>
<th>IA Marks</th>
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<th>Exam Marks</th>
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</table>

Credits – 3 (Each module – 08 Hours)

**Module -1**  
**Introduction To Big Data:**  


**Module -2**  
**Technologies and Tools For Big Data Analytics:**  
NoSQL (Not Only SQL), Hadoop.

**Module -3**  
**Introduction:** Cloud Computing at a Glance, Historical Developments, Building Cloud Computing Environments, Computing Platforms and Technologies,


**Module -4**  
**Cloud Computing Architecture:** Introduction, Cloud Reference Model, Architecture, Types of Clouds, Economics of the Cloud, Open Challenges,

**Aneka: Cloud Application Platform:** Framework Overview, Anatomy of the Aneka Container, Building Aneka Clouds, Cloud Programming and Management.

**Module -5**  
**Cloud Platforms In Industry:** Amazon Web Services, Google App Engine, Microsoft Azure.

**Cloud Applications:** ECG Analysis in the Cloud, Satellite Image Processing, Social Networking, Media Applications, Multiplayer Online Gaming.

**Course Outcomes:** After studying this course, students will be able to:

1. Describe the concepts and technologies of big data analytics.
2. Apply the techniques in handing and analysis of big data.
3. Discuss the concepts and terminologies of cloud computing.
4. Demonstrate cloud frameworks and technologies
5. Describe and apply fine data intensive computing.
6. Demonstrate cloud applications.

### Question Paper Pattern
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

### Text Books:

### Reference Books:
B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester - VII  

**Process Control and Virtual Instrumentation Lab**

<table>
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</thead>
<tbody>
<tr>
<td>: 42</td>
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</tr>
</tbody>
</table>

**Credits – 2**

1. Rig up and test the circuit to display the temperature using RTD/Thermistor with suitable signal conditioning circuit.
2. Rig up and test the circuit to display the temperature using IC AD590 / LM35 with suitable signal conditioning circuit.
3. Rig up and test the circuit to display the load/ strain using load cell/ strain gauge with suitable signal conditioning circuits.
4. Realize Op-amp based Proportional (P), Derivative (D) and Integral (I) analog controller modes.
5. Realize Op-amp based PI and PD composite analog controller modes.
6. Conduct an experiment to perform and analyze PC based temperature/pressure controller. Plot the optimum response of different controller modes for different set-points.
7. Conduct an experiment to perform and analyze PC based level/flow controller. Plot the optimum response of different controller modes for different set-points.
8. Realization of basic gate functions using PLC. The logic should be solved using ladder diagram.
   (i) AND (ii) OR (iii) NAND (iv) XOR (v) NOR (vi) Latch and Unlatch of output
9. Study and demonstration of working of different types of Timers and Counters of PLC. The logic should be solved using ladder diagram.
10. Study and demonstration of Bottle Filling Process using PLC. The logic should be solved using ladder diagram.
11. Study and demonstration of Lift/Elevator System using PLC. The logic should be solved using ladder diagram.
12. Conduct an experiment to plot the characteristics of different types process control valves.
13. Basic operations, simple programming structure using LabVIEW.
   (i) Basic arithmetic operations  (ii) Boolean operations (iii) Sum of ‘n’ numbers using ‘for’ loop (iv) Sorting even numbers using ‘while’ loop in an array
14. Creation of a CRO using LabVIEW and measurement of frequency and amplitude.
15. Data acquisition using LabVIEW for temperature measurement with thermocouple and AD590.

**Course Outcomes:** After studying this course, students will able to:

1. Design and evaluate signal conditioning circuits for given transducer/sensor.
2. Design and evaluate the controllers to obtain the optimal response.
3. Write ladder logic programs for specific applications using PLC.
4. Develop LabVIEW programs for specific applications.
5. Design and develop complete process control system for specific application.

**Conduct of Practical Examination:**

- All laboratory experiments are to be included for practical examination.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script for breakup of
marks.

- Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VII

ARM Processor Lab
(Common to EI, BM & ML)

Subject Code : 15EI/BM/ML77
IA Marks : 20

Teaching Hours/Week : 03 (1I+2P)
IA Marks : 20

Exam Marks : 80
Exam Hours : 03

Credits – 2

PART-A: Conduct the following experiments by writing Assembly Language Program (ALP) using
ARM Cortex M3 Registers using an evaluation board/simulator and the required software tool.
1. Write an ALP to multiply two 16 bit binary numbers.
2. Write an ALP to find the sum of first 10 integer numbers.
3. Write an ALP to find factorial of a number.
4. Write an ALP to add an array of 16 bit numbers and store the 32 bit result in internal RAM
5. Write an ALP to add two 64 bit numbers.
6. Write an ALP to find the square of a number(1 to 10) using look-up table.
7. Write an ALP to find the largest/smallest number in an array of 32 numbers.
8. Write an ALP to arrange a series of 32 bit numbers in ascending/descending order.
9. Write an ALP to count the number of ones and zeros in two consecutive memory locations.
10. Write an ALP to Scan a series of 32 bit numbers to find how many are negative.

PART-B: Conduct the following experiments on an ARM CORTEX M3 evaluation board using
evaluation version of Embedded 'C' & Keil Uvision-4 tool/compiler.
1. Display “Hello World” message using Internal UART.
2. Interface and Control a DC Motor.
3. Interface a Stepper motor and rotate it in clockwise and anti-clockwise direction.
4. Determine Digital output for a given Analog input using Internal ADC of ARM controller.
5. Interface a DAC and generate Triangular and Square waveforms.
6. Interface a 4x4 keyboard and display the key code on an LCD.
7. Using the Internal PWM module of ARM controller generate PWM and vary its duty cycle.
8. Demonstrate the use of an external interrupt to toggle an LED On/Off.
9. Display the Hex digits 0 to F on a 7-segment LED interface, with an appropriate delay in
   between.
10. Interface a simple Switch and display its status through Relay, Buzzer and LED.

Note: More weightage should be given for PART-B experiments in the evaluation of Internal
Assessment and Laboratory Examinations.

Conduction of Practical Examination:
1. All laboratory experiments (Part-A + Part-B) are to be included for practical examination.
2. Students are allowed to pick & execute one experiment from each part.
3. Strictly follow the instructions as printed on the cover page of answer script for breakup of
   marks.
4. Change of experiment is allowed only once and 15% of Marks allotted to the procedure part to
   be made zero.
<table>
<thead>
<tr>
<th>Course Outcomes:</th>
<th>After studying this course, students will able to:</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Write ALP for implementation of specific arithmetic or logical operations.</td>
<td></td>
</tr>
<tr>
<td>2. Write programs to demonstrate functioning of various devices interfaced to ARM processor.</td>
<td></td>
</tr>
<tr>
<td>3. Develop programs for ARM processors to implement real world problems.</td>
<td></td>
</tr>
<tr>
<td>4. Design and develop mini projects.</td>
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</tbody>
</table>
### B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VII

#### Project Work Phase- I + Project Work Seminar

<table>
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<tr>
<th>Subject Code</th>
<th>Teaching Hours/Week</th>
<th>IA Marks</th>
<th>Exam Marks</th>
<th>Exam Hours</th>
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<tr>
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<td>42</td>
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</tbody>
</table>

**Credits – 2**

During Project Work Phase – I, students are expected to do the followings:

1. Identify the project domain and topic.
2. Carryout necessary literature survey.
3. Define the problem and objectives of the proposed project work.
4. Finalize the methodology to carry out the project work in Phase- II and submit the Synopsis.
5. Present a Seminar on topic selected for the project.

**Evaluation Procedure:**

- As per the University guidelines.
- The Internal Assessment marks shall be awarded based on the relevance of the proposed Project Domain & Topic, Quality of the Synopsis and timely submission, Seminar presentation on the proposed project work, participation in the question and answer session, and attendance in the seminar presentations.

**Course Outcomes:** After studying this course, students will able to:

1. Collect the literature and materials in the proposed project work.
2. Analyze the current state of art work in the proposed project work.
3. Prepare synopsis with objectives and methodology.
4. Justify the proposed project and its probable outcome in the seminar presentation.
5. Communicate the concepts by effective presentation.
6. Participate effectively as an individual and member of project team.
8th SEMESTER (CBCS)

B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VIII

Neural Network and Fuzzy Logic Systems

<table>
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<th>Subject Code</th>
<th>IA Marks</th>
<th>Number of Lecture Hours /Week</th>
<th>Exam Marks</th>
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</table>

Credits – 4 (Each module – 10 Hours)

Module -1
Artificial Neural Network: An Introduction. - Fundamental Concept, Evolution of Neural Networks, Basic models of Artificial Neural Networks (ANN), Important Technologies of ANNs, McCulloch-Pitts Neuron, Linear Separability.

Module -2
Hebb Network and simple problems,

Module -3

Module -4
Introduction to Fuzzy Logic, Classical sets and Fuzzy sets.

Module -5
Membership Functions – Introduction, Features of the Membership functions, Fuzzication, Methods of Membership Value Assignments, Simple Problems

Course Outcomes: After studying this course, students will be able to:
1. Compare and contrast the biological neural network and ANN.
2. Discuss the ANN for pattern classification.
3. Develop and configure ANNs with different types of functions and learning algorithms.
4. Apply ANN for real world problems.
5. Discuss the fundamentals of fuzzy logic, implementation and their functions
6. Apply fuzzy logic concepts in building automated control systems.

**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
<table>
<thead>
<tr>
<th><strong>B.E. Electronics and Instrumentation Engineering (EI)</strong></th>
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<td><strong>Choice Based Credit System (CBCS)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Semester - VII</strong></td>
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</table>

### LASERs and Optical Instrumentation

<table>
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<th>Subject Code</th>
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</table>

**Credits – 4 (Each module – 10 Hours)**

**Module -1**
**Lasers -I:** Introduction, Emission and absorption of radiation, Einstein relation, population inversion, threshold conditions, Line shape function, population inversion and pumping threshold conditions.
**Lasers -II:** LASER modes: Axial & Transverse modes; Classes of LASER: Doped insulator LASERs, semiconductor LASERs, Gas LASERs, Liquid dye LASERs.

**Module -2**
**Generation of Lasers:** Single mode operation, frequency stabilization. Q-switching, mode locking, lasing threshold.
**Applications of Laser:** Measurement of distance: Interferometric methods, Beam modulation telemetry, Pulse echo techniques; Holography & its Applications.

**Module -3**
**Overview of Optical Fiber Communications:** Motivations for light wave communications, optical spectral bands, Decibel units, Network information rates, WDM concepts, Key elements of optical fiber systems, standards for optical fiber communications.
**Structures, Wave guiding, and Fabrication I:** The nature of light, basic optical laws and definitions, optical fiber modes and configurations, Mode theory for circular waveguides, Single mode fibers.

**Module -4**
**Structures, Wave guiding, and Fabrication II:** Graded index fiber structure, Fiber materials, Photonic crystal fibers, Fiber fabrication, Mechanical properties of fibers, Fiber optic cables.
**Optical Amplifiers:** Types of optical amplifiers and its applications, Semiconductor optical amplifiers, Erbium-doped fiber amplifiers, Amplifier noise, Optical SNR, System Applications, Raman amplifiers, wideband optical amplifiers.

**Module -5**

**Textbook 3:** Unit 9.1, 9.2, 9.2.1, 9.2.2, 9.2.5, 9.3.4, 9.5.2.3, 9.7.3, 9.8.2, 9.9.2, 9.11.4.3

**Course Outcomes:** After studying this course, students will be able to:
1. Explain the principle and working of Laser system.
2. Discuss the engineering applications of laser systems.
3. Discuss the fundamentals of optical fiber communications.
4. Evaluate the design of optical fibers.
5. **Apply fiber optic laser systems in medical field.**

**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
B.E. Electronics and Instrumentation Engineering (EI)  
Choice Based Credit System (CBCS)  
Semester – VIII

Instrumentation Buses and Industrial Data Networks

<table>
<thead>
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</table>

Credits – 3 (Each module – 08 Hours)

Module -1  

Module-2  
**Inter Networking:** Bridges – Routers – Gateways –Standard ETHERNET and ARCNET configuration- special requirement for networks used for control.

Module -3  

Module -4  
**Modbus and Profibus PA/DP/FMS:** MODBUS protocol structure, function codes troubleshooting Profibus; Introduction – Profibus protocol stack –Profibus communication model communication objects – system operation – troubleshooting – review of foundation field bus.

Module -5  

Course Outcomes: After studying this course, students will be able to:

1. Explain basic concepts of network hierarchy and switching.
2. Apply network data communication protocols.
3. Describe routers and gateways Standards followed in inter-networking.
4. Evaluate appropriateness of different industrial data networks.
5. Develop the various communication networks for industries.

Question Paper Pattern

- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each
module.

<table>
<thead>
<tr>
<th><strong>Text Books:</strong></th>
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<tbody>
<tr>
<td>2. Buchanan, W., “Computer Buses”, CRC Press, 2000,</td>
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<th><strong>Reference Books:</strong></th>
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B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VIII

Industrial Process Instrumentation

<table>
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<th>IA Marks</th>
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</table>

Credits – 3 (Each module – 08 Hours)

Module - 1
Food Industry Instrumentation: Instrumentation in canning, baking, dairy industries.

Module-2
Paper and Pulp Instrumentation: Different types of pulping, pulp bleaching, pulp blending, wet end and drier instrumentation.

Module - 5
Cement Plants: Objectives of automation system, automation strategy, Distributed Control System for Cement Plant- A case study.

Module - 3
Thermal power plant Instrumentation: Automation strategy, distributed system structure, Man-machine interface, software system, communication, advanced control systems.

Module - 4
Steel Plant: Automation strategy, production planning and area supervision, iron zone, steel zone, mill zone, utility zone.

Course Outcomes: After studying this course, students will be able to:
1. Sketch the typical instrumentation and control strategy in the process industries like paper, pulp, cement, power and steel plants.
2. List the variable to be monitored and controlled in the above said process industries.
3. Describe the instrumentation, control requirements and complete functioning of the above said industries.
4. Analyze the hardware and software tools required for the above said process industries.
5. Design and develop instrumentation and control strategy for simple process industry.

Question Paper Pattern
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

Text Books:

**Reference Books:**

1. Donald P Eckman, “Industrial Instrumentation”, J.Wiley
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester – VIII

**Biosensors and Bioinstrumentation**

<table>
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<td>Credits – 3 (Each module – 08 Hours)</td>
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**Module - 1**
**Introduction:** A historical perspective, Bio-analytical sensors & systems, Transduction modes & classifications, Approaches to Immobilization, Basic designs of discrete sensors, calibration & figures of merit

**Chemical Measurement:** Objectives of chemical Measurement, Requirements & limitations in chemical measurement. Chemical transducer – Electro-chemical transducer, electrical potential & reference electrodes, potentiometric sensors, Amperometric sensors, electrochemical gas sensors

**Module - 2**
**Biosensors** – Enzyme based biosensors, immunosensors, Microbial sensors, continuous measurement of chemical quantities – intravascular measurements, tissue measurements, Measurement - by blood drainage, Measurements by Microdialysis, Measurements by effluent fluid analysis. Transcutaneous Measurements of pO₂, pCO₂. Transcutaneous arterial oxygen saturation monitoring – basics of oximetry, pulse oximeter. PCR: Principle, procedure and instrumentation.

**Module - 3**
**Optically based Energy Transduction:** Fiber optic devices, Guided waves, Evanescent wave, Fiber optic sensor designs, planar wave guides, near field optical sensing. Surface Plasmon resonance, Thermal sensor.

**Surface Characterization in Biomaterials and Tissue Engineering**- Molecules and Biomaterials, Molecules & Tissue Engineering, surface analysis- Transmission electron Microscope, Scanning electron Microscope, scanning Tunneling Microscope, Scanning Force Microscope.

**Module - 4**
**Blood Cell Counters:** Blood components & processing, calculation of size of cells, methods of cell counting – microscopic method, automatic optical method, electrical conductivity method. Coulter counters – multiparameter coulter counter, picoscale. automatic recognitions and differential counting of cells

**Cellular Measurements in Biomaterials and Tissue Engineering:** Cell Measurement overview, Light Microscopy, Cell orientation, Cell-rolling velocity, Cell Deformation, Cell proliferation, Cell Differentiation, Cell Signaling & Regulation

**Module -5**
**Body Temperature, Fat and Movement Measurement:** Regulation of Body Temperature, Infrared Thermometer, Measurement of Body fat, Direct & Indirect Measurement of body fat, Measurements of body Movement.

**Force Measurement:** Muscle contraction measurements, Measurement of stresses in the bone, Ground force measurement.
**Course Outcomes:** After studying this course, students will be able to:

1. Explain the principle and working of chemical transducers and biosensors.
2. Apply biosensors in the measurement of many physiological and biomechanics parameters.
3. Demonstrate the functioning of simple biosensors.
4. Evaluate the characteristics of biomaterials and tissues using bioinstrumentation techniques.
5. Develop and demonstrate the simple biosensors for physiological measurements.

**Question Paper Pattern**

- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**


**Reference Books:**

### B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VIII

#### Programmable System on Chip (PSoC)
(Common to EI & ML)

<table>
<thead>
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<th>Subject Code</th>
<th>IA Marks</th>
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Credits – 3 (Each module – 08 Hours)

**Module -1**
**Introduction to PSoC:** PSoC Technology, Programmable Routing and Interconnect, Configurable Analog and Digital Blocks, CPU Sub system , Families of PSoC (PSoC 1, PSoC 3, PSoC 5), Difference between PSoC and conventional MCU.

**Module -2**
**Introduction to PSoC 3/5:** PSoC 3/5 Architecture – Block Diagram, System Wide Resources, I/O Interfaces, CPU Subsystem, Memory Organization, Digital Subsystems, Analog Subsystems.

**Module -3**
**PSoC Design Modules:** Why Cypress PSoC, Structure of PSoC, PSoC Designer Suit, Limitations of PSoC, PSoC Subsystem Design, PSoC Memory Management.

**Module -4**
**Mixed – Signal Embedded Design:** Overview of Mixed Signal Embedded Systems Design, Hardware and Software subsystems of mixed signal architectures, PSoC Hardware Components, PSoC Software Components, PSoC Interrupt Subsystem, Introduction to PSoC Express, System Design using PSoC Express.

**Module -5**
**PSoC Components:** Universal Digital Blocks (UDB), UDB arrays and Digital System Interconnect (DSI), Timer, Counter and PWM, Digital Filter Blocks (DFB), Delta Sigma ADC Topologies and Circuits, Programmable Gain Amplifiers, Switched Capacitor / Continuous Time, Analog Routing, Flash Temperature Sensors, DTMF Dialers, Sleep Timers, UART, I2C, SPI, USB, CAN Buses.

**System Design Using PSoC**
Interfacing of Temperature Sensor and Tachometer, SPI and UART based Communication, Low Noise Continuous Time Signal Processing with PSoC, Data Acquisition and control system with PSoC, Ultra-wide band RADAR, and Serial Bit Receiver with Hardware Manchester Decoder, DTMF Detector, Ultrasonic Vehicle Parking Assistant, and Universal Wide Range Signal Generator.

**Course Outcomes:** After studying this course, students will be able to:
1. Explain the basic concepts and families of PSoC.
2. Recognize the various features of PSoC architecture and programmable blocks.
3. Analyze the various features of PSoC Designer for configuring the digital and analog modules
4. Design the hardware and software components for mixed signal components.
5. Develop the programming skills for interfacing various I/O modules to PSoC.
**Question Paper Pattern**
- The question paper will have TEN questions.
- Each full question carry 16 marks
- There will be TWO full questions (with maximum of THREE sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

**Reference Books:**
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VIII

Internship / Professional Practice

<table>
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Credits – 2

Course objectives:
Internship/Professional practice provide students the opportunity of hands-on experience that include personal training, time and stress management, interactive skills, presentations, budgeting, marketing, liability and risk management, paperwork, equipment ordering, maintenance, responding to emergencies etc. The objective are further,
- To put theory into practice
- To relate to, interact with, and learn from current professionals in the field.
- To gain a greater understanding of the duties and responsibilities of a professional
- To understand and adhere to professional standards in the field.
- To gain insight to professional communication including meetings, memos, reading, writing, public speaking, research, client interaction, input of ideas, and confidentiality.
- To identify personal strengths and weaknesses.
- To develop the initiative and motivation to be a self-starter and work independently.

Internship/Professional practice: Students under the guidance of internal guide/s and external guide shall take part in all the activities regularly to acquire as much knowledge as possible without causing any inconvenience at the place of internship.

Seminar: Each student, is required to
- Present the seminar on the internship orally and/or through power point slides.
- Answer the queries and involve in debate/discussion.
- Submit the report duly certified by the external guide.

Course outcomes: At the end of the course the student will be able to:
- Acquire practical experience within industry in which the internship is done.
- Apply knowledge and skills learned to classroom work.
- Experience the activities and functions of professionals.
- Develop and refine oral and written communication skills.
- Recognize the areas for future knowledge and skill development.
- Acquire the basic knowledge of administration, marketing, finance and economics.
- Develop the skills to enable lifelong learning.

Evaluation Procedure:
- As per University guidelines.
- Evaluation of IA Marks: The Internal Assessment marks shall be awarded based on the Internship/Professional Practice Report and Seminar Presentation.
- Semester End Examination: The marks shall be awarded based on the Internship/Professional Practice Report and Seminar Presentation as per the University norms by the examiners appointed VTU.
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VIII

Project Work Phase-II

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<th>Number of Lecture Hours /Week</th>
<th>Exam Marks</th>
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Credits – 6

Course objectives:
- To support independent learning.
- To develop interactive, communication, organization, time management, and presentation skills.
- To impart flexibility and adaptability.
- To inspire independent and team working.
- To expand intellectual capacity, credibility, judgment, intuition.
- To adhere to punctuality, setting and meeting deadlines.
- To instil responsibilities to oneself and others.
- To train students to present the topic of project work in a seminar without any fear, face audience confidently, enhance communication skill, involve in group discussion to present and exchange ideas.

Project Work Phase - II: Each student of the project batch shall involve in carrying out the project work jointly in constant consultation with internal guide, co-guide, and external guide and prepare the project report as per the norms avoiding plagiarism.

Course outcomes: At the end of the course the student will be able to:
- Describe the project and be able to defend it.
- Develop critical thinking and problem solving skills.
- Learn to use modern tools and techniques.
- Communicate effectively and to present ideas clearly and coherently both in written and oral forms.
- Develop skills to work in a team to achieve common goal.
- Develop skills of project management and finance.
- Develop skills of self learning, evaluate their learning and take appropriate actions to improve it.
- Prepare themselves for life-long learning to face the challenges and support the technological changes to meet the societal needs.

Evaluation Procedure:
- As per University guidelines
- **Internal Marks:** The Internal marks (100 marks) evaluation shall be based on Phase wise completion of the project work, Project report, Presentation and Demonstration of the actual/model/prototype of the project.
- **Semester End Examination:** SEE marks for the project (100 marks) shall be based on Project report, Presentation and Demonstration of the actual/model/prototype of the project, as per the University norms by the examiners appointed VTU.
B.E. Electronics and Instrumentation Engineering (EI)
Choice Based Credit System (CBCS)
Semester - VIII

Seminar

<table>
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</table>

Number of Lecture Hours /Week : --  
Total Number of Lecture Hours : --  

Credits – 1

Course objectives:
The objective of the seminar is to inculcate self-learning, face audience confidently, enhance communication skill, involve in group discussion and present and exchange ideas. Each student, under the guidance of a Faculty, is required to choose, preferably, a recent topic of his/her interest relevant to the course of specialization. Carryout literature survey, organize the Course topics in a systematic order.

- Conduct literature survey in the domain area to find appropriate topic.
- Prepare the synopsis report with own sentences in a standard format.
- Learn to use MS word, MS power point, MS equation and Drawing tools or any such facilities in the preparation of report and presentation.
- Present the seminar topic orally and/or through power point slides.
- Communicate effectively to answer the queries and involve in debate/discussion.

The participants shall take part in discussion to foster friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident.

Course outcomes:
At the end of the course the student will be able to:

- Develop knowledge in the field of Electronics & Instrumentation Engineering and other disciplines through independent learning and collaborative study.
- Identify and discuss the current, real-time issues and challenges in engineering & technology.
- Develop written and oral communication skills.
- Explore concepts in larger diverse social and academic contexts.
- Apply principles of ethics and respect in interaction with others.
- Develop the skills to enable life-long learning.

Evaluation Procedure:

- As per University guidelines.
- The Internal Assessment marks for the seminar shall be awarded based on the relevance of the seminar topic, quality of the report, presentation skills, participation in the question and answer, and attendance in the seminar classes/sessions.