

VTU Sponsored

Five Day

Faculty Development Programme (FDP)
"Design of Embedded Systems with ARM
Cortex M3"

January 16- 20, 2018

Registration Form

1. Name (Block Letters):

2. Gender: M / F

3. Designation:

4. Organization:

5. Address for Communication:

6. Email:

7. Mobile Number:

8. DD No.

Date:

9. Qualification:

10. Teaching Experience (Years):

11. Research Experience (Years):

Date:

Place:

Signature of the Applicant:

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Dr. A.G.Nataraj, Principal, BIT

Convener

Dr. Shanthala S, HOD, TE, BIT

Workshop Coordinator

Prof. N. Chetan Anand, TE, BIT

Email: workshop.tce.bit@gmail.com

Phone: 09844009474



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FACULTY DEVELOPMENT PROGRAM

On
"DESIGN OF EMBEDDED SYSTEMS WITH
ARM CORTEX M3"

16th - 20th January 2018

Organized by
Department of
Telecommunication Engineering



Bangalore Institute of Technology

K.R.Road, V.V.Pura
Bengaluru-560004

www.bangalore-bit.edu.in

In Association with



About Bangalore Institute of Technology (BIT)

BIT was started in the year 1979 with the intention of providing quality education in the field of technology and there by serving our society. At present BIT has 9 Undergraduate, 10 Post graduate and Ph.D. Courses. The Institute offers Bachelor of Engineering (B.E.) Degree in Civil, Mechanical, Electrical & Electronics, Electronics & Communication, Computer Science, Electronics & Instrumentation Technology, Telecommunication Engineering, Information Science & Engineering and Industrial Engineering & Management. There are a number of centers carrying out inter-disciplinary research and many collaborative programs exist between the college and other Institutions, like IISc, NAL., etc. Teaching & Research Work is also amply supported by facilities such as Central Library with 90,000 volumes of text books and reference books, and an Industrial Consultancy and Research Center.

About the Department

The Department of Telecommunication Engineering was started in the year 1992-93. The Department has a team of well qualified and dedicated staff members.

The Department offers Under-Graduate [UG] Course and has a Research Center under V.T.U. It has excellent infrastructure facilities and all the Laboratories are well equipped. The objective of the Department is to "Empower Every Student to be Creative and Productive in the Field of Telecommunication Engineering by Imparting Excellent Technical Education and Inculcating Human Values".

The Department has organized many National Level workshops in collaboration with AICTE, DST, KSTA and VTU. The Department has received project funds for R & D from VGST & KSCST.

About our Associate

M/s. Advanced Electronics Systems, (ALS), Bangalore, India, is one of the leading organizations involved in the development and manufacture of quality products based on Microprocessor, Microcontrollers, ARM Controllers, FPGA Boards, Communication boards & PC based application since 1983 for both the educational sector and the Industry sector. They also conduct seminars, workshops and internship programs for student and faculty. ALS has executed many projects for Space, Defense and Atomic centers. They are active members- BOS in many of the Autonomous Engineering colleges.

About the FDP

The workshop is aimed at providing a broad overview of ARM Cortex M3 Microcontrollers as per the new VTU CBCS syllabus of Electronics & Communication / Telecommunication Engineering Courses. The Workshop will highlight the emergence of ARM Cortex M3 Microcontroller and its Architecture. The focus will be on the topics related to Embedded Controller Laboratory Experiments in providing Hands on experiences.

Topics Covered

- Basics on ARM-32 bit Microcontroller.
- ARM Cortex M3 Instruction Sets and Programming.
- Embedded System Concepts and Components.
- RTOS and IDE for Embedded System Design.
- Hands on Execution of Software & Interfacing Programs. (As per VTU CBCS Syllabus).

Registration Fees

1. Faculty/Research Scholars Rs.750/-
2. Industry Delegates Rs.1000/-

DD to be drawn in favour of Principal, BIT payable at Bangalore.

Confirm your participation by mailing the Registration Form duly signed along with scanned copy of D.D. to workshop_tce_bit@gmail.com

Maximum Forty participants will be enrolled.

Selection will be made purely on First-Come-First- Serve basis (Subject to fulfilling the eligibility Criteria).

For Registration Contact

Prof. B. Sudha: 099801 03459
Prof. A.R. Manjula: 094494 56108

Important Dates

Submission of application forms : **3rd January 2018.**
Confirmation of Selection (by Email): **5th January 2018**