

		<b>Session 3 (2. 00 to 4. 30 PM)</b>
	Demonstration - Examples on using linear and nonlinear simulators,	
	<b>Hands on Session on LNA Design</b>	
	<b>Hands on Session on PA Design</b>	
	<b>Hands on Session on Oscillator Design</b>	
	<b>Hands on Session on Mixer Design</b>	
<b>Lunch Break</b>		
	<b>Session (11. 30 to 1. 00PM)</b>	
	RF/MW active Circuits implementations technologies, HMIC, MMIC/RFIC, LTCC, RF MEMS, Lay out Designs	
	Low Noise Amplifier Design	
	Power Amplifier Design	
	VCO Design	
	Mixer Design	
	Characterization of Oscillators and Mixer	
<b>Tea Break</b>		
	<b>Session 1(9. 30 to 11. 00AM)</b>	
	Introduction to Circuit Simulations, Linear & Nonlinear Simulation Harmonic balance simulations, SPICE simulation EM Simulation	
	Low Noise Amplifier Design	
	Power Amplifier Design	
	Oscillator Design	
	Mixer Design	
	Characterization of RF Subsystems : LNA, PA	
<b>Day</b>		
1		
2		
3		
4		
5		
6		

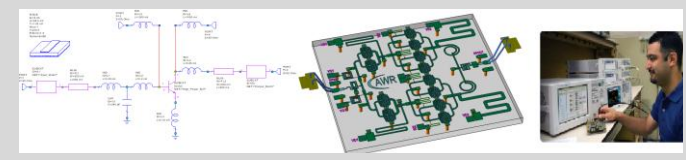
### Target Audience

UG/PG Students, Faculties and Practicing professionals with knowledge on RF Circuits, Wireless Communication

### Outcome of the Program

#### Participants will acquire

- Skill sets on usage on NI/AWR MW EDA Tool
- Skill sets on design, simulation & optimization of RF/Microwave Active Circuits.
- Knowledge on fabrication and implementation techniques.
- Knowledge on testing of RF Active Circuits.



### Advisory Committee

Dr. K. N. Subramanya, Principal, RVCE  
Dr. G. Sadashivappa, Professor and HOD, TCE

### Content Delivery

Prof. R.K Manjunath, Dean PG (Circuits) manjunathrk@rvce.edu.in ; 9901568568  
Prof. Nagendra N. N., Assistant Professor, nagendrann@rvce.edu.in ; 9901378482  
Mr. Manjunath Reddy H. V., Senior Member IEEE manjunatha\_hv@ieee.org ; 9880215780  
Mr. L. Shanthakumar, Former Advisor, BEL  
Mr. Ramaprabhu, Head, R&D, MicronovaImpex,  
Mr. Apparao paidi, Director, ALTIOSTAR  
Mr. Palla Kumara Swamy, Cambium Networks

### Coordination Committee

Dr. H. V. Kumaraswamy, Professor kumaraswamyhv@rvce.edu.in ; 9845316282  
Dr. K. Sreelakshmi, Professor, sreelakshmik@rvce.edu.in ; 9845530311  
Prof. P Nagaraju, Associate Professor, nagarajup@rvce.edu.in ; 9916911720

Rashtrreya Sikshana Samithi Trust  
**RV COLLEGE OF ENGINEERING**  
(Autonomous Institution Affiliated to VTU, Belagavi)  
Bengaluru - 560 059, Karnataka



**Hands on Training Course on  
“Active RF/Microwave Circuit  
Design”**  
26<sup>th</sup> Feb - 3<sup>rd</sup> Mar 2018

**Department of Telecommunication  
Engineering  
R. V. College of Engineering  
Bengaluru -560059**

# Hands on Training Course on Active RF/Microwave Circuit Design

26<sup>th</sup> Feb - 3<sup>rd</sup> Mar 2018

## Registration Form

Mr/Mrs \_\_\_\_\_

Designation\_\_\_\_\_

Department\_\_\_\_\_

Qualification\_\_\_\_\_

Address for Communication\_\_\_\_\_

\_\_\_\_\_

\_\_\_\_\_

Phone\_\_\_\_\_

E-mail\_\_\_\_\_

## Registration Fee Details

### Course Fee

Students

Rs. 3000

Academia / Industries

Rs. 5000

Cash/ DD/Online\_\_\_\_\_

Amount\_\_\_\_\_

Name of the Branch\_\_\_\_\_

\_\_\_\_\_

Place\_\_\_\_\_

Date\_\_\_\_\_

Applicants Signature

Signature of the Head of the  
Institution/Sponsoring Authority  
(for Sponsored Candidates)

DD May please be drawn in the name of Principal RV College of Engineering

## About RVCE

R.V. College of Engineering (RVCE), Bengaluru, Karnataka, South India, is one of the premier Engineering institutes of the country, established in 1963. RVCE has celebrated its Golden Jubilee in the year 2015. The college currently has about 5600 students and over 400 faculty and 250 supporting staff. RVCE currently offers 12 Bachelors and 21 Masters Programs with an annual intake of about 1750 students. There are 16 centers for research with provision for Ph.D studies. RVCE has set a vision “Leadership in Quality Technical Education, Research & Innovation, through Teamwork, with a focus on Development of Sustainable and Inclusive Technology”. Keeping with the vision the faculty and students are encouraged to take up interdisciplinary research projects. As of now, RVCE has funded projects to the tune of over Rs.350 million. RVCE was rated 49th in the NIRF(National Institutional ranking frame work) rating conducted by MHRD, ranked 7<sup>th</sup> among the top 113 private colleges by The Week magazine, ranked 9<sup>th</sup> among the top 100 colleges in the country by education World Magazine. RVCE has been awarded “Engineering College of the year-2015” by the higher education review magazine for enhancing employability during December 2015. RVCE has been certified as Global League Institute by Great Place to Study Research Institute (GPSRI) on the 19th November 2015 at prestigious venue - House of Commons, UK by Rt. Hon. Virendra Sharma, MP. RVCE believes in skill and knowledge development among the faculty and the student’s community. This is achieved through continuous FDP and Research initiatives. Recent Initiatives of RVCE includes creation of ecosystem for Innovation activation and Entrepreneurial development in the region.

## About the Course

This unique, hands-on training course provides impetus skill sets on the practical approach for the design, simulation and optimization techniques of RF/Microwave active circuits through an active devices design theory, techniques and measurements. The course includes the relevant theoretical aspects along with emphasis on the practical aspects of RF/Microwave engineering. The course is structured on live demonstrations involving computer-based simulation tools to illustrate concepts. Participants will be trained on the usage of NI/AWR Microwave Office design suite and RF measurement techniques using state-of-the-art Keysight instruments. The course faculties are domain experts with many years of experience from reputed industries and academia. The aim of the course is to enable participants to acquire the skill sets on the practical design and simulation of RF / Microwave active circuits.

## Objectives of the Training Program

- To impart sound knowledge on the performance parameters ,characterization RF/ Microwave Active Circuits
- To impart practical skill sets on the usage of NI/Microwave office EDA tool
- To impart hands on practical design & optimize skill sets on Low Noise Amplifiers (LNA),Power Amplifiers (PA)
- To impart hands on practical training design simulate and optimize Oscillators & VCOs
- To impart hands on practical training on design, simulate and optimize Mixer
- To impart training on the testing of RF /MW subsystems like LNA,PA, oscillators, Mixers etc. using state of the art Key sight Test instrumentation.