



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ  
ವಿಟಿಯು ಅಧಿನಿಯಮ ೧೯೯೪ರ ಅಡಿಯಲ್ಲಿ ಕರ್ನಾಟಕ ಸರ್ಕಾರದಿಂದ ಸ್ಥಾಪಿತವಾದ ರಾಜ್ಯವಿಶ್ವವಿದ್ಯಾಲಯ  
**VISVESVARAYA TECHNOLOGICAL UNIVERSITY**



State University of Government of Karnataka Established as per the VTU Act, 1994 "JnanaSangama" Belagavi-590018

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REGISTRAR

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REF: VTU/BGM/BoS/BCSL404-CE/683/2024-25/ 5520

DATE: 27 JAN 2025

**CIRCULAR**

**Subject:** BCSL404- Course title correction in Computer Engineering program regarding..

**Reference:**

1. Email from Chairperson BoS in CSE dated: 26.01.2025
2. The Hon'ble Vice-Chancellor's approval dated:

With reference to the subject mentioned above, it has been observed that in the Computer Engineering program, the course code **BCSL404** was erroneously listed as **Database Management System** in the scheme, and the syllabus for this course was missing. However, in other CSE stream programs, the same course code corresponds to **BCSL404 - Analysis & Design of Algorithm Lab**, and the syllabus is available.

The error has been rectified, and the course title has been corrected to **BCSL404- Analysis & Design of Algorithm Lab** in the scheme. The syllabus has also been added to the Computer Engineering program on the VTU web portal.

All principals of engineering colleges under the university's ambit are hereby requested to take note of this update and ensure the contents of this circular are brought to the attention of all concerned without fail.

**Encl:** BCSL404- Analysis & Design of Algorithm lab syllabus and Scheme

R 27/01/25 BE  
REGISTRAR  
[Signature]

To,

All the Principals of Engineering Colleges are under the ambit of the university.  
The Chairpersons/Program Coordinators of the University Department at Kalburgi,  
Belagavi, Mysuru, and Bengaluru

Copy to,

- Registrar (Evaluation) VTU Belagavi for information and needful
- The chairperson and BoS members of the CSE Board for UG programs for information
- The Director, ITI SMU, VTU Belagavi for information and make arrangements to upload the circular on the VTU web portal
- The Special Officer, QPDS Section, VTU Belagavi for information and also make arrangements to upload the circular on the VTU web portal for stakeholder's reference.
- Office copy

VARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI  
**B.E. in Computer Engineering**  
**Scheme of Teaching and Examinations 2022**  
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)  
 (Effective from the academic year 2023-24)

IV SEMESTER													
Sl. No	Course and Course Code		Course Title	Teaching Department (TD) and Question Paper Setting Board (PSB)	Teaching Hours /Week				Examination				Credits
					Theory Lecture	Tutorials	Practical / Drawing	Self - Study	Duration in hours	CIE Marks	SEE Marks	Total Marks	
1	PCC/BS C	BCS401	Analysis & Design of Algorithms	TD: CE PSB : CS	3	0	0		03	50	50	100	3
2	IPCC	BCS402	Microcontrollers	TD: CE PSB : CS	3	0	2		03	50	50	100	4
3	IPCC	BCS403	Database Management Systems	TD: CE PSB : CS	3	0	2		03	50	50	100	4
4	PCCL	BCSL404	<b>Analysis &amp; Design of Algorithm Lab</b>	TD: CE PSB : CS	0	0	2		03	50	50	100	1
5	ESC	BCS405x	ESC/ETC/PLC	TD: CS/Maths PSB : CS/Maths	2	2	0		03	50	50	100	3
6	AEC/SEC	BCS456x	Ability Enhancement Course/Skill Enhancement Course- IV	TD : Concerned department PSB: CS	If the course is Theory				01	50	50	100	1
					1	0	0						
					If the course is a lab				02				
					0	0	2						
4	BSC	BBOC407	Biology For Computer Science	TD / PSB: BT, CHE,	2	0	0		03	50	50	100	2
7	UHV	BUHK408	Universal human values course	Any Department	1	0	0		01	50	50	100	1
9	MC	BNSK459	National Service Scheme (NSS)	NSS coordinator	0	0	2		100	---	100	0	
		BPEK459	Physical Education (PE) (Sports and Athletics)	Physical Education Director									
		BYOK459	Yoga	Yoga Teacher									

<b>Analysis &amp; Design of Algorithms Lab</b>		Semester	4
Course Code	<b>BCSL404</b>	CIE Marks	50
Teaching Hours/Week (L:T:P: S)	0:0:2:0	SEE Marks	50
Credits	01	Exam Hours	2
Examination type (SEE)	Practical		
<b>Course objectives:</b>			
<ul style="list-style-type: none"> <li>To design and implement various algorithms in C/C++ programming using suitable development tools to address different computational challenges.</li> <li>To apply diverse design strategies for effective problem-solving.</li> <li>To Measure and compare the performance of different algorithms to determine their efficiency and suitability for specific tasks.</li> </ul>			
Sl.No	Experiments		
1	Design and implement C/C++ Program to find Minimum Cost Spanning Tree of a given connected undirected graph using Kruskal's algorithm.		
2	Design and implement C/C++ Program to find Minimum Cost Spanning Tree of a given connected undirected graph using Prim's algorithm.		
3	a. Design and implement C/C++ Program to solve All-Pairs Shortest Paths problem using Floyd's algorithm. b. Design and implement C/C++ Program to find the transitive closure using Warshal's algorithm.		
4	Design and implement C/C++ Program to find shortest paths from a given vertex in a weighted connected graph to other vertices using Dijkstra's algorithm.		
5	Design and implement C/C++ Program to obtain the Topological ordering of vertices in a given digraph.		
6	Design and implement C/C++ Program to solve 0/1 Knapsack problem using Dynamic Programming method.		
7	Design and implement C/C++ Program to solve discrete Knapsack and continuous Knapsack problems using greedy approximation method.		
8	Design and implement C/C++ Program to find a subset of a given set $S = \{s_1, s_2, \dots, s_n\}$ of n positive integers whose sum is equal to a given positive integer d.		
9	Design and implement C/C++ Program to sort a given set of n integer elements using Selection Sort method and compute its time complexity. Run the program for varied values of $n > 5000$ and record the time taken to sort. Plot a graph of the time taken versus n. The elements can be read from a file or can be generated using the random number generator.		
10	Design and implement C/C++ Program to sort a given set of n integer elements using Quick Sort method and compute its time complexity. Run the program for varied values of $n > 5000$ and record the time taken to sort. Plot a graph of the time taken versus n. The elements can be read from a file or can be generated using the random number generator.		
11	Design and implement C/C++ Program to sort a given set of n integer elements using Merge Sort method and compute its time complexity. Run the program for varied values of $n > 5000$ , and record the time taken to sort. Plot a graph of the time taken versus n. The elements can be read from a file or can be generated using the random number generator.		
12	Design and implement C/C++ Program for N Queen's problem using Backtracking.		

**Course outcomes (Course Skill Set):**

At the end of the course the student will be able to:

1. Develop programs to solve computational problems using suitable algorithm design strategy.
2. Compare algorithm design strategies by developing equivalent programs and observing running times for analysis (Empirical).
3. Make use of suitable integrated development tools to develop programs
4. Choose appropriate algorithm design techniques to develop solution to the computational and complex problems.
5. Demonstrate and present the development of program, its execution and running time(s) and record the results/inferences.

**Assessment Details (both CIE and SEE)**

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum marks (20 marks out of 50) and for the SEE minimum passing mark is 35% of the maximum marks (18 out of 50 marks). A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/course if the student secures a minimum of 40% (40 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together

**Continuous Internal Evaluation (CIE):**

CIE marks for the practical course are **50 Marks**.

The split-up of CIE marks for record/ journal and test are in the ratio **60:40**.

- Each experiment is to be evaluated for conduction with an observation sheet and record write-up. Rubrics for the evaluation of the journal/write-up for hardware/software experiments are designed by the faculty who is handling the laboratory session and are made known to students at the beginning of the practical session.
- Record should contain all the specified experiments in the syllabus and each experiment write-up will be evaluated for 10 marks.
- Total marks scored by the students are scaled down to **30 marks** (60% of maximum marks).
- Weightage to be given for neatness and submission of record/write-up on time.
- Department shall conduct a test of 100 marks after the completion of all the experiments listed in the syllabus.
- In a test, test write-up, conduction of experiment, acceptable result, and procedural knowledge will carry a weightage of 60% and the rest 40% for viva-voce.
- The suitable rubrics can be designed to evaluate each student's performance and learning ability.
- The marks scored shall be scaled down to **20 marks** (40% of the maximum marks).

The Sum of scaled-down marks scored in the report write-up/journal and marks of a test is the total CIE marks scored by the student.

**Semester End Evaluation (SEE):**

- SEE marks for the practical course are 50 Marks.

- SEE shall be conducted jointly by the two examiners of the same institute, examiners are appointed by the Head of the Institute.
- The examination schedule and names of examiners are informed to the university before the conduction of the examination. These practical examinations are to be conducted between the schedule mentioned in the academic calendar of the University.
- All laboratory experiments are to be included for practical examination.
- (Rubrics) Breakup of marks and the instructions printed on the cover page of the answer script to be strictly adhered to by the examiners. **OR** based on the course requirement evaluation rubrics shall be decided jointly by examiners.
- Students can pick one question (experiment) from the questions lot prepared by the examiners jointly.
- Evaluation of test write-up/ conduction procedure and result/viva will be conducted jointly by examiners.

General rubrics suggested for SEE are mentioned here, writeup-20%, Conduction procedure and result in -60%, Viva-voce 20% of maximum marks. SEE for practical shall be evaluated for 100 marks and scored marks shall be scaled down to 50 marks (however, based on course type, rubrics shall be decided by the examiners)

Change of experiment is allowed only once and 15% of Marks allotted to the procedure part are to be made zero.

The minimum duration of SEE is 02 hours

**Suggested Learning Resources:**

- Virtual Labs (CSE): <http://cse01-iiith.vlabs.ac.in/>