M.TECH IN SIGNAL PROCESSING (LSP)

Choice Based Credit System (CBCS) and Outcome Based Education (OBE)

(Effective from the academic year 2022-23)

Semester-1

		Advanced Engineering Mathem	atics				
Course Code		22LSP11	CIE Marks	50			
Teaching Hour	s/Week (L:P:SDA)	3:0:0	SEE Marks	50			
Total Hours of	Pedagogy	40 hours of Theory	Total Marks	100			
Credits		03	Exam Hours	03			
Course Learnir • To lo • To u elec	Credits 03 Exam Hours 03 Course Learning objectives: • To learn principles of advanced engineering mathematics through linear algebra and calculus of variations. • To understand probability theory and random process that serve as an essential tool for applications of electronics and communication engineering sciences.						
		Module-1					
Teaching- Learning	Chalk and Talk / Power Po	int Presentations.					
Process		Madula 2					
		Module-2					
Teaching- Learning Proce	Chalk and Talk / Power	r Point Presentations.					
		Module-3					
Teaching- Learning Process	Chalk and Talk / Power Po	int Presentations.					
		Module-4					
Teaching- Learning Process	Chalk and Talk / Power Po	int Presentations.					
	1	Module-5					
Teaching- Learning	Chalk and Talk / Power Poi	nt Presentations.					

	22
Process	
Assessment D	etails (both CIE and SEE)
The weightage mark for the o student shall b the student se SEE (Semester Continuous In 1. Three Ur 2. Two assig to attain The sum of thr CIE methods /	e of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if cures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and End Examination) taken together. ternal Evaluation: hit Tests each of 20 Marks gnments each of 20 Marks or one Skill Development Activity of 40 marks the COs and POs ee tests, two assignments/skill Development Activities, will be scaled down to 50 marks question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for
the course.	
Semester End 1. The SEE of 2. The ques 3. Each full module. 4. Each full 5. The stud	Examination: question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50. tion paper will have ten full questions carrying equal marks. question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each question will have a sub-question covering all the topics under a module. ents will have to answer five full questions, selecting one full question from each module
Suggested Lea	rning Resources:
Books 1. Educa 2. 3. 4. 5. 6. Elsevi 7.	David C.Lay, Steven R.Lay and J.J.McDonald: "Linear Algebra and its Applications", 5th Edition, Pearson ntion Ltd., 2015 Elsgolts, L.:"Differential Equations and Calculus of Variations", MIR Publications 3rd Edition, 1977. T.Veerarajan: "Probability, Statistics and Random Process", 3rd Edition, Tata Mc-Graw Hill Co., 2016. Gilbert Strang: Introduction to Linear Algebra, 5th Edition, Wellesley-Cambridge Press., 2016 Richard Bronson: "Schaum's Outlines of Theory and Problems of Matrix Operations", McGraw-Hill, 1988. Scott L.Miller,Donald G.Childers: "Probability and Random Process with application to Signal Processing", er Academic Press, 2nd Edition,2013. E. Kreyszig, "Advanced Engineering Mathematics", 10th edition, Wiley, 2015.
Web links and 1. http://r 2. http://v 3. http://d 4. www.w	Video Lectures (e-Resources): nptel.ac.in/courses.php?disciplineId=111 www.class-central.com/subject/math(MOOCs) ocw.mit.edu/courses/mathematics/ olfram.com

Skill Development Activities Suggested

• Practice more number of complex problems

Course outcome (Course Skill Set):

At the end of the course the student will be able to :

Description	Blooms Level
Understand vector spaces, basis, linear transformations and the process of obtaining	Understand
matrix of linear transformations arising in magnification and rotation of images.	
Apply techniques of constrained optimization for problems arising in control system	Understand
analysis, signals and systems.	
Apply the techniques of inner product, orthogonal vectors, Q.R decomposition and	Analyze
singular value decomposition for data compression.	
Learn the idea of random variables (discrete/continuous) and probability distributions in	Understand
analyzing the probability models arising in control systems and system communications.	
Analyze random process through parameter-dependent variables in various random	Analyze
processes.	
	DescriptionUnderstand vector spaces, basis, linear transformations and the process of obtaining matrix of linear transformations arising in magnification and rotation of images.Apply techniques of constrained optimization for problems arising in control system analysis, signals and systems.Apply the techniques of inner product , orthogonal vectors , Q.R decomposition and singular value decomposition for data compression.Learn the idea of random variables (discrete/continuous) and probability distributions in analyzing the probability models arising in control systems and system communications.Analyze random process through parameter-dependent variables in various random processes.

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		Advanced Digital Signal Processing				
Course Code		22LSP12	CIE Marks	50		
Teaching Hours	/Week (L:P:SDA)	3:2:0	SEE Marks	50		
Total Hours of F	Pedagogy	40 hours Theory + 10-12 Lab slots	Total Marks	100		
Credits		04	Exam Hours	03		
 Course objectives: Understand Multirate digital signal processing principles and its applications. Estimate the various spectral components present in the received signal using different spectral estimation methods such as Parametric and Nonparametric. 						
DesignUnderst	and implement an optimu stand the concepts and ma	m adaptive filter using LMS and RLS alg thematical representations of Wavelet	orithms. transforms.			
		MODULE-1				
Review of tra Discrete Cosine	nsforms, Z-Transform, Dis e Transform (DCT), Short Ti	screte Time Fourier Transform (DTFT me Fourier Transform (STFT).), Discrete Fourier	Transform (DFT),		
Learning Process						
		MODULE-2				
zeros, FIR filter sampling meth transformatior Teaching- Learning Proces	structures, IIR filter struct ad, Finite word length effort, Finite word length effect: Chalk and Talk / Powe	ures, Design of FIR filters, Linear Phase ects, Design of IIR filters, Pole zero pla s r Point Presentations.	Systems, Window m cement, Impulse inv	ethod, Frequency ariance, Bilinear Z		
		MODULE-3				
Linear prediction Representation Equations. The	on and Optimum Linear F of a Stationary Random Pr Levinson-Durbin Algorithr	ilters: Random signals, Correlation Fur ocess. Forward and Backward Linear Pr n. Properties of the Linear Prediction	nctions and Power Sp rediction. Solution of -Error Filters.	ectra, Innovations the Normal		
Teaching- Learning	Chalk and Talk / Power Po	int Presentations.				
Process						
MODULE-4 Adaptive filters: Applications of Adaptive Filters-Adaptive Channel Equalization, Adaptive noise cancellation, Linear Predictive coding of Speech Signals, Adaptive direct form FIR filters-The LMS algorithm, Properties of LMS algorithm. Adaptive direct form filters- RLS algorithm.						
Learning Process						
MODULE 5						
Power Spectrum Estimation: Non parametric Methods for Power Spectrum Estimation - Bartlett Method, Welch Method, Blackman and Tukey Methods. Parametric Methods for Power Spectrum Estimation: Relationship between the auto correlation and the model parameters, Yule and Walker methods for the AR Model Parameters, Burg Method for the AR Model parameters, Unconstrained least-squares method for the AR Model parameters, Sequential estimation methods for the AR Model parameters, ARMA Model for Power Spectrum Estimation.						
Learning	Chaik and Taik / Power Po	onic Presentations.				

Process		

5

PRACTICAL COMPONENT OF IPCC(*May cover all / major modules*)

SI.NO	Experiments
1	Write a program in Matlab implement the Levinson – Durbin algorithm of prediction
2	Write a program in Matlab to implement LMS adaptive filter
3	Write a program in Matlab to implement RLS filter
4	Write a program in Matlab to compute the power spectrum estimation using Bartlett method, Welch method, Blackman and Tukey method
5	Write a program in Matlab to estimate AR model parameters
6	Write a program in Matlab to estimate ARMA model parameters
7	Write a program in Matlab to compress the signal using the DWT
8	Write a program in Matlab to estimate the signal from corrupted signal , also compute the SNR
9	Demonstration Experiments (For CIE) if any
10	Design a simulink using Matlab to implement LMS and RLS filter
11	Design a simulink using Matlab to implement power spectrum estimation
	ant Dataile (bath CIE and CEE)

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together

CIE for the theory component of IPCC

- 1. Two Tests each of 20 Marks
- 2. Two assignments each of 10 Marks/One Skill Development Activity of 20 marks
- 3. Total Marks of two tests and two assignments/one Skill Development Activity added will be CIE for 60 marks, marks scored will be proportionally scaled down to **30 marks**.

CIE for the practical component of IPCC

• On completion of every experiment/program in the laboratory, the students shall be evaluated and marks shall

be awarded on the same day. The**15 marks** are for conducting the experiment and preparation of the laboratory record, the other **05 marks shall be for the test** conducted at the end of the semester.

- The CIE marks awarded in the case of the Practical component shall be based on the continuous evaluation of the laboratory report. Each experiment report can be evaluated for 10 marks. Marks of all experiments' write-ups are added and scaled down to 15 marks.
- The laboratory test at the end /after completion of all the experiments shall be conducted for 50 marks and scaled down to 05 marks.

Scaled-down marks of write-up evaluations and tests added will be CIE marks for the laboratory component of IPCC for **20 marks**.

SEE for IPCC

Theory SEE will be conducted by University as per the scheduled timetable, with common question papers for the course (duration 03 hours)

- 1. The question paper will be set for 100 marks and marks scored will be scaled down proportionately to 50 marks.
- 2. The question paper will have ten questions. Each question is set for 20 marks.
- 3. There will be 2 questions from each module. Each of the two questions under a module (with a maximum of 3 subquestions), **should have a mix of topics** under that module.
- 4. The students have to answer 5 full questions, selecting one full question from each module.

The theory portion of the IPCC shall be for both CIE and SEE, whereas the practical portion will have a CIE component only. Questions mentioned in the SEE paper shall include questions from the practical component).

- The minimum marks to be secured in CIE to appear for SEE shall be the 15 (50% of maximum marks-30) in the theory component and 10 (50% of maximum marks -20) in the practical component. The laboratory component of the IPCC shall be for CIE only. However, in SEE, the questions from the laboratory component shall be included. The maximum of 04/05 questions to be set from the practical component of IPCC, the total marks of all questions should not be more than the 20 marks.
- SEE will be conducted for 100 marks and students shall secure 40% of the maximum marks to qualify in the SEE. Marks secured will be scaled down to 50. (Student has to secure an aggregate of 50% of maximum marks of the course(CIE+SEE)

Suggested Learning Resources:

Books

- 1. 'Digital Signal Processing, Principles, Algorithms and Applications', John G. Proakis, Dimitris G.Manolakis, Pearson, Fourth edition, 2007
- 2. 'Insight into Wavelets- from Theory to Practice', K P Soman, Ramachandran, Resmi, PHI, Third Edition, 2010

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Activity Based Learning (Suggested Activities in Class)/ Practical Based learning

Mini project in the area of digital signal processing using modern tools like MATLAB, Python, scilab

Course Outcomes

At the end of the course the student will be able to :

SI.	Description	Blooms Level
No.		
CO1	Able to understand and analyze the linear prediction and optimum linear filters	L1 L2 Ll3
CO2	Able to understand and analyze LMS and RLS adaptive filter algorithms	L2 L4
CO3	Able to analyze and implement the parametric and non parametric methods of power spectrum estimation	L2 L4

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				8	
	Pattern reco	gnition and Machine Learning for Data	Processing		
Course Code		22LSP13	CIE Marks	50	
Teaching Hours	s/Week (L:P:SDA)	3:0:2	SEE Marks	50	
Total Hours of	Pedagogy	40 hours Theory + 10-12 slots for Skill Development Activities	Total Marks	100	
Credits		04 Exam Hours 03			
 Course Learning objectives: To understand the basic theory underlying machine learning. To be able to formulate machine learning problems corresponding to different applications. To understand a range of machine learning algorithms along with their strengths and weaknesses. To be able to apply machine learning algorithms to solve problems of moderate complexity. To apply the algorithms to a real-world problem, optimize the models learned and report on the expected accuracy that can be achieved by applying the models. Module-1 Introduction, concept Learning :Well posed learning problems ,Designing a Learning system Perspective and Issues in Machine Learning. Concept Learning: Concept learning task, Concept learning as search ,Find-S algorithm, Version space, Candidate Elimination algorithm, Inductive Bias 					
Teaching- Learning Process	Chalk and Talk / Power Po	pint Presentations.			
	I	Module-2			
Teaching- Learning Proce	Chalk and Talk / Powe	r Point Presentations.			
		Module-3			
Natural Gradi Analysis, Naiv Boosting and Concentration Teaching- Learning Process	ent, Exponential Family a e Bayes), Kernel Method (Gradient Boosting), Lea Inequalities, Generalizatio Chalk and Talk / Power Po	nd Generalized Linear Models, Genera SVM, Gaussian Processes), Tree Enseml rning Theory, Regularization, Bias-Var n and Uniform Convergence, VC-dimens pint Presentations.	ative Models (Gaus bles (Decision trees, 'iance Decomposition,	sian Discriminant Random Forests, on and Tradeoff,	
		Module-4			
Deep Learning Mixture Mode Components A Processes (MD Teaching- Learning Process	: Neural Networks, Backp I (GMM), Expectation Ma nalysis (PCA), Independen P), Bellmans Equations, Val Chalk and Talk / Power Po	ropagation, Deep Architectures, Unsu eximization (EM), Variational Auto-enc t Components Analysis (ICA), Reinforce ue Iteration and Policy Iteration, Value F pint Presentations.	pervised Learning, oder (VAE), Factor ement Learning (RL) Function Approxima	K-means, Gaussian Analysis, Principal : Markov Decision tion, Q-Learning,	
1100033		Module-5			
Application: Ac Topic: Compute	lvice on structuring an ML p er Vision. Special Topic: NL	project, Evaluation Metrics, Missing data	a techniques and tra	cking, Special	
Special topic: Sensing, Specia constrained se	Machine listening and Mu Il topics: Array processing, paration, spectral factorizat	sic Information Retrieval, Special Topic beam forming, independent component ions.	c: Speech, Special ٦ t analysis, MIMO/SII	opic: Compressive NO models, under-	

Learning Process

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 3. Three Unit Tests each of 20 Marks
- 4. Two assignments each of **20 Marks** or **one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 6. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 7. The question paper will have ten full questions carrying equal marks.
- 8. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 9. Each full question will have a sub-question covering all the topics under a module.
- 10. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

Suggested Learning Resources:

Books

- 1. "Pattern Recognition and Machine Learning", C.M. Bishop, 2nd Edition, Springer, 2011.
- 2. Probabilistic machine learning by Kevin P Murphy
- 3. Pattern recognition by Duda and Hart
- 4. Machine Learning for Signal Processing: Data Science, Algorithms, and Computational Statistics, Max A. Little
- 5. Deep Learning By Ian Goodfellow, Yoshua Bengio, Aaron Courville Online book, 2017
- 6. Deep Learning with Python By J. Brownlee

7. Deep Learning Step by Step with Python: A Very Gentle Introduction to Deep Neural Networks for Practical Data Science By N. D. Lewis

8. "Machine Learning for Audio, Image and Video Analysis", F. Camastra, Vinciarelli, Springer, 2007. link http://www.dcs.gla.ac.uk/~vincia/textbook.pdf

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

Mini project group wise on implementation of Machine learning algorithm using python

Course Outcomes

At the end of the course the student will be able to :

SI. No. Description

	10	
CO1	Understand a very broad collection of machine learning algorithms and problems.	L2
CO2	To Demonstrate knowledge in the application/analysis of Machine Learning algorithms to solve various types of learning tasks	L2
CO3	Learn algorithmic topics of machine learning and mathematically deep enough to introduce the required theory	L2 L3
CO4	Carry out research/Investigation for a given Machine Learning Technique	L5

[11		
		DSP System Design				
Course Code		22LSP14	CIE Marks	50		
Teaching Hour	s/Week (L:P:SDA)	2:0:2	SEE Marks	50		
Total Hours of	Total Hours of Pedagogy 25 hours Theory + 10-12 slots for Skill Total Marks 100 Development Activities 100					
Credits		03	Exam Hours	03		
 Course Learning objectives: To explain the architecture and instruction set DSP processor To understand the linear mid rate quantization process 						
		Module-1				
Implementat	on considerations:Introd	uction,Data representation and arit	hmetic, Finite wor	d length effects,		
Programming	issues, Real time implement	ntation considerations, Hardware interf	acing, Experiments			
Teaching-	Chalk and Talk / Power P	oint Presentations.				
Learning						
Process						
		Module-2				
Architecture	and Instruction Set of the (C6x Processor: Introduction. TMS320C6	ox Architecture. Funct	tional Units. Linear		
and Circular	Addressing Modes TMS22	C6x Instruction Set Interrupts Multic	hannel Ruffered Ser	ial Ports Memory		
Consideration	c Eived and Electing Doin	t Format Constraints	shanner bunered Ser	iai i orts, ivieniory		
Consideration	is, rixeu- allu rioatilig-Polit	t Format, constraints				
Teaching-	Chalk and Talk / Powe	er Point Presentations.				
Learning Proce	255					
		Module-3				
Fixed point I	Digital signal processor :	Introduction, TMS320C2000, TMS3200	C54X, TMS320C55X,	TMS320C62X and		
TMS320C64X	Floating point digital sign	nal processor: Introduction, TMS320C3	X,TMS320C67XX TM	S320C6713 Digital		
signal process	or. TMS3203X6416 Digital	signal processor		-		
- 8						
Teaching-	Chalk and Talk / Power P	oint Presentations.				
Learning						
Process						
		Module-4				
Introduction to	o Z transform, Discrete sign	als, FIR filters, FIR lattice structure. FIR	implementation usin	g Fourier series and		
window functi	ons. Implementation using	g Matlab IIR filters: Introduction, IIR f	ilter structures, bilin	ear transformation.		
implementatio	n using Matlab					
	<u> </u>					
Teaching-	Chalk and Talk / Power P	oint Presentations.				
Learning						
Process						
		Module-5				
Linear midrate	e quantization, μLawcomp	anding,DPCM,DeltaModulation and ad	aptive DPCM, DCT,	Modified DCT and		
Transform coding in MPEG audio, Examples of signal quantization using TMS320CX713DSK,Matlab programs Sub band						
and wavelet b	ased coding : sub band cod	ding basics, sub band decomposition, su	ub band coding of sig	nals, wavelet basics		
and families of wavelet multi resolution equations. Discrete wavelet transform wavelet transform coding of						
signals matlab programs						
signals, matidu	programs.					
		int Duccontation				
reaching-	Chaik and Talk / Power Po	int Presentations.				
Loorning						

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 1. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 2. The question paper will have ten full questions carrying equal marks.
- 3. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 4. Each full question will have a sub-question covering all the topics under a module.
- 5. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

1. 'Digital Signal Processors, architecture, Implementations and Applications Sen M Kuo, Woon-SengS.Gan Pearson prentice hall publishers-7th edition.

2. 'Digital Signal Processing and Application with C6713 and C6416 DSK', RulphChassaing, Donald ReayWiley-Interscience Publication.

3. Digital signal processing fundamentals and applications Li Tan, Jean Jiang Elsevier second edition

4. Digital signal processing Principles, algorithm and applications by John G proakis, Dimitris G manolokis Pearson fourth edition.

5. Digital signal processing by salivahanan, Avallavaraj, Gnanapriya TaTaMcGrawl-Hill 2007

6.Digital signal processing with Matlab programs Dr.sanjaysharma SK kataria&sons sixth edition

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• To simulate the signals and signal processing using DSP processor using CCS Studio of TMS320c6XXX series

Course Outcomes

SI.	Description	Blooms Level
No.		
CO1	Able to understand and analyze the DSP processor architecture	L1 L2 L3
CO2	Able to analyze the fixed point and floating point DSP processor	L4
CO3	Able to understand and analyze digital filter systems	L2L4
CO4	Able to understand and analyze the digital modulation systems	L2 L4

	A -b	and Markinsviets Contained Silters D		14		
Course Code	Advan	Ced Multivariate Systems and Filters Ba	Inks	50		
Course Code		22LSP15		50		
Teaching Hour	S/WEEK (L:P:SDA)	2:0:2	SEE Marks	50		
Total Hours of Pedagogy		25 hours Theory + 10-12 slots for Skill Development Activities	Total Marks	100		
Credits		03	Exam Hours	03		
Course Learnir • Descr • Under	 Course Learning objectives: Describe the need of multi-rate systems and it applications. Understand the theory of multi-rate DSP solve numerical problems and write algorithms 					
• Under	rstand theory of prediction	and solution of normal equation				
		Module-1				
Fundamentals representation	of Multirate Systems: Bas , multistage implementati	sic multi-rate operations, interconnecti on, applications of multi-rate systems,	on of building bloc special filters and fil	ks, poly-phase ter bank		
Teaching- Learning Process	Chalk and Talk / Power Po	pint Presentations.				
		Module-2				
M-channel filt structured filte Teaching- Learning Proce	er banks, poly-phase repr er banks, trans-multiplexers Chalk and Talk / Powe	esentation, perfect reconstruction syste r Point Presentations.	ms, alias-free filter b	oanks, tree		
		Module-3				
Para-unitary Po unitariness, tw	erfect Reconstruction Filter o channel Para-unitary latti	Banks: Lossless transfer matrices, filter l ces, M-channel FIR Para-unitary QMF ba	oank properties indu inks, transform codi	uced by para- ng		
Teaching-	Chalk and Talk / Power Po	pint Presentations.				
Learning						
Process						
		Module-4				
Linear Phase Perfect Reconstruction QMF Banks: Necessary conditions, lattice structures for linear phase FIR PR QMF banks, formal synthesis of linear phase FIR PR QMF lattice Cosine Modulated Filter Banks: Pseudo-QMF bank and its design, efficient poly-phase structures, properties of cosine matrices, cosine modulated perfect reconstruction systems						
Teaching-	Chalk and Talk / Power Po	pint Presentations.				
Learning						
Process Module-5						
Wavelet Transform: Short-time Fourier transform, Wavelet L2, L3 transform, discrete-time Ortho-normal wavelets, continuous time Ortho-normal wavelets						
Teaching- Learning Process	Chalk and Talk / Power Poi	nt Presentations.				

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of **20 Marks**
- 2. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. P.P.Vaidyanathan, "Multirate Systems and Filter Banks", Pearson Education (Asia) Pte.Ltd, 2004.
- 2. Gilbert Strang and Truong Nguyen, "Wavelets and Filter Banks", Wellesley-Cambridge Press, 1996.
- 3. N. J. Fliege, "Multirate Digital Signal Processing", John Wiley & Sons, USA, 2000.
- 4. Vikram Gadre & Aditya Abhyankar "Multiresolution and Multirate Signal Processing: Introduction, Principles and Applications" McGraw Hill Education, First edition(2017).
- 5. Steven M. Kay "Modern Spectral Estimation" Pearson Education, First edition (2017)

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• To implement the filter bank and its spectrum using Matlab software

Course Outcomes

SI.	Description	Blooms Level
No.		
CO1	Able to understand and implement the multirate systems	L2 L4
CO2	Able to understand and analyze decimated filter banks and reconstruction filter banks	L2 L4
CO3	Able to understand QMF banks, efficient poly – phase structures	L1 L2
CO4	Able to understand and analyze the STFT, CWT and DWT	L2 L4

		Research Methodology and I	PR		
Course Code		22RMI16	CIE Marks	50	
Teaching Hours/Week (L:P:SDA)		3:0:0	SEE Marks	50	
Total Hours of Pedagogy		40 Hours	Total Marks	100	
Credits	0.01	03	Exam Hours	03	
Total Hours of Pedagogy 40 Hours Total Marks 100 Credits 03 Exam Hours 03 Course Learning objectives: 1. Discuss research methodology and the technique of defining a research problem 2. Explain the functions of the literature review in research, carrying out a literature search, developing theoretical and conceptual frameworks and writing a review. 3. Explain various research designs, sampling designs, measurement and scaling techniques and also different methods of data collections. 4. Explain several parametric tests of hypotheses, Chi-square test, art of interpretation and writing research reports 5. Discuss various forms of the intellectual property, its relevance and business impact in the changing global business environment and leading International Instruments concerning IPR Module-1					
Teaching- Learning Process	Chalk and Talk / Power Po	pint Presentations.			
		Module-2			
Teaching- Learning Proce	Chalk and Talk / Powe	r Point Presentations.			
	I	Module-3			
Teaching-	Chalk and Talk / Power Po	oint Presentations.			
Learning					
Process					
Module-4					
Teaching- Learning Process	Chalk and Talk / Power Po	pint Presentations.			

Module-5		
Teaching-	Chalk and Talk / Power Point Presentations.	
Learning		
Process		
Assessment Details (both CIE and SEE)		
The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum		
passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of		

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passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

Textbooks:

- 1. Research Methodology: Methods and Techniques, C.R. Kothari, Gaurav Garg, New Age International,4th Edition, 2018.
- 2. Research Methodology a step-by-step guide for beginners. (For the topic Reviewing the literature under module 2), Ranjit Kumar, SAGE Publications, 3rd Edition, 2011.
- 3. Study Material (For the topic Intellectual Property under module 5), Professional Programme Intellectual Property Rights, Law and Practice, The Institute of Company Secretaries of India, Statutory Body Under an Act of Parliament, September 2013.
- 4. Research Methods: the concise knowledge base, Trochim, Atomic Dog Publishing, 2005.
- 5. Conducting Research Literature Reviews: From the Internet to Paper, Fink A, Sage Publications, 2009.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Course outcome (Course Skill Set) At the end of the course the student will be able to :

Sl. No.	Description	Blooms Level
CO1	Discuss research methodology and the technique of defining a research problem.	L1, L2
CO2	Explain the functions of the literature review in research, carrying out a literature search, developing theoretical and conceptual frameworks and writing a review.	L1, L2
CO3	Explain various research designs, sampling designs, measurement and scaling techniques and also different methods of data collections.	L1, L2
CO4	Explain several parametric tests of hypotheses, Chi-square test, art of interpretation and writing research reports.	L1, L2, L3
CO5	Discuss various forms of the intellectual property, its relevance and business impact in the changing global business environment and leading International Instruments concerning IPR.	L1, L2, L3, L4

22LSPL17					
Course Code	Advanced Digital Signal Processing Lab	CIE Marks	50		
Teaching Hours/Week (L:T:P: S)	1:0:2:0	SEE Marks	50		
Credits	02	Exam Hours	03		
Course objectives:	Course objectives:				
 To understand the Matlab software 	 To understand the Matlab software for digital signal processing problems 				
• To learn the various programmir	To learn the various programming skills to implement the digital signal processing algorithms				
SI.NO	Experiments				

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	20
1	Generate various fundamental discrete time signals.
2	Basic operations on signals (Multiplication, Folding, Scaling).
3	Find out the DFT & IDFT of a given sequence without using inbuilt instructions.
4	Interpolation & decimation of a given sequence.
5	Generation of DTMF (Dual Tone Multiple Frequency) signals.
6	Estimate the PSD of a noisy signal using periodogram and modified periodogram.
7	Estimation Of PSD using different methods (Bartlett, Welch, Blackman- Tukey).
8	Design of Chebychev Type I,II Filters.
9	Cascade Digital IIR Filter Realization.
10	Parallel Realization of IIR filter.
11	Estimation of power spectrum using parametric methods (Yule-Walker & Burg).
12	Design of LPC filter using Levinson-Durbin algorithm.
13	Time-Frequency Analysis with the Continuous Wavelet Transform.
14	Signal Reconstruction from Continuous Wavelet Transform Coefficients.
	Demonstration Experiments (For CIE)
1	To implement the LPC filter using Matlab Simulink
2	To implement the Time-Frequency Analysis with the Continuous Wavelet Transform using Matlab Simulink
3	To implement the Signal Reconstruction from Continuous Wavelet Transform Coefficients using Matlab Simulink
Course	outcomes (Course Skill Set):
At the	end of the course the student will be able to:
1	

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each course. The student has to secure not less than 40% of maximum marks in the semester-end examination(SEE). In total of CIE and SEE student has to secure 50% maximum marks of the course.

Continuous Internal Evaluation (CIE):

CIE marks for the practical course is **50 Marks**.

The split-up of CIE marks for record/ journal and test are in the ratio 60:40.

- Each experiment to be evaluated for conduction with observation sheet and record write-up. Rubrics for the evaluation of the journal/write-up for hardware/software experiments designed by the faculty who is handling the laboratory session and is made known to students at the beginning of the practical session.
- Record should contain all the specified experiments in the syllabus and each experiment write-up will be evaluated for 10 marks.
- Total marks scored by the students are scaled downed to 30 marks (60% of maximum marks).
- Weightage to be given for neatness and submission of record/write-up on time.
- Department shall conduct 02 tests for 100 marks, the first test shall be conducted after the 8th week of the semester and the second test shall be conducted after the 14th week of the semester.
- In each test, test write-up, conduction of experiment, acceptable result, and procedural knowledge will carry a weightage of 60% and the rest 40% for viva-voce.
- The suitable rubrics can be designed to evaluate each student's performance and learning ability.
- The average of 02 tests is scaled down to 20 marks (40% of the maximum marks).

The Sum of **scaled-down** marks scored in the report write-up/journal and average marks of two tests is the total CIE marks scored by the student.

Semester End Evaluation (SEE):

SEE marks for the practical course is 50 Marks.

SEE shall be conducted jointly by the two examiners of the same institute, examiners are appointed by the University.

All laboratory experiments are to be included for practical examination.

(Rubrics) Breakup of marks and the instructions printed on the cover page of the answer script to be strictly adhered to by the examiners. **OR** based on the course requirement evaluation rubrics shall be decided jointly by examiners. Students can pick one question (experiment) from the questions lot prepared by the internal /external examiners jointly.

Evaluation of test write-up/ conduction procedure and result/viva will be conducted jointly by examiners.

General rubrics suggested for SEE are mentioned here, writeup-20%, Conduction procedure and result in 60%, Vivavoce 20% of maximum marks. SEE for practical shall be evaluated for 100 marks and scored marks shall be scaled down to 50 marks (however, based on course type, rubrics shall be decided by the examiners)

Change of experiment is allowed only once and 10% Marks allotted to the procedure part to be made zero. The duration of SEE is 03 hours

Suggested Learning Resources:

www.matworks.com

Sl No.	Course Code	Course Title	National Coordinator
1	22AUD18		NPTEL

	/22AEC18	Real-Time Digital Signal Processing	
2	22AUD18 /22AEC18	Patent Law for Engineers and Scientists	NPTEL
3	22AUD18 /22AEC18	Advanced Engineering Mathematics	NPTEL
4	22AUD18 /22AEC18	Embedded System Design With ARM	NPTEL
5	22AUD18 /22AEC18	Digital Image Processing	NPTEL
6	22AUD18 /22AEC18	Computer Vision	
7	22AUD18 /22AEC18	Digital Speech Processing	NPTEL
8	22AUD18 /22AEC18	Image Signal Processing	NPTEL

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Audit Courses /Ability Enhancement Courses Suggested by BOS (ONLINE courses):

Audit Courses: These are prerequisite courses suggested by the concerned Board of Studies. Ability Enhancement Courses will be suggested by the BoS if prerequisite courses are not required for the programs.

Ability Enhancement Courses:

- These courses are prescribed to help students to enhance their skills in fields connected to the field of specialization as well allied fields that leads to employable skills. Involving in learning such courses are impetus to lifelong learning.
- The courses under this category are online courses published in advance and approved by the concerned Board of Studies.
- Registration to Audit / Ability Enhancement Course shall be done in consultation with the mentor and is compulsory during the concerned semester.
- In case a candidate fails to appear for the proctored examination or fails to pass the selected online course, he/she can register and appear for the same course if offered during the next session or register for a new course offered during that session, in consultation with the mentor.

The Audit Ability Enhancement Course carries no credit and is not counted for vertical progression. However, a pass in such a course is mandatory for the award of the degree.

M.TECH IN SIGNAL PROCESSING (LSP)

Choice Based Credit System (CBCS) and Outcome Based Education (OBE)

(Effective from the academic year 2022-23)

Semester – 2

	Digital Compression				
Course Code		22LSP21	CIE Marks	50	
Teaching Hours	/Week (L:P:SDA)	2:0:2	SEE Marks	50	
Total Hours of F	Pedagogy	25 Hours of Teaching and 10 to 12			
		sessions of Skill Development	Total Marks	100	
		Activities.			
Credits		03	Exam Hours	03	
Course Learning Acqu Equi	g objectives: iire contemporary knowlec p with skills to analyze and	ge in Data Compression and Coding. evaluate different Data Compression a	nd Coding methods		
		Module-1			
Introduction: Compression techniques, Modelling & coding, Distortion criteria, Differential Entropy, Rate Distortion Theory, Coding uniquely decodable codes, Prefix codes, Kraft McMillan Inequality. Quantization: Quantization problem, Uniform Quantizer, Adaptive Quantization, Non-uniform Quantization; Entropy coded Quantization, Vector Quantization, LBG algorithm, Tree structured VQ, Structured VQ.					
Teaching-	eaching- Chalk and Talk / Power Point Presentations.				
Learning					
Process					
Module-2					
Differential Encoding: Basic algorithm, Prediction in DPCM, Adaptive DPCM, Delta Modulation, Speech coding–G.726, Image coding Transform Coding: Transforms–KLT,DCT,DST,DWHT; Quantization and coding of transform coefficients, Application Image compression–JPEG, Application to audio compression					
Teaching-	Chalk and Talk / Powe	r Point Presentations.			
Learning Proce	ss				
Module-3					
Sub-band Coding: Filters, Sub-band coding algorithm, Design of filter banks, Perfect reconstruction using two channel filter banks, M-band QMF filter banks, Poly-phase decomposition, Bit L1, L2 allocation, Speech coding– G.722, Audio coding–MPEG audio, Image compression.					
Teaching-	Chalk and Talk / Power Po	int Presentations.			
Learning					
Process					
	Module-4				

Wavelet Based Compression: Wavelets, Multi resolution analysis & scalingfunction, Implementation using filters, Image compression–EZW, SPIHT, JPEG 2000.

Analysis/Synthesis Schemes: Speech compression–LPC – 10,CELP, MELP.

Video Compression: Motion compensation, Video signal representation, Algorithms for video conferencing & video phones–H.261, H.263, Asymmetric applications–MPEG 4, MPEG 7, Packet video

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	

Module-5

Loss less Coding: Huffman coding, Adaptive Huffman coding, Golomb codes, Rice codes, Tunstall codes, Applications of Huffman coding, Arithmetic coding, Algorithm implementation, L1, L2 Applications of Arithmetic coding, Dictionary techniques–LZ77,LZ78, Applications of LZ78– JBIG, JBIG2, Predictive coding– Prediction with partial match, Burrows Wheeler Transform, Applications– CALIC, JPEG-LS.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 5. Three Unit Tests each of 20 Marks
- 6. Two assignments each of **20 Marks** or **one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 11. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 12. The question paper will have ten full questions carrying equal marks.
- 13. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 14. Each full question will have a sub-question covering all the topics under a module.
- 15. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

1. K.Sayood, "Introduction to Data Compression", Harcourt India Pvt. Ltd. & Morgan Kaufmann Publishers, 1996.

2. N.JayantandP.Noll, "Digital Coding of Waveforms: Principles and Applications to Speech and Video", Prentice Hall, USA, 1984.

- 3. D.Salomon, "Data Compression: The Complete Reference", Springer, 2000.
- 4. Z.LiandM.S.Drew, "Fundamentals of Multimedia", Pearson Education (Asia) Pvt.Ltd., 2004.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• Mini project to compress the signals and image using various algorithms

Course Outcomes

SI.	Description	Blooms Level
No.		
CO1	Able to understand the working principle of compression techniques	L1 L2
CO2	Able to understand and analyze the time domain and frequency domain transformation techniques of compression	L2 L4
CO3	Able to understand and analyze the image compression and video compression	L2 L4
CO4	Able to understand the wavelet based compression techniques	L1 L2

Image Processing & Machine Vision			
Course Code	22LSP22	CIE Marks	50
Teaching Hours/Week (L:P:SDA)	3:2:0	SEE Marks	50
Total Hours of Pedagogy	40 hours Theory + 10-12 Lab slots	Total Marks	100
Credits	04	Exam Hours	04
Course objectives:			

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- Describe the Digital Image fundamentals
- To implement the image enhancement algorithms
- To understand the computer vision techniques

MODULE-1

Introduction and Digital Image Fundamentals

Motivation & Perspective, Applications, Components of Image Processing System, Fundamentals Steps in Image Processing, Image Sampling and Quantization, Some basic relationships like Neighbors, Connectivity, Distance Measures between pixels

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	

MODULE-2

Image Enhancement in the Spatial and Frequency Domain

Image enhancement by point processing, Image enhancement by neighbourhood processing, Basic Gray Level Transformations, Histogram Processing, Enhancement Using Arithmetic and Logic operations, Zooming, Basics of Spatial Filters, Smoothening and Sharpening Spatial Filters, Combining Spatial Enhancement Methods. Introduction to Fourier Transform and the frequency Domain, Smoothing and Sharpening Frequency Domain Filters, Homomorphic Filtering

Teaching-	Chalk and Talk / Power Point Presentations.
Learning Process	

MODULE-3

Image Restoration and Image Compression Model of The Image Degradation / Restoration Process, Noise Models, Restoration in the presence of Noise Only Spatial Filtering, Periodic Noise Reduction by Frequency Domain Filtering, Linear Position-Invariant Degradations, Estimation of Degradation Function, Inverse filtering, Wiener filtering, Constrained Least Square Filtering, Geometric Mean Filter, Geometric Transformations. Data Redundancies, Image Compression models, Elements of Information Theory, Lossless and Lossy compression, Huffman Coding, Shanon-Fano Coding, Arithmetic Coding, Golomb Coding, LZW Coding, Run Length Coding, Loss less predictive Coding, Bit Plane Coding, Image compression standards.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	
	MODULE-4
Image Segment	tation and Morphological Image Processing
Discontinuity b	ased segmentation, similarity based segmentation, Edge linking and boundary detection, 20% Threshold,
Region based S	egmentation Introduction to Morphology, Dilation, Erosion, Some basic Morphological Algorithms
Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	
	MODULE 5
Object Represe	ntation and description and Computer Vision Techniques
Introduction to	Morphology, Some basic Morphological Algorithms, Representation, Boundary Descriptors, Regional
Descriptors. Ch	ain Code. Structural Methods. Review of Computer Vision applications: Fuzzy-Neural algorithms for

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computer visi	on applications	_;
Teaching	Chalk and Talk / Dower Point Precentations	
reaching-	chaik and taik / Fower Foint Fresentations.	
Learning		
Process		

PRACTICAL COMPONENT OF IPCC(May cover all / major modules)

SI.NO	Experiments
1	Write a program in Matlab to compute the histogram of an Image
2	Write a program in Matlab to compute the logic operations on image
3	Write a program in Matlab to find the edge of an image
4	Write a program in Matlab to compute the Huffman coding
5	Write a program in Matlab to compute the bit plane slicing
6	Write a program in Matlab to lossless predictive coding
	Demo experiments for CIE
1	Write a program in Matlab to implement the Fuzzy logic algorithm
2	Write a program in Matlab to implement the neural network algorithm

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together

CIE for the theory component of IPCC

- 4. Two Tests each of 20 Marks
- 5. Two assignments each of 10 Marks/One Skill Development Activity of 20 marks
- 6. Total Marks of two tests and two assignments/one Skill Development Activity added will be CIE for 60 marks, marks scored will be proportionally scaled down to **30 marks**.

CIE for the practical component of IPCC

- On completion of every experiment/program in the laboratory, the students shall be evaluated and marks shall be awarded on the same day. The **15 marks** are for conducting the experiment and preparation of the laboratory record, the other **05 marks shall be for the test** conducted at the end of the semester.
- The CIE marks awarded in the case of the Practical component shall be based on the continuous evaluation of the laboratory report. Each experiment report can be evaluated for 10 marks. Marks of all experiments' write-ups are added and scaled down to 15 marks.
- The laboratory test at the end /after completion of all the experimentsshall be conducted for 50 marks and scaled down to 05 marks.

Scaled-down marks of write-up evaluations and tests added will be CIE marks for the laboratory component of IPCC for **20 marks**.

SEE for IPCC

Theory SEE will be conducted by University as per the scheduled timetable, with common question papers for the course (duration 03 hours)

- 5. The question paper will be set for 100 marks and marks scored will be scaled down proportionately to 50 marks.
- 6. The question paper will have ten questions. Each question is set for 20 marks.
- 7. There will be 2 questions from each module. Each of the two questions under a module (with a maximum of 3 subquestions), **should have a mix of topics** under that module.
- 8. The students have to answer 5 full questions, selecting one full question from each module.

The theory portion of the IPCC shall be for both CIE and SEE, whereas the practical portion will have a CIE component only. Questions mentioned in the SEE paper shall include questions from the practical component).

- The minimum marks to be secured in CIE to appear for SEE shall be the 15 (50% of maximum marks-30) in the theory component and 10 (50% of maximum marks -20) in the practical component. The laboratory component of the IPCC shall be for CIE only. However, in SEE, the questions from the laboratory component shall be included. The maximum of 04/05 questions to be set from the practical component of IPCC, the total marks of all questions should not be more than the 20 marks.
- SEE will be conducted for 100 marks and students shall secure 40% of the maximum marks to qualify in the SEE. Marks secured will be scaled down to 50. (Student has to secure an aggregate of 50% of maximum marks of the course(CIE+SEE)

Suggested Learning Resources:

Books

- 1. Digital Image Processing Rafael C. Gonzalez Pearson Education3rd edition & Richard E. Woods
- 2. Computer Vision: A Modern David A. Forsyth, Prentice Hall Approach Jean Ponce
- 3. Fundamental of Digital Image Processing A.K. Jain PHI
- 4. Digital Image Processing W K Pratt

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Activity Based Learning (Suggested Activities in Class)/ Practical Based learning

• To do mini project in the field of Digital Image Processing and Machine vision

Course Outcomes

SI.	Description	Blooms
No.		Level
CO1	Able to understand the fundamentals of digital image	L2
CO2	Able to understand and analyze the image enhancement in the spatial and frequency domain	L2 L3
CO3	Able to analyze the image restoration and compression in spatial and frequency domain	L6
CO4	Able to understand and analyze image segmentation and morphological image processing in spatial and frequency domain	L2 L4
CO5	Able to analyze the computer vision techniques	L3

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Caura Cada		VLSI in Signal Processing		50
Course Code		22LSP231		50
Teaching Hour	S/WEEK (L:P:SDA)	2:U:2	SEE WIARKS	50
Total Hours of	Pedagogy	25 Hours of Teaching and 10 to 12	Total Marks	100
				100
Credits		03	Exam Hours	03
cicuits		05	Examinours	05
Course Learni	ng obiectives:			
• To ex	plain the VLSI applications			
• To un	derstand the VI SI in signa	Inforessing		
		Module-1		
Introduction T	o DSP Systems: Introduct	ion: representation of DSP algorithms: Bl	lock Diagram signal fl	owgranh data
flow graph de	pendence graph Iteration	Bound: Data flow graph representation	s loop bound and iter	ation bound
longest path n	natrix algorithm, iteration	bound of Multirate data flow graphs.		
0	U ,	5 1		
Teaching-	Chalk and Talk / Power	Point Presentations.		
Learning				
Process				
		Module-2		
Pipelining an	d Parallel Processing: Inti	roduction. Pipelining of FIR Digital Filte	ers. Parallel Processin	g. Pipelining and
Parallel Proce	essing for Low Power Ret	iming: Introduction Definition and Prov	nerties Solving System	n of Inequalities
Petiming Tech			perties, solving syster	n or mequanties,
Retining reti	iniques.			
Teaching-	Chalk and Talk / Pow	ver Point Presentations.		
Learning Proce	255			
		Module-3		
Unfolding: In	troduction an Algorithms	for Unfolding, Properties of Unfolding,	Critical Path, Unfold	ing and Retiming
Application o	f Unfolding. Folding: Intro	duction to Folding Transformation, Regi	ster Minimization Tec	hniques, Register
Minimization	in Folded Architectures, F	olding in Multirate Systems.		
Teaching-	Chalk and Talk / Power	Point Presentations.		
Learning				
Process				
1100033		Module-4		
Suctobe Arel:	tooturo Docigo, latas dus	tion Systelia Array Design Mathedal		rave Coloction of
Systolic Archi	tecture Design: Introduc	tion, Systolic Array Design Methodol	ogy, FIR Systolic Ar	rays, selection of
Scheduling Ve	ector, Matrix Multiplicatio	on and 2D Systolic Array Design, Syst	tolic Design for Spac	e Representations
Containing De	ays.			
Teaching-	Chalk and Talk / Power	Point Presentations.		
Learning				
Process				
	·	Module-5		
Fast Convoluti	on: Introduction, Cook, T	oom Algorithm, Winogard Algorithm, It	erated Convolution, C	Cyclic Convolution,
Design of Fast	Convolution Algorithm by	Inspection.		
Teaching-	Chalk and Talk / Power P	oint Presentations.		
Learning				
Process				

Assessment Details (both CIE and SEE)

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Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of 20 Marks or one Skill Development Activity of 40 marks
- to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources: Books

1. Keshab K. Parhi. VLSI Digital Signal Processing Systems, Wiley-Inter Sciences, 1999

2. Mohammed Ismail, Terri, Fiez, Analog VLSI Signal and Information Processing, McGraw Hill, 1994.

3. Kung. S.Y., H.J. While house T.Kailath, VLSI and Modern singal processing, Prentice Hall, 1985.

4. Jose E. France, YannisTsividls, Design of Analog Digital VLSI Circuits for Telecommunications and Signal Processing' Prentice Hall, 1994.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• To visit telecommunication manufacturing companies

Course Outcomes

SI.	Description	Blooms Level
No.		
CO1	Understand VLSI design methodology for signal processing systems.	L1 L2
CO2	Be familiar with VLSI algorithms and architectures for DSP.	L2 L4
CO3	Be able to implement basic architectures for DSP using CAD tools.	L4

		Nano Electronics		
Course Code		22LSP232	CIE Marks	50
Teaching Hours	/Week (L:P:SDA)	2:0:2	SEE Marks	50
Total Hours of	Pedagogy	25 Hours of Teaching and 10 to 12		
		sessions of Skill Development	Total Marks	100
		Activities.		
Credits		03	Exam Hours	03
Course Learnin • To ana • To und • To und	g objectives: alyze the Moore's law derstand the spectroscopy derstand the fabrication tee	techniques chniques		
Interation.	Overview of approximate	Module-1	u a a in unique fabricati	an and cleatures
Introduction:	Overview of nanoscience a	and engineering. Development milesto	nes in microfabricatio	on and electronic
industry. Moo	res' law and continued mi	niaturization, Classification of Nanostru	ictures, Electronic pro	operties of atoms
and solids: Isc	lated atom, Bonding betw	een atoms, Giant molecular solids, Fre	e electron models a	nd energy bands,
crystalline soli	ds, Periodicity of crystal la	attices, Electronic conduction, effects o	of nanometer length	scale, Fabrication
methods: Top	down processes, Bottom	up processes methods for templating t	he growth of nanom	aterials, ordering
of nano syster	ns			
Teaching-	Chalk and Talk / Power Po	oint Presentations.		
Learning				
Process				
		Module-2		
Characterizatio	on: Classification. Mic	croscopic techniques. Field ion n	nicroscopy, scanning	probe techniques.
diffraction tech	iniques: bulk and surface d	iffraction techniques,		
beam, Reflecto Teaching- Learning Proce	metry, Techniques for prop Chalk and Talk / Powe ss	perty measurement: mechanical, electro	on, magnetic, therma	l properties
		Module-3		
Inorganic semi nanostructures Carbon Nanost	conductor nanostructures :: quantum wells, quantum :ructures: Carbon molecule	: overview of semiconductor physics. C wires, quantum dots, super-lattices, ba s, Carbon Clusters, Carbon Nanotubes,	Quantum confinement and offsets, electronic application of Carbor	t in semiconductor density of states Nanotubes
Teaching-	Chalk and Talk / Power Po	pint Presentations.		
Learning	· ·			
Process				
		Module_4		
Entrication to	chniques: requirements e	f ideal comiconductor onitavial grow	th of quantum wall	c lithography and
etching, cleave dots and wires colloidal quant	d-edge over growth, growt s, Quantum well width flu um dots, self-assembly tecl	the of vicinal substrates, strain induced of uctuations, thermally annealed quant hniques.	dots and wires, electr um wells, semicondu	ostatically induced actor nanocrystals,
Physical proce transport, Inte confined stark optical electrica	esses: modulation doping, or band absorption, intra effect, nonlinear effects, or al and structural	quantum hall effect, resonant tunne band absorption, Light emission pro coherence and dephasing, characteriza	elling, charging effec ocesses, phonon bot ation of semiconducto	ts, ballistic carrier ttleneck, quantum or nanostructures:
Teaching-	Chalk and Talk / Power Pr	pint Presentations.		
Learning Process				

3	2
-	

Module-5 Methods of measuring properties: atomic, crystallography, microscopy, spectroscopy

Applications: Injection lasers, quantum cascade lasers, single-photon sources, biological tagging, optical memories, coulomb blockade devices, photonic structures, QWIPs, NEMS, MEMS

Teaching-
Learning
D

Chalk and Talk / Power Point Presentations.

Process

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of 20 Marks or one Skill Development Activity of 40 marks to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. 'Nanoscale Science and Technology', Ed Robert Kelsall, Ian Hamley, Mark Geoghegan, John Wiley, 2007
- 2. 'Introduction to Nanotechnology', Charles P Poole, Jr, Frank J Owens, John Wiley, Copyright 2006, Reprint 2011.
- 3. 'Hand Book of Nanoscience Engineering and Technology', Ed William A Goddard III, Donald W Brenner, Sergey E. Lyshevski, Gerald J lafrate, CRC Press, 2003

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

To visit nanotechnology laboratories

Course Outcomes

SI.	Description	Blooms Level
No.		

		35
CO1	Able to understand the Moore's law, bottom up process	L1 L2
CO2	Able to understand and analyze the microscopic and spectroscopy techniques	L2 L4
CO3	Able to understand the carbon nanostructures	L2
CO4	Able to understand and analyze the fabrication techniques	L2 L4
CO5	Able to understand the applications of nano electronics	L2

			36	
Deep learning in Signal Processing				
Course Code	22LSP233	CIE Marks	50	
Teaching Hours/Week (L:P:SDA)	2:0:2	SEE Marks	50	
Total Hours of Pedagogy	25 Hours of Teaching and 10 to 12			
	sessions of Skill Development	Total Marks	100	
	Activities.			
Credits	03	Exam Hours	03	

Course Learning objectives:

- To understand the foundations of linear algebra and Artificial Neural Networks ٠
- To acquire the knowledge on Deep Learning Concepts
- To gain knowledge to apply optimization strategies

Module-1

Introduction, Brief Review of concepts from linear Algebra, vector calculus, Types of errors, bias-variance trade-off, overfitting-underfitting, brief review of concepts from optimization, variants of gradient descent, momentum based methods

Chalk and Talk / Power Point Presentations. **Teaching-**Learning Process

Module-2

Basic concepts of artificial neurons, single and multi layer perceptrons, perceptron learning algorithm, its convergence proof, different activation functions, softmax cross entropy loss function, Basic concepts of Linear and Logistic Regression.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	

Module-3

ConvNets: Basic concepts of Convolutional Neural Networks starting from filtering. Convolution and pooling operation and arithmetic's of these. ConvNet Architectures: Discussions on famous convnet architectures - AlexNet, ZFNet, VGG, C3D, GoogLeNet, ResNet, MobileNet-v1, MobileNet-v2, EfficientNet, GhostNet.

Discussion on regularization, Dropout, Batchnorm etc

Process

Teaching-Chalk and Talk / Power Point Presentations. Learning Process

Module-4

Discussion on detection, segmentation problem definition, challenges, Evaluation, Datasets and Localization by regression. Discussion on detection as classification, region proposals, RCNN and YOLO architectures, fully convolutional segmentations, Mask-RCNNs.

Teaching-	Chalk and Talk / Power Point Presentations.			
Learning				
Process				
Module 5				
Deep generative models – Auto encoders, Boltzmann machines, adversarial networks				
Applications in computer vision and speech recognition				
Teaching-	Chalk and Talk / Power Point Presentations.			
Learning				
The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together. **Continuous Internal Evaluation**:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of **20 Marks** or **one Skill Development Activity of 40 marks**
- to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four subquestions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. Linear Algebra and its applications by Gilbert strang ,4th edition
- 2. Deep Learning: An MIT Press Book By Ian Goodfellow and Yoshua Bengio and Aaron Courville
- 3. Neural Networks and Learning Machines, Simon Haykin, 3rd Edition, Pearson Prentice Hall..

Reference text Books

- 1. Duda, R.O., Hart, P.E., and Stork, D.G. Pattern Classi cation. Wiley-Interscience. 2nd Edition. 2001. ■
- 2. Theodoridis, S. and Koutroumbas, K. Pattern Recognition. Edition 4. Academic Press, 2008. Russell, S. and Norvig, N. Arti cial Intelligence: A Modern Approach. Prentice Hall Series in Arti cial Intelligence. 2003.
- 3. Bishop, C. M. Neural Networks for Pattern Recognition. Oxford University Press. 1995.
- 4. Hastie, T., Tibshirani, R. and Friedman, J. The Elements of Statistical Learning. Springer. 2001.
- 5. Koller, D. and Friedman, N. Probabilistic Graphical Models. MIT Press. 2009.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

- http://cs231n.stanford.edu/ CS231n: Convolutional Neural Networks for Visual Recognition
- http://web.stanford.edu/class/cs224n/ CS224n: Natural Language Processing with Deep Learning
- http://rll.berkeley.edu/deeprlcourse/ CS 294: Deep Reinforcement Learning
- http://distill.pub/ Very nice explanations of some DL concepts

Skill Development Activities Suggested

• To perform experiments on basics of Deep learning and Machine learning algorithms

37

		38
SI.	Description	Blooms Level
No.		
CO1	Able to understand the basics of Linear Algebra , fundamentals of Deep learning and optimization techniques.	L2 L3
CO2	Able to understand the concepts of Artificial Neural networks and its implementation using regression models	L2 L3
CO3	Able to understand convolution neural networks and implement the various architectures of convolution neural networks	L2 L3
CO4	Able to understand and analyze the detection and segmentation problems	L1 L2 L4

	39		
Recontigurable Computing	E0		
Course Code ZZLSPZ34 CIE IVIAIRS Teaching Hours (Week (L:P:SDA) 2:0:2 SEE Marks	50		
Teaching Hours of Pedagogy 25 Hours of Teaching and 10 to 12			
sessions of Skill Development Total Marks	100		
Activities.	100		
Credits 03 Exam Hours	03		
 Course Learning objectives: To explain reconfigurable computing system To understand High Level Synthesis for Reconfigurable Devices 			
Module-1			
Introduction: History, Reconfigurable vs Processor based system, RC Architecture.			
Reconfigurable Logic Devices: Field Programmable Gate Array, Coarse Grained Reconfigurable Arrays.			
Reconfigurable Computing System: Parallel Processing on Reconfigurable Computers, A survey of Re	econfigurable		
Computing System			
Teaching- Chalk and Talk / Power Point Presentations.			
Learning			
Process			
Module-2			
Languages and Compilation: Design Cycle, Languages, HDL, High Level Compilation, Low level Design flow	w, Debugging		
Reconfigurable Computing Applications			
Teaching Chalk and Talk / Dawar Daint Dresontations			
Learning Process			
Modulo 2			
Implementation: Integration, FPGA Design flow, Logic Synthesis.			
High Level Synthesis for Reconfigurable Devices: Modelling, Temporal Partitioning Algorithms			
Teaching- Chalk and Talk / Power Point Presentations.			
Learning			
Process			
Module-4			
Partial Reconfiguration Design: Partial Reconfiguration Design, Bitstream Manipulation with JBits, The n	nodular Design		
flow, The Early Access Design Flow, Creating Partially Reconfigurable Designs, Partial Reconfiguration	using Hansel-C		
Designs, Platform Design	0		
Teaching- Chalk and Talk / Power Point Presentations.			
Learning			
Process			
Module-5			
Niodule-5 Signal Processing Applications: Reconfigurable computing for DSP. DSP application building bloc	ks. Examples:		
Signal Processing Applications : Reconfigurable computing for DSP, DSP application building bloc Beamforming, Software Radio, Image and video processing, Local Neighbourhood functions. Convolution	ks, Examples:		
Signal Processing Applications: Reconfigurable computing for DSP, DSP application building bloc Beamforming, Software Radio, Image and video processing, Local Neighbourhood functions, Convolution Programmable Chip: Introduction to SoPC Adaptive Multiprocessing on Chip	ks, Examples: System on a		
Viodule-5 Signal Processing Applications: Reconfigurable computing for DSP, DSP application building bloc Beamforming, Software Radio, Image and video processing, Local Neighbourhood functions, Convolution Programmable Chip: Introduction to SoPC, Adaptive Multiprocessing on Chip Teaching- Chalk and Talk / Power Point Presentations	ks, Examples: System on a		
Wiodule-5 Signal Processing Applications: Reconfigurable computing for DSP, DSP application building bloc Beamforming, Software Radio, Image and video processing, Local Neighbourhood functions, Convolution Programmable Chip: Introduction to SoPC, Adaptive Multiprocessing on Chip Teaching- Chalk and Talk / Power Point Presentations. Learning Chalk and Talk / Power Point Presentations.	ks, Examples: System on a		

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- to attain the COs and POs

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Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. 'Reconfigurable Computing: Accelerating Computation with Field-Programmable Gate Arrays', M. Gokhale and P. Graham, Springer, ISBN: 978-0-387-26105-8, 2005.
- 2. 'Introduction to Reconfigurable Computing: Architectures, Algorithms and Applications', C. Bobda, Springer, ISBN: 978-1-4020-6088-5, 2007.
- 3. 'Practical FPGA Programming in C', D. Pellerin and S. Thibault, Prentice-Hall, 2005.
- 4. 'FPGA Based System Design', W. Wolf, Prentice-Hall, 2004.
- 5. 'Rapid System Prototyping with FPGAs: Accelerating the Design Process', R. Cofer and B. Harding, Newnes, 2005.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• To perform experiments on signal processing applications using Matlab / Labview software

SI.	Description	Blooms Level
No.		
CO1	Able to understand the reconfigurable systems	L2
CO2	Able to understand and analyze the partial reconfigurable design	L2 L4
CO3	Able to analyze the signal processing techniques	L4

				42
	Art	tificial Neural Network and Applications		
Course Code		22LSP235	CIE Marks	50
Teaching Hours	/Week (L:P:SDA)	2:0:2	SEE Marks	50
Total Hours of Pedagogy		25 Hours of Teaching and 10 to 12		
		sessions of Skill Development	Total Marks	100
		Activities.		
Credits		03	Exam Hours	03
Course Learnin • To des • To exp	g objectives: cribe the artificial neural m lain the supervised learning	odel g and support vector machine		
		Module-1		
Introduction: Biological Neur Architecture: Feed forward a Problem, Multil Learning: Learning Algorit Algorithm, Perc	on- Artificial Neural Model- nd Feedback, Convex Sets, layer Networks. thms, Error correction and ceptron Convergence Theor	Types of activation functions- Convex Hull and Linear Separability, No Gradient Descent Rules, Learning objec em.	n-Linear Separable I tive of TLNs, Percep	Problem. XOR tron Learning
Teaching- Learning	Chalk and Talk / Power Po	int Presentations.		
Process				
		Module-2		
Perceptron lear Search, JL-LMS Architecture, Ba Teaching-	Supervised Learning: Perceptron learning and Non Separable sets, aLeast Mean Square Learning, MSE Error surface, Steepest Descent Search, JL-LMS approximate to gradient descent, Application of LMS to Noise Cancelling, Multi-layered Network Architecture, Back propagation Learning Algorithm, Practical consideration of BP algorithm. Teaching. Chalk and Talk / Power Point Presentations			
Learning Proce	ss			
	·	Module-3		
Support Vector Learning from E Radial Basis Fur recognition.	Support Vector Machines and Radial Basis Function: Learning from Examples, Statistical Learning Theory, Support Vector Machines, SVM application to Image Classification, Radial Basis Function Regularization theory, Generalized RBF Networks, Learning in RBFNs, RBF application to face recognition.			
Teaching- Learning Process	Chalk and Talk / Power Po	vint Presentations.		
-		Module-4		
Attractor Norm	al Networks			
Attractor Neura Associative Lea Hopfield Netwo Associative Me	Attractor Neural Networks: Associative Learning Attractor Associative Memory, Linear Associative memory, Hopfield Network, application of Hopfield Network, Brain State in a Box neural Network, Simulated Annealing, Boltzmann Machine, Bidirectional Associative Memory.			
Teaching- Learning Process	Chalk and Talk / Power Po	vint Presentations.		
		Module-5		

Self-organization Feature Map:

Maximal Eigenvector Filtering, Extracting Principal Components, Generalized Learning Laws, Vector Quantization, Self organization Feature Maps, Application of SOM, Growing Neural Gas.

Teaching-Learning Process

Chalk and Talk / Power Point Presentations.

ing

Assessment Details (both CIE and SEE)

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Semester End Examination:

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- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. Neural Networks A Classroom Approach- Satish Kumar, McGraw Hill Education (India) Pvt. Ltd, Second Edition.
- 2. Introduction to Artificial Neural Systems-J.M. Zurada, Jaico Publications 1994.
- 3. Artificial Neural Networks-B. Yegnanarayana, PHI, New Delhi 1998.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

Perform experiments on implementing Neural network applications using Matlab software

SI.	Description	Blooms Level
No.		
C01	Able to understand the biological neuron and artificial neural network model	L1 L2
CO2	Able to understand and analyze the support vector machine and radial basis function	L2 L4
CO3	Able to understand the supervised learning algorithms	L2
CO4	Able to understand and analyze the associative memory, brain state in box neural network	L2 L4

Course Code		Cryptography and Network Security		
		22LSP241	CIE Marks	50
Teaching Hours/Week	: (L:P:SDA)	2:0:2	SEE Marks	50
Total Hours of Pedago	gy	25 Hours of Teaching and 10 to 12		
		sessions of Skill Development	Total Marks	100
		Activities.		
Credits		03	Exam Hours	03
 Course Learning objectives: To explain the crypto systems To describe IP security 				
		Module-1		
Foundations: Termino	logy, Steganography	, substitution ciphers and transposition	s ciphers, Simple X	OR, One-Time Pads,
Computer Algorithms	(Text 2: Chapter 1: S	ection 1.1 to 1.6).		, , ,
SYMMETRIC CIPHERS	: Traditional Block	Cipher structure, Data encryption star	ndard (DES), The A	ES Cipher. (Text 1:
Chapter 2: Section2.1,	2.2, Chapter 4).			
Teaching- Chalk	and Talk / Power Po	pint Presentations.		
Learning				
Process				
		Module-2		
Introduction to mod	lular arithmetic, Pr	ime Numbers, Fermat's and Euler's	theorem, primalit	y testing, Chinese
Remainder theorem, o	discrete logarithm.			
Principles of Public-Ke	ey Cryptosystems, T	he RSA algorithm, Diffie - Hellman Ke	y Exchange, Elliptio	c Curve Arithmetic,
Elliptic Curve Cryptogr	aphy			
Teaching Chalk and Talk / Power Point Presentations				
Teaching- Ch	halk and Talk / Powe	r Point Presentations.		
Teaching- Ch Learning Process	halk and Talk / Powe	r Point Presentations.		
Teaching- Ch Learning Process	halk and Talk / Powe	r Point Presentations.		
Teaching- Ch Learning Process Pseudo-Random-Sequ	alk and Talk / Powe	r Point Presentations. Module-3 Ind Stream Ciphers: Linear Congruent	ial Generators. Lin	ear Feedback Shift
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Teaching- CH Learning Process CH Pseudo-Random-Sequ Registers, Design and Additive generators, CH Additive generators CH Teaching- Ch-alk Learning Process One-Way Hash Funct Ch	analk and Talk / Powe Jence Generators a analysis of stream ci Sifford, Algorithm M, and Talk / Power Pc	r Point Presentations. Module-3 and Stream Ciphers: Linear Congruent phers, Stream ciphers using LFSRs, A5, I PKZIP oint Presentations. Module-4 Snefru, N-Hash, MD4, MD5, Secure F	ial Generators, Lin Hughes XPD/KPD, N	ear Feedback Shift anoteq, Rambutan, Maj, One way hash
Teaching- Ch Learning Process Ch Pseudo-Random-Sequ Registers, Design and Additive generators, Ch Teaching- Chalk Learning Chalk Process Chalk One-Way Hash Funct Funct functions using symm Ch	tions: Background, etric block algorithm	r Point Presentations. Module-3 and Stream Ciphers: Linear Congruent phers, Stream ciphers using LFSRs, A5, I PKZIP bint Presentations. Module-4 Snefru, N-Hash, MD4, MD5, Secure H ns, Using public key algorithms, Choosin	ial Generators, Lin Hughes XPD/KPD, N Hash Algorithm [SH ng a one-way hash	ear Feedback Shift anoteq, Rambutan, IA], One way hash functions, Message
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Teaching-	Chalk and Talk / Power Point Presentations.				
Learning					
Process	ataile (hath CIF and SFF)				
The weighters	etails (both Cle and See)				
ne weightage	The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum				
	or the CIE is 50% of the maximum marks, winnmum passing marks in SEE is 40% of the maximum marks of				
SEE. A studer	It shall be deemed to have satisfied the academic requirements and earned the credits allotted to each				
subject/ cours	e if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous				
Internal Evalua	ation) and SEE (Semester End Examination) taken together.				
Continuous In	ternal Evaluation:				
1. Three	Unit Tests each of 20 Marks				
2. Two assig	gnments each of 20 Marks or one Skill Development Activity of 40 marks				
to attain	the COs and POs				
The sum of thr	ee tests, two assignments/skill Development Activities, will be scaled down to 50 marks				
CIE methods	question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome				
defined for the	e course.				
Semester End	Examination:				
3. The SEE of	question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.				
4. The ques	tion paper will have ten full questions carrying equal marks.				
each mor					
6. Each full	question will have a sub-question covering all the topics under a module.				
7. The stude	ents will have to answer five full questions, selecting one full question from each module				
Suggested Lea	rning Resources:				
Books					
1. 'Cry	ptography and Network Security Principles and Practice', William Stallings, Pearson Education Inc., ISBN:				
9/8- 2 ' App	93325-18/7-3, 6° Edition, 2014 lied Cryptography Protocols, Algorithms, and Source code in C ² , Bruce Schneier, Wiley Publications ISBN:				
2. App 9971	-51348-X. 2 nd Edition				
3. 'Cry	ptography and Network Security', Behrouz A. Forouzan, TMH, 2007				
4. Cryp	otography and Network Security', Atul Kahate, TMH, 2003				
Web links and	Video Lectures (e-Resources):				
https://www.	mooc.org/				
nttps://oninecourses.nptei.ac.in/					

Skill Development Activities Suggested

• To do mini project group wise on any cryptography applications

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SI.	Description	Blooms Level
No.		
CO1	Able to understand the fundamentals of data encryption standard	L2
CO2	Able to understand and analyze the E – mail security and web security	L2 L4
CO3	Able to understand and analyze the pseudo – random sequence generation and stream ciphers	L2 L4

				48
		Cyber Security		50
Course Code		22157242		50
Teaching Hours	/Week (L:P:SDA)	2:0:2	SEE Marks	50
Total Hours of I	Pedagogy	25 Hours of Teaching and 10 to 12 sessions of Skill Development Activities.	Total Marks	100
Credits		03	Exam Hours	03
Course Learnin To und To exp	g objectives: derstand the cyber crime a lain the phishing and ident	nd laws and computer forensics tity theft Module-1		
Introduction	to Cuborcrimo and Laws	Wodule-1		
Introduction, Cybercriminal Attacks, Cybe Teaching- Learning Process	Cybercrime: Definition a s? Classifications of Cybe rcafé and Cybercrimes, Bo Chalk and Talk / Power Po	nd Origins of the word, Cybercrime rcrimes. How Criminals Plan Them – I otnets, Attack Vector, The Indian IT AC oint Presentations.	and information S ntroduction, How C T 2000 and amend	ecurity, Who are riminals Plan the ments.
		Module-2		
Tools and Me	thods used in Cybercrime	9		
Spyware, Vir Buffer Overflo Teaching- Learning Proce	Chalk and Talk / Powe	and backdoors, Steganography, DC	DS and DDOS atta	ick, SQLinjection,
_	ł	Module-3		
Phishing and Introduction, Theft – PII, Ty forensics and Teaching-	Identity Theft Phishing – Methods of P (pes of Identity Theft, Te Digital Evidence, Digital Fo Chalk and Talk / Power Po	Phishing, Phishing Techniques, Phishir chniques of ID Theft. Digital Forensic prensics Life Cycle. point Presentations.	ng Toolkits andSpy I s Science, Need for	Phishing. Identity Computer Cyber
Learning				
Process				
		Module-4		
Understandir Science, The Digital Foren Investigation, Understandir Computer Computer Forensics Aud	ng Computer Forensics: I Need for Computer Fore sics Life Cycle, Chain of G Setting up a Computer F ngtheRequirements,Comp Forensics, Forensics ensicsfromCompliancePers diting, Anti forensics.	Introduction, Historical Background of ensics, Cyber forensics and Digital Evi Custody Concept, Network Forensics, Forensics Laboratory: PuterForensicsandSteganography,Rele and Social Networking Sites: pective,ChallengesinComputerForensics	of Cyber forensics, dence, Forensics Ai Approaching a Co vanceoftheOSI7Lay The Security/F s,SpecialToolsandTec	Digital Forensics nalysis of E- Mail, mputer Forensics er Model to Privacy Threats, chniques,
Teaching- Learning Process	Chalk and Talk / Power Po	oint Presentations.		
		Module-5		

Introduction to Security Policies andCyberLaws:NeedforAnInformationSecurityPolicy,Information SecurityStandards– Iso,IntroducingVariousSecurityPoliciesandTheirReviewProcess,IntroductiontoIndianCyberLaw,ObjectiveandScopeof the IT Act,2000,IntellectualPropertyIssues,Overview of Intellectual -Property - Related Legislation in India, Patent, Copyright, Law Related to Semiconductor Layout and Design,Software License

Teaching-
LearningChalk and Talk / Power Point Presentations.

Process

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. Anti-Hacker Tool Kit (Indian Edition) by Mike Shema, Publication McGraw Hill.
- 2. Cyber Security Understanding Cyber Crimes, Computer Forensics and Legal Perspectives by Nina Godbole and SunitBelpure, Publication Wiley.
- 3. Introduction to information securityand cyberlaws Surya Prakash Tripathi, RitendraGoyal, PraveenKumarShukla Dreamtech Press 2015
- 4. Marjie T. Britz Computer Forensics and Cyber Crime: An Introduction Pearson
- 5. Chwan-Hwa (John) Wu,J. David Irwin Introduction to Computer Networks and Cyber securityCRCPress
- 6. Bill Nelson, Amelia Phillips, Christopher Steuart Guide to Computer Forensics and InvestigationsCengage Learning

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• To do mini project group wise on any cyber security applications

Course Outcomes

At the end of the course the student will be able to :

SI.	Description	Blooms Level
No.		
CO1	Able to understand the cyber crime and cyber laws	L2
CO2	Able to understand and analyze the tools and methods used in cyber crime	L2 L4
CO3	Able to understand and analyze the phishing and identity, computer forensics	L2 L4

50

				51
		Micro Electro Mechanical Systems		
Course Code	h.,	22LSP243	CIE Marks	50
Teaching Hours	s/Week (L:P:SDA)	2:0:2	SEE Marks	50
Total Hours of I	Pedagogy	25 Hours of Teaching and 10 to 12 sessions of Skill Development Activities.	Total Marks	100
Credits		03	Exam Hours	03
Course Learnin • To exp • To und • To and	g objectives: Jlain MEMS derstand the working princ alyze the scaling laws in mi	ciples of micro systems niaturization		
		Module-1		
Overview of M of Microfabrica Applications an Teaching- Learning	EMS and Microsystems: I ation, Microsystems and Id Markets. Chalk and Talk / Power P	MEMS and Micro system, Typical MEMS Microelectronics, Multidisciplinary Nat oint Presentations.	5 and Microsystems F ture of Microsystem	Products, Evolution s, Miniaturization.
Process				
		Module-2		
accelerometers Engineering Sc Ionization, Mol Plasma Physics, Teaching- Learning Proce Engineering M Thermo mecha Thermo mecha Teaching- Learning Process	s, Micro fluidics. ience for Microsystems lecular Theory of Matter a , Electrochemistry Chalk and Talk / Power echanics for Microsystem nics, Fracture Mechanics, Chalk and Talk / Power P	Design and Fabrication: Introduction, and Inter-molecular Forces, Doping of s er Point Presentations. Module-3 ns Design: Introduction, Static Bending Thin Film Mechanics, Overview on Finite Point Presentations.	Atomic Structure of Semiconductors, The g of Thin Plates, Me e Element Stress Anal	Matters, lons and Diffusion Process, chanical Vibration, ysis.
		Module-4		
Scaling Laws in	Miniaturization			
Introduction, S Electromagneti	Scaling in Geometry, Sc c Forces, Scaling in Electric	aling in Rigid-Body Dynamics, Scalin city, Scaling in Fluid Mechanics, Scaling i	ng in Electrostatic I n Heat Transfer.	Forces, Scaling of
Teaching- Learning Process	Chalk and Talk / Power P	oint Presentations.		
		Module-5		
Overview of M Summary on M	icro-manufacturing: Intro- icro manufacturing.	duction, Bulk Micro-manufacturing, Surf	face Micromachining,	The LIGA Process,
Microsystem I Method.	Design: Introduction, Desi	gn Considerations, Process Design, Me	echanical Design, Us	ing Finite Element

Learning Process

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of **20 Marks**
- 2. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- **1.** 'MEMS and Micro systems: Design, Manufacture and Nanoscale Engineering', Tai-Ran Hsu, John Wiley & Sons, ISBN: 978-0470-08301-7, 2nd Edition, 2008
- 2. 'Micro and Nano Fabrication: Tools and Processes', Hans H. Gatzen, Volker Saile, Jurg Leuthold, Springer, 2015
- **3.** 'Micro Electro Mechanical Systems (MEMS)', Dilip Kumar Bhattacharya, Brajesh Kumar Kaushik, Cengage Learning.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• To set up MEMS model

SI.	Description	Blooms Level
No.		
CO1	Able to understand the MEMS and Micro systems	L1 L2
CO2	Able to understand and analyze the mechanics for Microsystems design	L1 L2 L3
CO3	Able to analyze the scaling laws in miniaturization	L4

[54	
		Detection and Estimation			
Course Code	h h /)	22LSP244	CIE Marks	50	
Teaching Hour	s/Week (L:P:SDA)	2:0:2	SEE Marks	50	
Total Hours of	Pedagogy	25 Hours of Teaching and 10 to 12			
		sessions of Skill Development	Total Marks	100	
		Activities.			
Credits		03	Exam Hours	03	
Course Learnii • To ex • To un	ng objectives: plain the detection estimati derstand the linear estimat	on theory ion			
		Module-1			
Classical Dete	ection and Estimation The	ory: Introduction, simple binary hypoth	nesis tests, M Hypoth	neses, estimation	
theory, comp	osite hypotheses, general G	aussian problem, performance bounds	and approximations		
Teaching-	Chalk and Talk / Power Po	pint Presentations.			
Learning					
Process					
Module-2					
Representations of Random Processes: Introduction, orthogonal representations, random process characterization,					
homogenous integral equations and Eigen functions, periodic processes, spectral decomposition, vector random					
processes					
Teaching- Learning Proce	Chalk and Talk / Powe	r Point Presentations.			
Module-3					
Detection of Signals & Estimation of Signal Parameters: Introduction, detection and estimation in white Gaussian					
noise, detecti	on and estimation in nonv	vhite Gaussian noise, signals with unw	vanted parameters, m	nultiple channels	
and multiple	parameter estimation	. 2	•		
Teaching-	Chalk and Talk / Power Po	oint Presentations			
Learning					
Brocoss					
FIULESS		Na dula d			
.	0 11 11 1				
Estimation of	Continuous Waveforms:	ntroduction, derivation of estimator e	equations, lower bou	ind on the mean-	
square estimat	tion error, multidimensiona	I waveform estimation, non-random wa	avetorm estimation		
Teaching-	Chalk and Talk / Power Po	pint Presentations.			
Learning					
Process					
		Module-5			
Linear Estimat	ion: Properties of optimum	n processors, realizable linear filters, Ka	alman-Bucy filters, fu	ndamental role of	
optimum linea	r filters				
Teaching-	Chalk and Talk / Power Po	nt Presentations.			
Learning					
Process					

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of 20 Marks or one Skill Development Activity of 40 marks
- to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. 'Detection, Estimation, and Modulation Theory', Part I, Harry L. Van Trees, John Wiley & Sons, USA, 2001.
- 2. 'Random Signals: Detection, Estimation and Data Analysis', K Sam Shanmugam, Arthur M Breipohl, John Wiley & Sons, 1998.
 - 3. 'Introduction to Statistical Signal Processing with Applications', M.D. Srinath, P.K. Rajasekaran and R. Viswanathan, Pearson Education (Asia) Pvt. Ltd. /Prentice Hall of India, 2003.
- 4. 'Fundamentals of Statistical Signal Processing,' Volume I: 'Estimation Theory', Steven M. Kay, Prentice Hall, USA, 1998.
- 5. 'Fundamentals of Statistical Signal Processing', Volume II: 'Detection Theory,' Steven M. Kay, Prentice Hall, USA, 1998.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

Perform experiment on various detection and estimation algorithms

SI.	Description	Blooms Level
No.		
CO1	Able to understand and analyze the classical detection and estimation theory	L1 L2 L4
CO2	Able to understand the estimation of continuous waveforms	L1L2
CO3	Able to understand linear estimation	L1 L2

Error Control Coding Course Code 22L5P245 CIE Marks 50 Teaching Hours/Week (L:P:SDA) 2:0:2 SEE Marks 50 Total Hours of Pedagogy 25 Hours of Teaching and 10 to 12 sessions of Skill Development Activities. Total Marks 100 Credits 03 Exam Hours 03 Course Learning objectives: 03 Exam Hours 03 Course Learning objectives: • To solve the problems linear block codes, BCH codes, cyclic codes, convolution codes Information theory: Introduction, Entropy, Source coding theorem, discrete memoryless channel, Mutual Information, Channel Capacity Channel coding theorem Introduction to algebra: Groups, Fields, binary field arithmetic, Construction of Galois Fields GF (2 ^m) and its properties, (Only statements of theorems without proof) Computation using Galois field GF (2 ^m) arithmetic, Vector spaces and Matrices Module-2 Learning Chalk and Talk / Power Point Presentations. Learning Chalk and Talk / Power Point Presentations. Error detecting and error correcting capabilities, Standard array and syndrome decoding, Single Parity Check Codes (SPC), Repetition codes, Self dual codes, Hamming codes, Reed-Muller codes. Product codes and Interleaved codes Chalk and Talk / Power Point Presentations.	Course Code Teaching Hours/We Total Hours of Peda	ek (L:P:SDA) gogy	Error Control Coding 22LSP245 2:0:2	CIE Marks SEE Marks	50 50	
Course Code 221SP245 CIE Marks 50 Teaching Hours/Week (L:P:SDA) 2:0:2 SEE Marks 50 Total Hours of Pedagogy 25 Hours of Teaching and 10 to 12 sessions of Skill Development Activities. Total Marks 100 Credits 03 Exam Hours 03 Course Learning objectives: 03 Exam Hours 03 To understand the Galois fields construction • To solve the problems linear block codes, BCH codes, cyclic codes, convolution codes Information theory: Introduction, Entropy, Source coding theorem, discrete memoryless channel, Mutual Information, Channel Capacity Channel coding theorem Information to algebra: Groups, Fields, binary field arithmetic, Construction of Galois Fields GF (2 ^m) and its properties, (Only statements of theorems without proof) Computation using Galois field GF (2 ^m) arithmetic, Vector spaces and Matrices Teaching- Learning Chalk and Talk / Power Point Presentations. Process Module-2 Linear block codes: Generator and parity check matrices, Encoding circuits, Syndrome and error detection, Minimum distance considerations, Error detecting and error correcting capabilities, Standard array and syndrome decoding, Single Parity Check Codes (SPC), Repetition codes, Self dual codes, Hamming codes, Reed-Muller codes. Product codes and Interleaved codes Teaching- Learning Process Chalk and Talk / P	Course Code Teaching Hours/We Total Hours of Peda	ek (L:P:SDA) gogy	22LSP245 2:0:2	CIE Marks SEE Marks	50 50	
Teaching Hours/Week (L:P:SDA) 2:0:2 SEE Marks 50 Total Hours of Pedagogy 25 Hours of Teaching and 10 to 12 sessions of Skill Development Activities. Total Marks 100 Credits 03 Exam Hours 03 Course Learning objectives: 03 Exam Hours 03 Course Learning objectives: • To understand the Galois fields construction • • To solve the problems linear block codes, BCH codes, cyclic codes, convolution codes • Module-1 Information theory: Introduction, Entropy, Source coding theorem, discrete memoryless channel, Mutual Information, Channel Capacity Channel coding theorem • • Introduction to algebra: Groups, Fields, binary field arithmetic, Construction of Galois Fields GF (2 ^m) and its properties, (Only statements of theorems without proof) Computation using Galois field GF (2 ^m) and its properties, Vector spaces and Matrices Teaching- Learning Chalk and Talk / Power Point Presentations. • • Inear block codes: Generator and parity check matrices, Encoding circuits, Syndrome and error detection, Minimum distance considerations, Error detecting and error correcting capabilities, Standard array and syndrome decoding, Single Parity Check Codes (SPC), Repetition codes, Self dual codes, Hamming codes, Reed-Muller codes. Product codes and Interleaved codes • • Teaching- Learning Process	Teaching Hours/We Total Hours of Peda	ek (L:P:SDA) gogy	2:0:2	SEE Marks	50	
Total Hours of Pedagogy 25 Hours of Teaching and 10 to 12 sessions of Skill Development Activities. Total Marks 100 Credits 03 Exam Hours 03 Course Learning objectives: • 03 Exam Hours 03 • To understand the Galois fields construction • • 03 • To solve the problems linear block codes, BCH codes, cyclic codes, convolution codes • • Information theory: Introduction, Entropy, Source coding theorem, discrete memoryless channel, Mutual Information, Channel Capacity Channel coding theorem • • Introduction to algebra: Groups, Fields, binary field arithmetic, Construction of Galois Fields GF (2 ^m) and its properties, (Only statements of theorems without proof) Computation using Galois field GF (2 ^m) and its properties, Vector spaces and Matrices Teaching- Learning Chalk and Talk / Power Point Presentations. • • Linear block codes: Generator and parity check matrices, Encoding circuits, Syndrome and error detection, Minimum distance considerations, Error detecting and error correcting capabilities, Standard array and syndrome decoding, Single Parity Check Codes (SPC), Repetition codes, Self dual codes, Hamming codes, Reed-Muller codes. Product codes and Interleaved codes • • Teaching- Learning Process Chalk and Talk / Power Point Presentations. • • • •	Total Hours of Peda	gogy				
Credits 03 Exam Hours 03 Course Learning objectives: • To understand the Galois fields construction • • To solve the problems linear block codes, BCH codes, cyclic codes, convolution codes Information theory: Introduction, Entropy, Source coding theorem, discrete memoryless channel, Mutual Information, Channel Capacity Channel coding theorem Module-1 Introduction to algebra: Groups, Fields, binary field arithmetic, Construction of Galois Fields GF (2 ^m) and its properties, (Only statements of theorems without proof) Computation using Galois field GF (2 ^m) arithmetic, Vector spaces and Matrices Teaching-Learning Chalk and Talk / Power Point Presentations. Linear block codes: Generator and parity check matrices, Encoding circuits, Syndrome and error detection, Minimum distance considerations, Error detecting and error correcting capabilities, Standard array and syndrome decoding, Single Parity Check Codes (SPC), Repetition codes, Self dual codes, Hamming codes, Reed-Muller codes. Product codes and Interleaved codes Teaching-Learning Process Chalk and Talk / Power Point Presentations.	sessions of Skill Development Total Marks 100 Activities. 03 Exam Hours 03					
Course Learning objectives: • To understand the Galois fields construction • To solve the problems linear block codes, BCH codes, cyclic codes, convolution codes Module-1 Information theory: Introduction, Entropy, Source coding theorem, discrete memoryless channel, Mutual Information, Channel Capacity Channel coding theorem Introduction to algebra: Groups, Fields, binary field arithmetic, Construction of Galois Fields GF (2 ^m) and its properties, (Only statements of theorems without proof) Computation using Galois field GF (2 ^m) arithmetic, Vector spaces and Matrices Module-2 Learning Process Module-2 Linear block codes: Generator and parity check matrices, Encoding circuits, Syndrome and error detection, Minimum distance considerations, Error detecting and error correcting capabilities, Standard array and syndrome decoding, Single Parity Check Codes (SPC), Repetition codes, Self dual codes, Hamming codes, Reed-Muller codes. Product codes and Interleaved codes Teaching- Laching- Chalk and Talk / Power Point Presentations.	Credits 03 Exam Hours 03					
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and Interleaved codes Teaching- Learning Process	Single Parity Check Codes (SPC), Repetition codes, Self dual codes, Hamming codes, Reed-Muller codes. Product codes					
Teaching- Chalk and Talk / Power Point Presentations. Learning Process	and Interleaved cod	les				
Teaching- Chalk and Talk / Power Point Presentations. Learning Process Chalk and Talk / Power Point Presentations.						
Learning Process	Teaching-	Chalk and Talk / Powe	r Point Presentations.			
	Learning Process					
Module-3	I		Module-3			
Cyclic codes: Introduction, Generator and parity check polynomials. Encoding of cyclic codes. Syndrome computing and						
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error detection, Decoding of cyclic codes, Error trapping Decoding, Cyclic namming codes, Shortened Cyclic codes	error detection, Det	coung of cyclic codes,	Error trapping Decoding, Cyclic hammin	ng codes, shortened c	yclic codes	
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	Learning					
Learning	Process					
Learning Process			Module-4			
Learning Process Module-4	BCH codes: Binary	primitive BCH codes, D	ecoding procedures, Implementation	of Galois field arithm	etic. Primitive BCH	
Learning Process Module-4 BCH codes: Binary primitive BCH codes, Decoding procedures, Implementation of Galois field arithmetic. Primitive BCH	codes over GF (q), F	leed -Solomon codes				
Learning Process Module-4 BCH codes: Binary primitive BCH codes, Decoding procedures, Implementation of Galois field arithmetic. Primitive BCH codes over GF (q), Reed -Solomon codes	Majority Logic deco	dable codes: One -ste	p majority logic decoding, Multiple-ste	p majority logic		
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Learning Process Module-4 BCH codes: Binary primitive BCH codes, Decoding procedures, Implementation of Galois field arithmetic. Primitive BCH codes over GF (q), Reed -Solomon codes Majority Logic decodable codes: One -step majority logic decoding, Multiple-step majority logic	Teaching- Cha	alk and Talk / Power Po	bint Presentations			
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Learning Module-4 BCH codes: Binary primitive BCH codes, Decoding procedures, Implementation of Galois field arithmetic. Primitive BCH codes over GF (q), Reed -Solomon codes Majority Logic decodable codes: One -step majority logic decoding, Multiple-step majority logic Teaching- Chalk and Talk / Power Point Presentations. Learning Module-5	Convolution codes	Encoding of convolution	onal codes: Systematic and Non system	natic Convolutional Co	des. Feed forward	
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The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of 20 Marks or one Skill Development Activity of 40 marks
- to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

Textbooks:

- 1. 'Digital Communication systems', Simon Haykin, Wiley India Private. Ltd, ISBN 978-81-265-4231-4, First edition, 2014
- 2. 'Error control coding', Shu Lin and Daniel J. Costello. Jr, Pearson, Prentice Hall, 2nd edition, 2004
- 3. 'Theory and practice of error control codes', Blahut. R. E, Addison Wesley, 1984
- 4. 'Introduction to Error control coding', Salvatore Gravano, Oxford University Press, 2007
- 5. 'Digital Communications Fundamentals and Applications', Bernard Sklar, Pearson Education (Asia) Pvt. Ltd., 2nd Edition, 2001

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• Perform experiments to implement linear block codes, BCH codes, Cyclic codes and convolutional codes usin Matlab software

SI.	Description	Blooms Level
No.		
CO1	Able to understand and solve the problems of algebra	L1 L2 L4
CO2	Able to understand and solving problems of linear block codes and cyclic codes	L1 L2 L3
CO3	Able to understand and analyze the BCH and convolution codes	L2 L4

	MINI PROJECT WITH SEMINAR		•••
Course Code	22LSP25	CIE Marks	100
Teaching Hours/Week (L:P:SDA)	(0:4:2)	SEE Marks	-
Total Hours of Pedagogy	30 hours Practical+10 Hours SDA	Total Marks	100
Credits	03	Exam Hours	-

6N

Course Learning objectives: This course will enable students to:

- To support independent learning and innovative attitude.
- To guide to select and utilize adequate information from varied resources upholding ethics.
- To guide to organize the work in the appropriate manner and present information (acknowledging thesources) clearly.
- To develop interactive, communication, organisation, time management, and presentation skills.
- To impart flexibility and adaptability.
- To inspire independent and team working.
- To expand intellectual capacity, credibility, judgement, intuition.
- To adhere to punctuality, setting and meeting deadlines.
- To instil responsibilities to oneself and others.
- To train students to present the topic of project work in a seminar without any fear, face audienceconfidently, enhance communication skill, involve in group discussion to present and exchange ideas.

Mini Project With Seminar: This may be hands-on practice, survey report, data collection and analysis, coding, mobile app development, field visit and report preparation, modelling of system, simulation, analysing and authenticating, case studies, etc. Each student of the project batch shall involve in carrying out the project work jointly in constant consultation with internal guide, co-guide, and external guide and prepare the project report as per the norms avoiding plagiarism.

Course outcomes: At the end of the course the student will be able to:

- 1. Present the mini-project and be able to defend it.
- 2. Make links across different areas of knowledge and to generate, develop and evaluate ideas and information so as to apply these skills to the project task.
- 3. Habituated to critical thinking and use problem solving skills.

4. Communicate effectively and to present ideas clearly and coherently in both the written and oral forms.5. Work in a team to achieve common goal.

6.Learn on their own, reflect on their learning and take appropriate actions to improve it.

Continuous Internal Evaluation

CIE marks shall be awarded by a committee comprising of HoD as Chairman, Guide/co-guide, if any, and a senior faculty of the department. Students can present the seminar based on the completed mini-project. Participation in the seminar by all postgraduate students of the program shall be mandatory.

The CIE marks awarded for Mini-Project work and Seminar shall be based on the evaluation of Mini Project work and Report, Presentation skill and performance in Question and Answer session in the ratio **50:25:25**. Mini-Project with Seminar shall be considered as a head of passing and shall be considered for vertical progression as well as for the award of degree. Those, who do not take-up/complete the Mini Project and Seminar shall be declared as fail in that course and have to complete the same during the subsequent semester.

There is no SEE for this course. **RBT Level: L3, L4, L5, L6**

	61				
Image Processing Laboratory					
Course	Code	22LSPL26	CIE Marks	50	
Teachin	g Hours/Week (L:T:P: S)	1:2:0	SEE Marks	50	
Credits		02	Exam Hours	03	
Course	objectives:				
•	To understand the Matlab comma	nds to compose code for complex	algorithm		
•	To implement Image processing algorithms				
SI.NO	Experiments				
1	Study the effects of				
	a) Boolean operations on binary images b) Quantization of gray level images				
2	Study the effects of Contrast enhancement using a) Histogram equalization b) Histogram stretching.				
3	Using connected component labelling algorithms, express Pixel				
	neighbourhood relationships in terms of a graph				
4	Create a binary image from imag	e by replacing all values above a d	letermined threshold level	using	
	a) global thresholding				
	b) adaptive thresholding technique				
5	Transform an image given using Spatial Transformation				
6	Study how to compute forward 2D FFT and a) Find the log magnitude & phase and the inverse 2D FFT of an image. b) Compute the forward 2D FFT of the filter kernel. c) Design a Laplacian High Pass Filter d) Study the Two Dimensional Filter Design using filter design functions				
7	Determine the suitability of homo – morphic filtering using a low pass				
	filter for image enhancement to fix non- uniform of illumination				
8	Implement inverse, Wiener, Regular, and Lucy-Richardson for image restoration. And formulate how noise information in an image can be used to restore a degraded image.				
9	Study different methods of edge	detection for use on noisy images	s, specifically, a) Motion blu	ır b) Gaussian	
	noise c) Filtered Gaussian noise v	ia averaging.			
10	Write an algorithm for recognizing	of circles and triangles.			
		Demonstration Experiments (For	CIE) if any		
11	Write a program in Matlab to imp	plement the Fuzzy logic algorithm	· · · · · · · · · · · · · · · · · · ·		
12	Write a program in Matlab to imp	plement the neural network algor	rithm		

Course outcomes (Course Skill Set): At the end of the course the student will be able to:

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each course. The student has to secure not less than 40% of maximum marks in the semester-end examination(SEE). In total of CIE and SEE student has to secure 50% maximum marks of the course.

Continuous Internal Evaluation (CIE):

CIE marks for the practical course is **50 Marks**.

The split-up of CIE marks for record/ journal and test are in the ratio 60:40.

- Each experiment to be evaluated for conduction with observation sheet and record write-up. Rubrics for the evaluation of the journal/write-up for hardware/software experiments designed by the faculty who is handling the laboratory session and is made known to students at the beginning of the practical session.
- Record should contain all the specified experiments in the syllabus and each experiment write-up will be evaluated for 10 marks.
- Total marks scored by the students are scaled downed to 30 marks (60% of maximum marks).
- Weightage to be given for neatness and submission of record/write-up on time.
- Department shall conduct 02 tests for 100 marks, the first test shall be conducted after the 8th week of the semester and the second test shall be conducted after the 14th week of the semester.
- In each test, test write-up, conduction of experiment, acceptable result, and procedural knowledge will carry a weightage of 60% and the rest 40% for viva-voce.
- The suitable rubrics can be designed to evaluate each student's performance and learning ability.
- The average of 02 tests is scaled down to **20 marks** (40% of the maximum marks).

The Sum of **scaled-down** marks scored in the report write-up/journal and average marks of two tests is the total CIE marks scored by the student.

Semester End Evaluation (SEE):

SEE marks for the practical course is 50 Marks.

SEE shall be conducted jointly by the two examiners of the same institute, examiners are appointed by the University.

All laboratory experiments are to be included for practical examination.

(Rubrics) Breakup of marks and the instructions printed on the cover page of the answer script to be strictly adhered to by the examiners. **OR** based on the course requirement evaluation rubrics shall be decided jointly by examiners. Students can pick one question (experiment) from the questions lot prepared by the internal /external examiners jointly.

Evaluation of test write-up/ conduction procedure and result/viva will be conducted jointly by examiners. General rubrics suggested for SEE are mentioned here, writeup-20%, Conduction procedure and result in -60%, Vivavoce 20% of maximum marks. SEE for practical shall be evaluated for 100 marks and scored marks shall be scaled down to 50 marks (however, based on course type, rubrics shall be decided by the examiners) Change of experiment is allowed only once and 10% Marks allotted to the procedure part to be made zero. The duration of SEE is 03 hours

Suggested Learning Resources:

www.mathworks.com

SI No.	Course Code	Course Title	National Coordinator
1	22AUD27	Programming, Data Structures And Algorithms	NPTEL
		Using Python	
2	22AUD27	Principles of Signal Estimation for MIMO/	NPTEL
		OFDM Wireless Communication	
3	22AUD27	Fundamentals Of Artificial Intelligence	NPTEL
4	22AUD27	Cryptography And Network Security	NPTEL
5	22AUD27	Introduction To Internet Of Things	NPTEL
6	22AUD27	Introduction to Machine Learning	NPTEL
7	22AUD27	Fundamentals of micro and nanofabrication	NPTEL
8	22AUD27	Introduction to Biomedical Imaging Systems	NPTEL

Audit Courses /Ability Enhancement Courses Suggested by BOS (ONLINE courses):

Audit Courses: These are prerequisite courses suggested by the concerned Board of Studies. Ability Enhancement Courses will be suggested by the BoS if prerequisite courses are not required for the programs.

Ability Enhancement Courses:

- These courses are prescribed to help students to enhance their skills in in fields connected to the field of specialization as well allied fields that leads to employable skills. Involving in learning such courses are impetus to lifelong learning.
- The courses under this category are online courses published in advance and approved by the concerned Board of Studies.
- Registration to Audit /Ability Enhancement Course shall be done in consultation with the mentor and is compulsory during the concerned semester.
- In case a candidate fails to appear for the proctored examination or fails to pass the selected online course, he/she can register and appear for the same course if offered during the next session or register for a new course offered during that session, in consultation with the mentor.

The Audit Ability Enhancement Course carries no credit and is not counted for vertical progression. However, a pass in such a course is mandatory for the award of the degree.

M.TECH IN SIGNAL PROCESSING (LSP)

Choice Based Credit System (CBCS) and Outcome Based Education (OBE)

(Effective from the academic year 2022-23)

Semester – 3

Adaptive Signal Processing					
Course Code		22LSP31	CIE Marks	50	
Teaching Hour	s/Week (L:P:SDA)	3:0:2	SEE Marks	50	
Total Hours of	Pedagogy	40 Hours Theory+10 Hours SDA	Total Marks	100	
Credits		04	Exam Hours	03	
 Course Learning objectives: To understand the adaptive filter concepts To analyze the LMS algorithms and its applications 					
Module-1					
Adaptive systems signal and wei smoothing and	ems: Definitions and charac ght vectors - performance I prediction - linear optimur	teristics - applications - properties-exa function-gradient and minimum mean n filtering – orthogonality - Wiener – H	amples - adaptive line square error - introe opf equation perform	ear combiner input duction to filtering- nance Surface.	
Teaching- Learning Process	Teaching- Chalk and Talk / Power Point Presentations. Learning Process				
Module-2					
Imethod of steepest descent - comparison - gradient estimation - performance performance - excess wise and time constants – mis adjustments. Teaching- Learning Process Chalk and Talk / Power Point Presentations. Module-3 LMS algorithm convergence of weight vector: LMS/Newton algorithm - properties - sequential regression algorithm – adaptive recursive filters - random-search algorithms - lattice structure - adaptive filters with orthogonal signals Teaching- Learning Chalk and Talk / Power Point Presentations. Process Chalk and Talk / Power Point Presentations.					
		Module-4			
Applications-adaptive modelling: Multipath communication channel, geophysical exploration, FIR digital filter synthesis.					
Teaching- Learning Process	Teaching- Chalk and Talk / Power Point Presentations. Learning Process				
		Module-5			
Applications: Adaptive Equa	inverse adaptive modelling lization of Telephone Chanr	, deconvolution and equalization, Ge els, Adapting Poles and Zeros for IIR D	neral Description of igital Filter Synthesis.	Inverse Modeling,	
Teaching- Learning Process	Chalk and Talk / Power Poi	nt Presentations.			

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 7. Three Unit Tests each of 20 Marks
- 8. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 16. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 17. The question paper will have ten full questions carrying equal marks.
- 18. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 19. Each full question will have a sub-question covering all the topics under a module.
- 20. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. 'Adaptive Signal Processing', Bernard Widrow and Samuel D Stearns, Pearson Education, 2005.
- 2. 'Theory and Design of Adaptive Filters', John R Treichler, C Richard Johnson, Michael G Larimore, Prentice-Hall of India, 2002
- 3. 'Adaptive Signal Processing-Theory and Application', S Thomas Alexander, Springer-Verlag.
- **4.** 'Statistical and Adaptive Signal Processing', D. G. Manolakis, V. K. Ingle and S. M. Kogar, McGraw Hill International Edition, 2000.
- 5. 'Adaptive Filter Theory', Simon Haykin, Pearson Education, 2003.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

• Mini project in the area of adaptive signal processing using modern tools like MATLAB, Python, scilab

SI.	Description	Blooms Level
No.		
CO1	Able to understand the adaptive systems, wiener – Hopf equation	L1 L2
CO2	Able to understand the LMS algorithm convergence, lattice structures	L2
CO3	Able to understand and analyze the applications of applications of adaptive modeling	L2 L4
CO4	Able to understand the inverse adaptive filtering, deconvolution algorithms and equalization	L1 L2

				67		
Course Code		Array Signal Processing	CIE Marks	50		
Teaching Hours	s/Week (L·P·SDA)	3:0:0	SFF Marks	50		
Total Hours of	Pedagogy	40 hours Theory	Total Marks	100		
Credits		03	Exam Hours	03		
 Course Learning objectives: To understand the spatial signals and sensor arrays To design the array sensors system 						
	<u> </u>	Module-1				
Maxwell's Equation, Wave Equation. Solution to Wave equation in Cartesian Co-ordinate system –Wave number vector, Slowness vector.						
Teaching-	Chalk and Talk / Power Po	int Presentations.				
Learning						
Process						
		Module-2				
Wave number	-Frequency Space Spatial	Sampling: Spatial Sampling The	eorem-Nyquist Criteria, A	liasing in Spatial		
frequency domain, Spatial sampling of multidimensional signals.						
Teaching-	Chalk and Talk / Power	Point Presentations.				
Learning Proce	SS					
Sonsor Arrays	Linoar Arraya Blanar Array	Wiodule-3	spanse and Peam patter	n Array manifold		
vector, Conventional Beam former, Narrowband beam former.						
Teaching-	Chalk and Talk / Power Point Presentations.					
Learning						
Process						
Module-4						
Uniform Linea	r Arrays: Beam pattern in θ,	u and ψ -space, Uniformly Weight	ted Linear Arrays.			
Beam Pattern Grating Lobes,	Parameters: Half Power Bea Array Steering.	am Width, Distance to First Null, L	ocation of side lobes and	Rate of Decrease,		
Teaching- Learning	Chalk and Talk / Power Po	int Presentations.				
Process						
Annos: D!-	Mathada, Malhis as -1	Module-5	and Crass Derret			
Array Design Schelkunoff's Z	ero Placement Method, Fou	rier Series Method with windowin	n and Space -Domain S ng, Woodward -Lawson Fre	equency-Sampling		
Non narametri	c method -Beam forming De	elay and sum Method. Canons Met	thod.			
Teaching-	Chalk and Talk / Power Poir	t Presentations.				
Learning Process						

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- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. 'Optimum Array Processing Part IV of Detection, Estimation, and Modulation Theory', Harry L. Van Trees, John Wiley & Sons, ISBN: 9780471093909, 2002.
- 'Array Signal Processing: Concepts and Techniques', Don H. Johnson, Dan E. Dugeon, Prentice Hall Signal Processing Series, 1st Edition, ISBN-13: 978-0130485137.
- 3. 'Spectral Analysis of Signals', Petre Stoica and Randolph L. Moses, Prentice Hall, ISBN: 0-13-113956-8, 2005.
- 4. 'Electromagnetic Waves and Antennas', Sophocles J. Orfanidis, ECE Department, Rutgers University, 94 Brett Road Piscataway, NJ 88548058. http://www.ece.rutgers.edu/~orfanidi/ewa/

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested :

Implement array processing algorithms using any suitable software

SI.	Description	Blooms Level
No.		
CO1	Able to understand the spatial signals, spatial frequency vs temporal frequency	L2
CO2	Able to understand and implement the sensor arrays, linear arrays planar arrays	L2 L3
CO3	Able to understand the design array methods	L2

	Medical Imaging		
Course Code	22LSP322	CIE Marks	50
Teaching Hours/Week (L:P:SDA)	3:0:0	SEE Marks	50
Total Hours of Pedagogy	40 hours Theory	Total Marks	100
Credits	03	Exam Hours	03

Course Learning objectives:

- To explain the X ray diagnostics methods
- To understand the various imaging the systems

Module-1

Generation and Detection of X-Rays: X-Ray generation and X-Ray generators, Filters, Beam Restrictors and Grids, Screens, X-Ray Detectors. **X-Ray Diagnostic Methods**: Conventional X-Ray Radiography, Fluoroscopy, Angiography, Mammography, Xeroradiography, Image Subtraction.

X-Ray Image Characteristics: Spatial Resolution, Image Noise, Image contrast.

Biological Effects of Ionizing Radiation: Determination of biological effects, Short term and Long term effects.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	

Module-2

X-Ray Tomography: Conventional Tomography, Computed Tomography - Projection function, Algorithms for Image Reconstruction, CT number, Image Artifacts.

Digital Radiography: Digital Subtraction Angiography (DSA), Dual Energy Subtraction, K-Edge subtraction, 3-D Reconstruction.

Recent Developments: Dynamic Spatial Reconstructor (DSR), Imatron or Fastrac Electron Beam CT.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning Process	
	Module-3

Generation and Detection of Ultrasound: Piezoelectric effect, Ultrasonic Transducers, Transducer Beam Characteristics, Axial and Lateral resolution, Focusing and Arrays.

Ultrasonic Diagnostic Methods: Pulse Echo systems - A mode, B mode, M mode and C mode, Transmission Methods, Doppler methods, Duplex Imaging. **Biological Effects of Ultrasound**: Acoustic phenomena at high intensity levels, Ultrasound Bio effects.

Teaching-	Chalk and Talk / Power Point Presentations.			
Learning				
Process				
Module-4				

Generation and Detection of Nuclear Emission: Nuclear Sources, Radionuclide Generators, Nuclear Radiation Detectors, Collimators.

Diagnostic methods using Radiation Detector Probes: Thyroid Function test, Renal function test, Blood volume measurement.

New Radio Nuclide Imaging methods: Longitudinal Section Tomography, SPECT and PET

Characteristics of Radionuclide Images: Spatial Resolution, Image contrast, Image Noise.

Teaching- Learning Process	Chalk and Talk / Power Point Presentations.

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Module-5

Generation and Detection of NMR signal: The NMR Coil/Probe, The transmitter and the Receiver, Data acquisition.

Magnetic Resonance Imaging methods: Spin Echo Imaging, Gradient Echo Imaging, Blood flow Imaging.

Characteristics of MRI images: Spatial Resolution, Image Contrast, Imaging Safety.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	

Process

Assessment Details (both CIE and SEE)

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Suggested Learning Resources:

Books

- 1. 'Principles of Medical Imaging', Kirk Shung, Michael B Smith, Benjamin M W Tsui, Academic Press, 2012.
- 2. 'Fundamentals of Medical Imaging', Zhong Hicho and Manbir Singh, John Wiley, 1993.
- **3.** 'Nuclear Medicine Introductory Text', Peter Josefell & Edwards Sydney, William Blackwell Scientific Publishers, London.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

To visit multispeciality hospital for practical knowledge

SI.	Description	Blooms Level			
No.					
C01	Describe the fundamentals of x-ray radiography and computed tomography, and analyze thesystem requirements.	L1 L2			
CO2	Explain principles of ultrasound imaging and diagnostic methods and analyze the systemrequirements.	L2 L4			
CO3	Discuss the fundamentals of radionuclide imaging, MRI, thermal imaging and analyze thesystem requirements.	L3			
CO4	Describe the concepts of image Guided Intervention and image guided surgery.	L1 L2			
CO5	Design and develop prototype of simple medical imaging system.	L6			
Business Intelligence and its Applications					
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Course Code 22LSP323 CIE Marks 50					
Teaching Hours/Week (L:P:SDA)	3:0:0	SEE Marks	50		
Total Hours of Pedagogy	40 hours Theory	Total Marks	100		
Credits	03	Exam Hours	03		

Course Learning objectives:

- To explain the business intelligence steps
- To analyze the growth development and information technology applications

Module-1

Development Steps, BI Definitions, BI Decision Support Initiatives, Development Approaches, Parallel Development Tracks, BI Project Team Structure, Business Justification, Business Divers, Business Analysis Issues, Cost – Benefit Analysis, Risk Assessment, Business Case Assessment Activities, Roles Involved In These Activities, Risks of Not Performing Step, Hardware, Middleware, DBMS Platform, Non-Technical Infrastructure Evaluation

Teaching-	Chalk and Talk / Power Point Presentations.	
Learning		
Process		
	Module-2	
Managing The These Activities	BI Project, Defining And Planning The BI Project, Project Planning Activities, Roles And Risks Involved In 5, General Business Requirement, Project Specific Requirements, Interviewing Process.	
Teaching- Learning Proce	Chalk and Talk / Power Point Presentations. ss	
	Module-3	
Differences in	Database Design Philosophies, Logical Database Design, Physical Database Design, Activities, Roles And	
Risks Involved	In These Activities, Incremental Rollout, Security Management, Database Backup And Recovery	
Teaching-	Chalk and Talk / Power Point Presentations.	
Learning		
Process		
	Module-4	
Growth Manage Information As	gement, Application Release Concept, Post Implementation Reviews, Release Evaluation Activities, The set and Data Valuation, Actionable Knowledge – ROI, BI Applications, The Intelligence Dashboard.	
Teaching-	Chalk and Talk / Power Point Presentations.	
Learning		
Process		
Module-5		
Business View of Information technology Applications: Business Enterprise excellence, Key purpose of using IT, Type of		
aigital data, ba	sics of enterprise reporting, BI road ahead.	
Teaching-	Chalk and Talk / Power Point Presentations.	
Learning		
Process		

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- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. 'Business Intelligence Roadmap: The Complete Project Lifecycle for Decision Support Applications', Larissa T Moss and Shaku Atre, Addison Wesley Information Technology Series, 2003.
- 2. 'Fundamentals of Business Analytics', R N Prasad, Seema Acharya, Wiley India, 2011.
- 3. 'Business Intelligence: The Savvy Manager's Guide', David Loshin, Publisher: Morgan Kaufmann, ISBN 1-55860-196-4.
 - 4. 'Delivering Business Intelligence with Microsoft SQL Server 2005', Brian Larson, McGraw Hill, 2006.
 - 5. 'Foundations of SQL Server 2008', Lynn Langit, Business Intelligence Apress, ISBN13: 978-14302-3324-4, 2011.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

To learn SQL and implement various DBMS algorithms

Course Outcomes

SI. No.	Description	Blooms Level
CO1	Able to understand the business intelligence development approaches	L2
CO2	Able to understand and analyze growth management, application release concept, intelligence dashboard	L2 L4
CO3	Able to understand the business view of information technology applications	L2

Multimedia Systems and Applications				
Course Code		22LSP324	CIE Marks	50
Teaching Hours/Week (L:P:SDA)		3:0:0	SEE Marks	50
Total Hours of Pedagogy		40 hours Theory	Total Marks	100
Credits	Credits 03 Exam Hours 03			
Course Learnin • To exp • To des	g objectives: plain the multimedia information scribe the multimedia comm	ation representation nunication standards and across ne	etworks	
		Module-1		
Multimedia Co applications, A Information Re Teaching- Learning	pmmunications: Introductio pplication and networking to presentation: Introduction Chalk and Talk / Power Po	n, Multimedia information repres erminology. . Text, Images. int Presentations.	sentation, multimedia net	works, multimedia
Process				
		Module-2		
and Multimedia Operating Systems. Introduction, main reactires of a DMS, Resource management of DMS, Networking, and Multimedia Operating Systems. Teaching- Learning Process Chalk and Talk / Power Point Presentations. Module-3 Multimedia Processing in Communication: Introduction, Perceptual coding of digital Audio signals, Transform Audio Coders, Audio Sub band Coders.				
Teaching- Learning	Teaching- Chalk and Talk / Power Point Presentations. Learning			
Process		Madula A		
Baulutu II C		Module-4	alation and the same of the st	
Multimedia Communication Standards: Introduction, MPEG approach to multimedia standardization, MPEG-1, MPEG-2, Overview of MPEG-4.				
Teaching- Learning Process	Chalk and Talk / Power Po	int Presentations.		
Module-5				
Multimedia Communication Across Networks: Packet audio/video in the network environment, Video transport across generic networks, Multimedia Transport across ATM Networks.				
Teaching- Learning Process	Chalk and Talk / Power Poin	nt Presentations.		

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Suggested Learning Resources:

Books

1. Fred Halsall, "Multimedia Communications", Pearson education, 2001, ISBN - 9788131709948.

2. K. R. Rao, Zoran S. Bojkovic, Dragorad A. Milovanovic, "Multimedia Communication Systems", Pearson education, 2004. ISBN -9788120321458.

3. Raif steinmetz, Klara Nahrstedt, "Multimedia: Computing, Communication and Applications", Pearson education, 2002, ISBN -9788177584417

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

Implement various multimedia algorithms like JPEG MPEG using Matlab software

SI.	Description	Blooms Level
No.		
CO1	Able to understand the multimedia networks, information representation	L2
CO2	Able to understand and analyze the distributed multimedia systems	L2 L3
CO3	Able to understand and analyze the multimedia communication standards	L2 L4
CO4	Able to understand the multimedia communication across the networks	L1 L2

Internet of Things (IOT)			
Course Code	22LSP325	CIE Marks	50
Teaching Hours/Week (L:P:SDA)	3:0:0	SEE Marks	50
Total Hours of Pedagogy	40 hours Theory	Total Marks	100
Credits	03	Exam Hours	03

Course Learning objectives:

- Define IOT and its architecture
- To understand the engineering IoT networks

Module-1

What is IoT ?

Genesis, Digitization, Impact, Connected Roadways, Buildings, Challenges

IoT Network Architecture and Design

Drivers behind new network Architectures, Comparing IoT Architectures, M2M architecture, IoT world forum standard, IoT Reference Model, Simplified IoT Architecture.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	
	Module-2

IoT Network Architecture and Design

Core IoT Functional Stack, Layer1 (Sensors and Actuators), Layer 2 (Communications Sublayer), Access network sublayer, Gateways and backhaul sublayer, Network transport sublayer, IoT Network management.

Layer 3 (Applications and Analytics) – Analytics vs Control, Data vs Network Analytics, IoT Data Management and Compute Stack

Teaching-	Chalk and Talk / Power Point Presentations.
Learning Process	
	Module-3

Engineering IoT Networks

Things in IoT – Sensors, Actuators, MEMS and smart objects. Sensor networks, WSN, Communication protocols for WSN Communications Criteria, Range, Frequency bands, power consumption, Topology, Constrained Devices, Constrained Node Networks IoT Access Technologies, IEEE 802.15.4 Competitive Technologies – Overview only of IEEE 802.15.4g, 4e, IEEE 1901.2a Standard Alliances – LTE Cat 0, LTE-M, NB-IoT

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	

Module-4

Engineering IoT Networks

IP as IoT network layer, Key Advantages, Adoption, Optimization, Constrained Nodes, Constrained Networks, IP versions, Optimizing IP for IoT. Application Protocols for IoT – Transport Layer, Application Transport layer, Background only of SCADA, Generic web based protocols, IoT Application Layer

Data and Analytics for IoT – Introduction, Structured and Unstructured data, IoT Data Analytics overview and Challenges.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	

Module-5

IoT in Industry (Three Use cases)

IoT Strategy for Connected manufacturing, Architecture for Connected Factory Utilities - Power utility, IT/OT divide, Grid blocks reference model, Reference Architecture, Primary substation grid block and automation.

Smart and Connected cities -Strategy, Smart city network Architecture, Street layer, city layer, Data center layer, services layer, Smart city security architecture, Smart street lighting.

Teaching-
Learning
Dracacc

Chalk and Talk / Power Point Presentations.

Process

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Suggested Learning Resources:

Books

- 1. 'CISCO, IoT Fundamentals Networking Technologies, Protocols, Use Cases for IoT', David Hanes, Gonzalo Salgueiro, Patrick Grossetete, Robert Barton, Jerome Henry, Pearson Education, ISBN: 978-9386873743, First edition, 2017
- 2. 'Internet of Things A Hands on Approach', Arshdeep Bahga and Vijay

Madisetti, Orient Blackswan Private Limited - New Delhi, First edition, 2015

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested To implement various IOT protocols

Course Outcomes

SI.	Description	Blooms Level
No.		
CO1	Able to understand the IOT network architecture and design	L2
CO2	Able to understand and analyze WSN protocols, IEEE standard protocols, LTE	L2 L4
CO3	Able to analyze the engineering IOT networks	L4
CO4	Able to understand and analyze IOT in industry	L2

				81
Caura Cada		Biomedical Signal Processing	CIE Marilia	50
Course Code		22LSP331		50
Teaching Hours/Week (L:P:SDA)		3:U:U	SEE Marks	50
Total Hours of	Редадоду	40 hours Theory		100
Credits		03	Exam Hours	03
Course Learnin • To un • To an	ng objectives: derstand the biomedical sign alyze the various mathemati	nals acquisition and its analysis cal tools used in biomedical signal	analysis	
		Module-1		
Introduction-C Spectral analys	Genesis and significance of b sis.	io electric potentials, ECG, EEG, EN	/IG and their monitoring a	and measurement,
Teaching- Learning Process	Chalk and Talk / Power Po	int Presentations.		
	1	Module-2		
Filtering- Digi	tal and Analog filtering, Corr	elation and Estimation techniques,	, AR / ARMA models	
Teaching- Learning Proce	Chalk and Talk / Power	Point Presentations.		
		Module-3		
Base line wan based on decis	der removal, waveform red sion theory ECT compression	, Evoked potential estimation.	and rhythm analysis, aut	comated diagnosis
Teaching-	Chalk and Talk / Power Po	int Presentations.		
Learning				
Process				
	1	Module-4		
EEG : Evoked responses, Epilepsy detection, Spike detection, Hjorth parameters, averaging techniques, removal of Artifacts by averaging and adaptive algorithms, pattern recognition of alpha, beta, theta and delta waves in EEG waves, sleep stages.				
Teaching-	Chalk and Talk / Power Po	int Presentations.		
Learning				
Process				
		Module-5		
EMG -Wave pattern studies, bio feedback, Zero crossings, Integrated EMG. Time frequency methods and Wavelets in Biomedical Signal Processing.				
Teaching-	Chalk and Talk / Power Poir	nt Presentations.		
Learning Process				

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Suggested Learning Resources:

Books

- 1. Biomedical Digital Signal Processing', Willis J Tompkins, Prentice Hall of India, 1996.
- 2. Rangaraj M. Rangayyan, "Biomedical Signal Analysis", John Wiley & Sons, Inc, reprint 2000,
- **3.** 'Biomedical Signal Processing (in IV parts)', R E Challis and RI Kitney, Medical and Biological Engg. and current computing, 1990-91.
- 4. Special issue on 'Biological Signal Processing', Proc. IEEE 1972.
- 5. 'Biomedical Signal Processing', Arnon Cohen, Volumes I & II, CRC Press.
- **6.** 'Time frequency and Wavelets in Biomedical Signal Processing', Metin Akay, IEEE Press, 1999. Current Published literature.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

Design and implement the hardware circuit to acquire the various Bio – Medical signals Design and implement using the software to process the various Bio – Medical signals

Course Outcomes

SI.	Description	Blooms Level
No.		
CO1	Explain the significance of bio electric potentials	L1 L2
CO2	Discuss the AR/ARMA models for power spectral estimation	L2 L4
CO3	Implement the QRS detection, ST segment analysis, evoked potential estimation	L3 L4
CO4	Analyze the EEG pattern recognition, epilepsy detection, Hjorth parameters,	L\$

Speech and Audio Processing			•
Course Code	22LSP332	CIE Marks	50
Teaching Hours/Week (L:P:SDA)	3:0:0	SEE Marks	50
Total Hours of Pedagogy	40 hours Theory	Total Marks	100
Credits	03	Exam Hours	03

Course Learning objectives:

- Familiarize the basic mechanism of speech production and get an overview of articulatory and acoustic Phonetics.
- Learn the basic concepts of methods for speech analysis and parametric representation of speech.
- Acquire knowledge about various methods used for speech and audio coding.
- Get an overall picture about various applications of speech and audio processing.

Module-1 Digital Models for the Speech Signal: Process of speech production, Acoustic theory of speech production, Lossless tube models, and Digital models for speech signals (Text 1). Time Domain Models for Speech Processing: Time dependent processing of speech, Short time energy and average magnitude, Short time average zero crossing rate, Speech vs silence discrimination using energy & zero crossings, Pitch period estimation, Short time autocorrelation function, Short time average magnitude difference function, Pitch period estimation using autocorrelation function, Median smoothing.

Teaching-	Chalk and Talk / Power Point Presentations.		
Learning			
Process			
Module-2			
Digital Represe	entations of the Speech Waveform; Sampling speech signals, Instantaneous quantization. Adaptive		

quantization, Differential quantization, Delta Modulation, Differential PCM, Comparison of systems, direct digital code conversion. Short Time Fourier Analysis: Linear Filtering interpretation, Filter bank summation method, Overlap addition method, Design of digital filter banks, Implementation using FFT, Spectrographic displays, Pitch detection, Analysis by synthesis, Analysis synthesis systems.

Teaching-
Learning ProcessChalk and Talk / Power Point Presentations.

Module-3

Homomorphic Speech Processing: Homomorphic systems for convolution, Complex cepstrum, Pitch detection, Formant estimation, Homomorphic vocoder. Linear Predictive Coding of Speech: Basic principles of linear predictive analysis, Solution of LPC equations, Prediction error signal, Frequency domain interpretation, Relation between the various speech parameters, Synthesis of speech from linear predictive parameters, Applications.

Teaching-	Chalk and Talk / Power Point Presentations.	
Learning		
Process		
Module-4		

Speech Enhancement: Spectral subtraction & filtering, Harmonic filtering, parametric re-synthesis, Adaptive noise cancellation. Speech Synthesis: Principles of speech synthesis, Synthesizer methods, Synthesis of intonation, Speech synthesis for different speakers, Speech synthesis in other languages, Evaluation, Practical speech synthesis.

Teaching-	Chalk and Talk / Power Point Presentations.	
Learning		
Process		
Module-5		

Automatic Speech Recognition: Introduction, Speech recognition vs. Speaker recognition, Signal processing and analysis methods, Pattern comparison techniques, Hidden Markov Models, Artificial Neural Networks. Audio Processing: Auditory perception and psychoacoustics - Masking, frequency and loudness perception, spatial perception, Digital Audio, Audio Coding - High quality, low-bit rate audio coding standards, MPEG, AC- 3, Multichannel audio - Stereo, 3D

binaural and Multichannel surround sound.

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	

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- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. L. R. Rabiner and R. W. Schafer, 'Digital Processing of Speech Signals', Pearson Education (Asia) Pvt. Ltd., 2004.
- 2. L. R. Rabiner and B. Juang, 'Fundamentals of Speech Recognition', Pearson Education (Asia) Pvt. Ltd., 2004.
- 3. Z. Li and M.S. Drew, 'Fundamentals of Multimedia', Pearson Education (Asia) Pvt. Ltd., 2004.
- 4. D. O'Shaughnessy, 'Speech Communications: Human and Machine', Universities Press, 2001.

1.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/

https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

Design and implement the hardware circuit to acquire the speech signals Design and implement using the software to process the speech signals **Course Outcomes**

SI.	Description	Blooms Level
No.		
CO1	Understand basic concepts of speech production, speech analysis and synthesis	L1 L2
CO2	Analyze Speech coding techniques, Speech and speaker recognition systems.	L2 L4
CO3	Explain the Concepts of Audio Processing and learn modeling	L2
CO4	Implement Applications such as New audiogram matching techniques	L2 L3
CO5	Develop systems for various applications of speech processing.	L3

Advanced Embedded System				
Course Code	22LSP333	CIE Marks	50	
Teaching Hours/Week (L:P:SDA)	3:0:0	SEE Marks	50	
Total Hours of Pedagogy	40 hours Theory	Total Marks	100	
Credits	03	Exam Hours	03	

Course Learning objectives:

- Understand the basic hardware components and their selection method based on the characteristics and attributes of an embedded system.
- Describe the hardware software co-design and firmware design approaches
- Explain the architectural features of ARM CORTEX M3, a 32 bit microcontroller including memory map, interrupts and exceptions.
- Program ARM CORTEX M3 using the various instructions, for different applications.

Module-1 Embedded System: Embedded vs General computing system, classification, application and purpose of ES. Core of an Embedded System, Memory, Sensors, Actuators, LED, Opto coupler, Communication Interface, Reset circuits, RTC, WDT, Characteristics and Quality Attributes of Embedded Systems **Teaching-**Chalk and Talk / Power Point Presentations. Learning Process Module-2 Hardware Software Co-Design, embedded firmware design approaches, computational models, embedded firmware development languages, Integration and testing of Embedded Hardware and firmware, Components in embedded system development environment (IDE), Files generated during compilation, simulators, emulators and debugging **Teaching-**Chalk and Talk / Power Point Presentations. Learning Process Module-3 ARM-32 bit Microcontroller: Thumb-2 technology and applications of ARM, Architecture of ARM Cortex M3, Various Units in the architecture, General Purpose Registers, Special Registers, exceptions, interrupts, stack operation, reset sequence **Teaching-**Chalk and Talk / Power Point Presentations. Learning Process Module-4 Instruction Sets: Assembly basics, Instruction list and description, useful instructions, Memory Systems, Memory maps, Cortex M3 implementation overview, pipeline and bus interface **Teaching-**Chalk and Talk / Power Point Presentations. Learning Process Module-5 Exceptions, Nested Vector interrupt controller design, Systick Timer, Cortex-M3 Programming using assembly and C language, CMSIS Chalk and Talk / Power Point Presentations. Teaching-Learning Process

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

Text Books:

- 1. K. V. Shibu, "Introduction to embedded systems", TMH education Pvt. Ltd. 2009.
- 2. Joseph Yiu, "The Definitive Guide to the ARM Cortex-M3", 2nd edn, Newnes,

(Elsevier), 2010.

3. James K. Peckol, "Embedded systems- A contemporary design tool", John Wiley, 2008.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

Design and write a assembly code to implement various interfacing experiments using ARM microcontroller

Course Outcomes

SI.	Description	Blooms Level
No.		
CO1	Understand the basic hardware components and their selection method based on the characteristics and attributes of an embedded system.	L1 L2
CO2	Explain the hardware software co-design and firmware design approaches.	L2 L4
CO3	Acquire the knowledge of the architectural features of ARM CORTEX M3, a 32 bit microcontroller including memory map, interrupts and exceptions.	L2 L4
CO4	Apply the knowledge gained for Programming ARM CORTEX M3 for different applications.	L1 2

				90
		Wireless Sensor Networks		
Course Code		22LSP334	CIE Marks	50
Teaching Hou	ırs/Week (L:P:SDA)	3:0:0	SEE Marks	50
Total Hours of Pedagogy		40 hours Theory	Total Marks	100
Credits		03	Exam Hours	03
Course Learn	ing objectives:			
• To u	nderstand wireless sensor	networks		
• To de	escribe the various sensor	networks		
• To u	nderstand the measureme	nt of various transport layers and app	lication layer	
• To u	nderstand the time synchr	onization and localization		
		Module-1		
Introduction:	: Sensor Mote Platforms, W	/SN Architecture and Protocol Stack		
WSN Applications	tions: Military Applications	s, Environmental Applications, Health	Applications, Home App	lications, Industrial
Teaching-	Chalk and Talk / Power	Point Presentations.		
Learning Process				
		Module-2		
Factors Influe	encing WSN Design: Hardw	vare Constraints Fault Tolerance Scala	bility Production Costs V	VSN Topology.
Transmission	Media, Power Consumptio	on		
Teaching-	Chalk and Talk / Pov	ver Point Presentations.		
Learning Proc		Module-3		
Medium Acce	ess Control: Challenges for	MAC CSMA Mechanism Contention	-Based Medium Access	Reservation-Based
Medium Acce	ess Hybrid Medium Access	WAC, COMA Mechanism, contention	-Dased Mediani Access,	leser varion-based
Network Lave	er: Challenges for Routing	Data-centric and Flat Architecture Pro	otocols, Hierarchical Prot	tocols
Geographical	Routing Protocols			
	-			
Teaching-	Chalk and Talk / Power	Point Presentations.		
Learning				
Process				
		Module-4		
Transport	vor: Challongos for Transpo	vrt Lavor, Poliablo Multi Sogmont Tran	sport (PMST) Protocol	Jump Slowly Eatch
Quickly (PSFC	() Protocol, Congestion Det	tection and Avoidance (CODA) Protoc	ol, Event-to-Sink Reliable	inp slowly, retch
iransport (ES	KI) Protocol, GARUDA			
Application L	ayer: Source Coding (Data	Compression), Query Processing, Net	work Management	
Teaching-	Chalk and Talk / Power	Point Presentations.		
Learning				
Process				
		Module-5		
Time Synchro	nization : Challenges for Ti	me Synchronization Network Time P	rotocol. Timing-Sync Pro	tocol for Sensor
Networks (TP	SN), Reference- Broadcast	Synchronization (RBS), Adaptive Cloc	k Synchronization (ACS)	
Localization;	Challenges in Localization,	Ranging Techniques, Range-Based Lo	calization Protocols, Ran	ge-Free
Localization P	Protocols			

Teaching-	Chalk and Talk / Power Point Presentations.
Learning	
Process	
Assessment D	etails (both CIE and SEE)
Assessment D	etails (both CIE and SEE)

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Continuous Internal Evaluation:

- 1. Three Unit Tests each of **20 Marks**
- 2. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. Ian F. Akyildiz and Mehmet Can Vuran, 'Wireless Sensor Networks', John Wiley & Sons Ltd. ISBN 978-0-470-03601-3 (H/B), 2010
- 2. Ananthram Swami, 'Wireless Sensor Networks: Signal Processing and Communications Perspectives', et. al., John Wiley & Sons Ltd., ISBN 978-0470-03557-3, 2007

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

To test the various wireless sensor network protocols

Course Outcomes:

SI.	Description	Blooms Level
No.		
C01	Acquire knowledge of characteristics of mobile/wireless communication channels	L1 L2
CO2	Apply statistical models of multipath fading	L2 L4
CO3	Understand the multiple radio access techniques, radio standards and communication protocols to be used for wireless sensor	L2
CO4	Design wireless sensor network system for different applications under consideration.	L2 L4
CO5	Understand the hardware details of different types of sensors and select right type of sensor for various applications.	L3 L4

				93
		Statistical Signal Processing		
Course Code		22LSP335	CIE Marks	50
Teaching Hours/Week (L:P:SDA)		3:0:0	SEE Marks	50
Total Hours of	Pedagogy	40 hours Theory	Total Marks	100
Credits		03	Exam Hours	03
Course Learnir • To un • To un	ng objectives: derstand and analyze the pa derstand and analyze the ad	arametric method and non parame laptive filter algorithms	tric methods	
		Module-1		
Random Pro factorization,	cesses: Random variables, ARMA, AR and MA processe	, random processes, white nois s	se, filtering random pr	ocesses, spectral
Teaching- Learning Process	Teaching- Chalk and Talk / Power Point Presentations. Learning Process			
		Module-2		
Levinson-Durbin recursion; Schur recursion; Levinson recursion Teaching- Learning Process				
Constanting Fat	in ations. No so so so stais as	Module-3		
parametric m	ethods, frequency estimatio	n, principal components spectrum	estimation	entropy method,
Teaching-	Chalk and Talk / Power Po	int Presentations.		
Learning				
1100033		Module-4		
Optimal and A	daptive Filtering: FIR and II	R Wiener filters, Discrete Kalman	filter. FIR Adaptive filters	: Steepest descent.
LMS, LMS-based algorithms				
Teaching- Learning	Chalk and Talk / Power Po	int Presentations.		
Process		Madula F		
IVIOQUIE-5				
beam forming, linearly constrained minimum-variance beam-formers, side-lobe cancellers				
Teaching- Learning Process	Chalk and Talk / Power Poin	nt Presentations.		

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 50% of the maximum marks. Minimum passing marks in SEE is 40% of the maximum marks of SEE. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 50% (50 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together.

Continuous Internal Evaluation:

- 1. Three Unit Tests each of 20 Marks
- 2. Two assignments each of **20 Marks or one Skill Development Activity of 40 marks** to attain the COs and POs

The sum of three tests, two assignments/skill Development Activities, will be scaled down to 50 marks

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

- 3. The SEE question paper will be set for 100 marks and the marks scored will be proportionately reduced to 50.
- 4. The question paper will have ten full questions carrying equal marks.
- 5. Each full question is for 20 marks. There will be two full questions (with a maximum of four sub-questions) from each module.
- 6. Each full question will have a sub-question covering all the topics under a module.
- 7. The students will have to answer five full questions, selecting one full question from each module

Suggested Learning Resources:

Books

- 1. 'Statistical Digital Signal Processing and Modelling', Monson H Hayes, John Wiley & Sons (Asia) Pvt. Ltd., 2002.
- 2.'Statistical and Adaptive Signal Processing: Spectral Estimation, Signal Modelling, Adaptive Filtering and Array Processing', Dimitris G. Manolakis, Vinay K. Ingle, and Stephen M. Kogon, McGraw Hill International Edition, 2000.

Web links and Video Lectures (e-Resources):

https://www.mooc.org/ https://onlinecourses.nptel.ac.in/

Skill Development Activities Suggested

Design and implement using the software to process the various statistical signal processing algorithms

Course Outcomes

SI.	Description	Blooms Level
No.		
CO1	Able to understand and implement the digital filter design and least square sense	L2 L3
CO2	Able to understand and implement the parametric and non parametric methods of spectral estimation	L2 L3
CO3	Able to analyze forward and backward linear prediction of a stationary random process using Levinson-Durbin Algorithm	L4
CO4	Able to understand and analyze adaptive filters and its application using LMS algorithm & RLS algorithm.	L2 L4

PROJECT WORK PHASE - 1			
Course Code	22LSP34	CIE Marks	100
Teaching Hours/Week (L:P:SDA)	(0:6:0)	SEE Marks	-
Total Hours of Pedagogy	40 hours Practical	Total Marks	100
Credits	03	Exam Hours	-

Course Learning objectives: This course will enable students to:

- Support independent learning.
- Guide to select and utilize adequate information from varied resources maintaining ethics.
- Guide to organize the work in the appropriate manner and present information (acknowledging the sources) clearly.
- Develop interactive, communication, organization, time management, and presentation skills.
- Impart flexibility and adaptability.
- Inspire independent and team working.
- Expand intellectual capacity, credibility, judgement, intuition.
- Adhere to punctuality, setting and meeting deadlines.
- Instill responsibilities to oneself and others.
- Train students to present the topic of project work in a seminar without any fear, face audienceconfidently, enhance communication skill, involve in group discussion to present and exchange ideas.

Project Work Phase-1: The project work shall be carried out individually. However, in case a disciplinary or interdisciplinary project requires more participants, then a group consisting of not more than three shall be permitted.

Students in consultation with the guide/co-guide (if any) in disciplinary project or guides/co-guides (if any) of all departments in case of multidisciplinary projects, shall pursue a literature survey and complete the preliminary requirements of the selected Project work. Each student shall prepare a relevant introductory project document, and present a seminar.

Seminar: Each student, under the guidance of a Faculty, is required to

- Present the seminar on the selected project orally and/or through power point slides.
- Answer the queries and involve in debate/discussion.
- Submit two copies of the typed report with a list of references.

The participants shall take part in discussion to foster friendly and stimulating environment in which the students are

motivated to reach high standards and become self-confident.

Course outcomes: At the end of the course the student will be able to:

- 1. Demonstrate a sound technical knowledge of their selected project topic.
- 2. Undertake problem identification, formulation and solution.
- 3. Design engineering solutions to complex problems utilizing a systems approach.
- 4. Communicate with engineers and the community at large in written an oral forms.
- 5. Demonstrate the knowledge, skills and attitudes of a professional engineer.

Continuous Internal Evaluation

CIE marks shall be awarded by a committee comprising of HoD as Chairman, all Guide/s and co-guide/s (if any) and a senior faculty of the concerned departments. The CIE marks awarded for project work phase -1, shall be based on the evaluation of Project Report, Project Presentation skill, and performance in the Question

and Answer session in the ratio of 50:25:25.

RBT Level: L3, L4, L5, L6

SOCIETAL PROJECT			
Course Code	22LSP35	CIE Marks	100
Teaching Hours/Week (L:P:SDA)	(0:6:0)	SEE Marks	-
Total Hours of Pedagogy	40 hours Practical	Total Marks	100
Credits	03	Exam Hours	-

Course Learning objectives: This course will enable students to:

- Support independent learning.
- Guide to select and utilize adequate information from varied resources maintaining ethics.
- Guide to organize the work in the appropriate manner and present information (acknowledging the sources) clearly.
- Develop interactive, communication, organisation, time management, and presentation skills.
- Impart flexibility and adaptability.
- Inspire independent and team working.
- Expand intellectual capacity, credibility, judgement, intuition.
- Adhere to punctuality, setting and meeting deadlines.
- Instil responsibilities to oneself and others.
- Train students to present the topic of project work in a seminar without any fear, face audience confidently, enhance communication skill, involve in group discussion to present and exchange ideas.

Societal Project: Students in consultation with the internal guide as well as with external guide (much preferable) shall involve in applying technology to workout/proposing viable solutions for societal problems.

Seminar: Each student, under the guidance of a Faculty, is required to

- Present the seminar on the selected project orally and/or through power point slides.
- Answer the queries and involve in debate/discussion.
- Submit two copies of the typed report with a list of references.

The participants shall take part in discussion to foster friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident.

Course outcomes: At the end of the course the student will be able to:

- 1. Demonstrate a sound technical knowledge of their selected project topic.
- 2. Undertake societal problem identification, formulation and solution.
- 3. Design engineering solutions to complex societal problems utilizing a systems approach.
- 4. Communicate with engineers and the community at large in written an oral forms.
- 5. Demonstrate the knowledge, skills and attitudes of a professional engineer.

Continuous Internal Evaluation

CIE marks shall be awarded by a committee comprising of HoD as Chairman, Guide/co-guide if any, and a senior faculty of the department. The CIE marks awarded, shall be based on the evaluation of Project Report, Project Presentation skill, and performance in the Question and Answer session in the ratio of **50:25:25**.

NOTE: Those, who have not pursued /completed the Societal Project, shall be declared as fail in the course and have to complete the same during subsequent semester/s after satisfying the Societal Project requirements. There is no SEE (University examination) for this course.

RBT Level: L3, L4, L5, L6

INTERNSHIP			
Course Code	22LSPI36	CIE Marks	50
Teaching Hours/Week (L:P:SDA)	06 weeks Internship Completed	SEE Marks	50
Total Hours of Pedagogy	during the intervening vacation ofII and III semesters.	Total Marks	100
Credits	06	Exam Hours	3

Course Learning objectives: This course will enable students to:

- Internship provide students the opportunity of hands-on experience that include personal training, time and stress management, interactive skills, presentations, budgeting, marketing, liability and risk management, paperwork, equipment ordering, maintenance, responding to emergencies etc. The objectives are further,
- To put theory into practice.
- To expand thinking and broaden the knowledge and skills acquired through course work in the field.
- To relate to, interact with, and learn from current professionals in the field.
- To gain a greater understanding of the duties and responsibilities of a professional.
- To understand and adhere to professional standards in the field.
- To gain insight to professional communication including meetings, memos, reading, writing, publicspeaking, research, client interaction, input of ideas, and confidentiality.
- To identify personal strengths and weaknesses.
- To develop the initiative and motivation to be a self-starter and work independently

Internship: All the students shall have to undergo a mandatory internship of **06 weeks** during the vacation of II and III semesters. A University examination shall be conducted during III semester and the prescribed internship credit shall be counted in the same semester. The internship shall be considered as a head of passing and shall be considered for vertical progression as well as for the award of degree.

Students under the guidance of internal guide/s and external guide shall take part in all the activities regularly to acquire as much knowledge as possible without causing any inconvenience at the place of internship.

Seminar: Each student is required to

- Present the seminar on the internship orally and/or through power point slides.
- Answer the queries and involve in debate/discussion.
- Submit the report duly certified by the external guide.

The participants shall take part in discussion to foster friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident.

Course outcomes: At the end of the course the student will be able to:

- Gain practical experience within industry in which the internship is done.
- Acquire knowledge of the industry in which the internship is done.
- Apply knowledge and skills learned to classroom work.
- Develop a greater understanding about career options while more clearly defining personal careergoals.
- Experience the activities and functions of professionals.
- Develop and refine oral and written communication skills.
- Identify areas for future knowledge and skill development.
- Expand intellectual capacity, credibility, judgment, intuition.

• Acquire the knowledge of administration, marketing, finance and economics

Continuous Internal Evaluation

CIE marks shall be awarded by a committee comprising of HoD as Chairman, Guide/co-guide if any, and a senior faculty of the department. The CIE marks awarded for project work phase -1, shall be based on the evaluation of Project Report, Project Presentation skill, and performance in the Question and Answer session inthe ratio of **50:25:25**.

Semester End Examination

SEE marks for the Internship Report (30 Marks), Seminar (15 Marks) and Question and Answer Session (15 marks) shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session) by the examiners appointed by the University.

NOTE: Those, who have not pursued /completed the internship, shall be declared as fail in the internship course and have to complete the same during subsequent University examinations after satisfying the internship requirements. Internship SEE (University examination) shall be as per the University norms.

RBT Level: L3, L4, L5, L6

Choice Based Credit System (CBCS) and Outcome Based Education (OBE)

(Effective from the academic year 2022-23)

SEMESTER -IV

PROJECT WORK PHASE - 2			
Course Code	22LSP41	CIE Marks	100
Teaching Hours/Week (L:P:SDA)	(0:8:0)	SEE Marks	100
Total Hours of Pedagogy	40 hours Practical	Total Marks	200
Credits	18	Exam Hours	3

Course Learning objectives: This course will enable students to:

- Support independent learning.
- Guide to select and utilize adequate information from varied resources maintaining ethics.
- Guide to organize the work in the appropriate manner and present information (acknowledging thesources) clearly.
- Develop interactive, communication, organisation, time management, and presentation skills.
- Impart flexibility and adaptability.
- Inspire independent and team working.
- Expand intellectual capacity, credibility, judgement, intuition.
- Adhere to punctuality, setting and meeting deadlines.
- Instil responsibilities to oneself and others.
- Train students to present the topic of project work in a seminar without any fear, face audienceconfidently, enhance communication skill, involve in group discussion to present and exchange ideas.

Project Work Phase-2: Students in consultation with the guide/co-guide (if any) in disciplinary project or guides/co-guides (if any) of all departments in case of multidisciplinary projects, shall continue to work of Project Work phase -1 to complete the Project work. Each student / batch of students shall prepare project report as per the norms avoiding plagiarism and present a seminar.

Seminar: Each student, under the guidance of a Faculty, is required to

- Present the seminar on the selected project orally and/or through power point slides.
- Answer the queries and involve in debate/discussion.
- Submit two copies of the typed report with a list of references.

The participants shall take part in discussion to foster friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident.

Course outcomes: At the end of the course the student will be able to:

• Present the project and be able to defend it.

- Make links across different areas of knowledge and to generate, develop and evaluate ideas and information so as to apply these skills to the project task.
- Habituated to critical thinking and use problem solving skills
- Communicate effectively and to present ideas clearly and coherently in both the written and oral forms.

Work in a team to achieve common goal.

• Learn on their own, reflect on their learning and take appropriate actions to improve it.

Continuous Internal Evaluation

CIE marks shall be awarded by a committee comprising of HoD as Chairman, all Guide/s and co-guide/s (if any) and a senior faculty of the concerned departments. The CIE marks awarded for project work phase -2, shall be based on the evaluation of Project Report, Project Presentation skill, and performance in the Question and Answer session in the ratio of **50:25:25**.

Semester End Examination

SEE shall be at the end of IV semester. Project work evaluation and Viva-Voce examination (SEE), after satisfying the plagiarism check, shall be as per the University norms. SEE marks for the project report (50 marks), seminar (25 marks) and question and answer session (25 marks) shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session) by the examiners appointed

by the University. **RBT Level: L3, L4, L5, L6**