

PROFILE



Name: **Dr. Sudarshana Reddy.H.R.**
Date of Birth: 1st June 1966
Designation: Program Coordinator.,
**Visvesvaraya Technological University, PG
Center Muddenhali .**

❖ Academic Record

Certificate/ Degree	Specialization	Board/College/Institute/ University	Year of completion	Class
B.E	Electrical Engineering	Karnataka University, Dharwad	1989	First Class
M.E.	Electrical Power Systems	Bangalore University, Bangalore	1992	First Class
Ph.D.	Pattern Recognition	Kuvempu University, Shimoga, Karnataka	2007	-

4. Employment Details:

Employer	Post held	Date of Joining	Date of Leaving
Visvesvaraya Technological University, Belgaum	Professor	31.08.2009	Till date
Kuvempu University, Shimoga	Assistant Professor, Senior Lecturer & Lecturer	04.01.1997	30.08.2009
Vijayanagara Engineering College, Bellary	Lecturer	01.09.1992	03.01.1997

5. Academic Experience

Academic experience	Professor	6 years	6 months
	Assistant Professor, Senior Lecturer & Lecturer	17 years	0 months

6. Experience

Teaching experience	UG: 23 years 6 months	PG: 11 years
Research experience (Post-Ph.D.)	09 years	
Industrial experience	Nil	

7. Research guidance (total numbers in last 5 years)	Completed		Ongoing	
	Ph.D.	P.G.	Ph.D.	P.G.
	01	15	06	02

8. List of Research Publication details:

1. Sudarshana Reddy H.R. and Subba Reddy N.V., Preprocessing techniques for heterogeneous document image processing useful in non parametric type of recognition systems, National Conference on Document Analysis and Recognition, July 2001, Mandya, India.
2. Sudarshana Reddy H.R. and Subba Reddy N.V., Genetic algorithms based neural network training for handwritten numeral recognition, IEEE Sponsored National Conference on Intelligent Systems, Automation and Signal Processing, March 2003, Chennai, India.
3. Sudarshana Reddy H.R., and Subba Reddy N.V., Genetic algorithms based handwritten numeral recognition, International Conference on Pattern Recognition and Information Processing, May 2003, Minsk, Belarus.
4. Sudarshana Reddy H.R. and Subba Reddy N.V., Handwritten numerals recognition using new genetic algorithms, Seventh International Symposium on Signal Processing and its Applications, July 2003, Paris, France.
5. Sudarshana Reddy H.R. and Subba Reddy N.V., Neural network based approach for hand written numeral recognition, National Conference on Emerging Trends in Signal Systems and Communication, September 2003, Nandyal, A.P., India.
6. Sudarshana Reddy H.R. and Subba Reddy N.V., Recognition of unconstrained handwritten numerals by KNN classifier, IEEE Sponsored National Conference, February 2004, Chennai, India.
7. Sudarshana Reddy H.R. and Subba Reddy N.V., Neuro-Genetic algorithm for fingerprint recognition, International Workshop on Learning Adaptive Visual Systems, August 2004, Cambridge, UK.
8. Sudarshana Reddy H.R. and Subba Reddy N.V., Development of genetic algorithm embedded KNN for fingerprint recognition, Asian Applied Computing Conference 2004,

October 2004, Kathmandu, Nepal.

Sudarshana Reddy H.R. and Subba Reddy N.V., Development of Neuro-genetic algorithm for fingerprint recognition, National Conference, September 2004, Nandyal, A.P., India.

9. Sudarshana Reddy H.R. and Subba Reddy N.V., Neuro-Genetic hybrid system for unconstrained pattern recognition, International Conference on Knowledge Based Computer Systems (KBCS-04), December 2004, Hyderabad, India.
10. Sudarshana Reddy H.R. and Subba Reddy N.V., Novel algorithm for fingerprint recognition, International Conference on Emerging Technology, December 2004, Bhubaneswar, India.
11. Sudarshana Reddy H.R. and Subba Reddy N.V., Three-tier architecture for fingerprint recognition, International Conference, MVIP-05, Iran.
12. Sudarshana Reddy H.R., and Subba Reddy N.V., Three-tier architecture for handwritten numeral recognition, International Conference on Pattern Recognition and Information Processing, May 2005, Minsk, Belarus
13. Sudarshana Reddy H.R. and Subba Reddy N.V., Development of genetic algorithm embedded KNN for fingerprint recognition, electronic journal, Springer-Verlag Heidelberg, Germany, January 2005.
14. Sudarshana Reddy H.R. and Subba Reddy N.V., Recognition of Unconstrained Handwritten Numerals using K-NN Classifier, National Conference on Knowledge-Based Computing Systems & Frontier Technologies, February 2007, MIT, Manipal, Karnataka.
15. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Barcode based feature vectors for numerals recognition, National Conference on VLSI & Multimedia Communication-09 (NCVM-09), October 2009, RVCE, Bangalore, Karnataka.
16. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Statistical classifier with barcode based feature vectors for numerals recognition, National Conference on Soft Computing (SOFTCOMP-11), July 2011, VIT, Vellore, Tamilnadu.
17. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Statistical classifier with barcode based feature vectors for numerals recognition, International Journal Elixir Elect. Engg. 38 – December 2011(4390- 4393).
18. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Spatial cancrroid feature vectors for Kannada digits recognition, National Conference on Computation Intelligence in E&E Engineering (NCIEEE), March 2012 , Sathyabama University, Chennai, Tamilnadu.
19. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Neural network based Kannada numerals recognition system, National Conference on Advanced Computing and Communications (NACC-12), April 2012 , Kuvempu University, Shankaragatta, Karnataka.
20. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Neural network based Kannada numerals recognition system, International Journal of Computer Applications, August 2012 (ISBN: 973-93- 80869-84-1), Foundation of Computer Science, New York, USA.
21. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Handwritten Kannada Numerals Recognition Using Discrete Cosine Transform Coefficients, International Conference on Emerging Research in Electronics, Computer Science and Technology(ICERCT-12) ,December 2012, PES College of Engineering, Mandya, Karnataka.

22. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Handwritten Kannada Numerals Recognition Using Discrete Cosine Transform Coefficients, Lecture Notes in Electrical Engineering 248, Springer India 2014.
23. Sudarshana Reddy H.R. and Harish Kumar G., Load Flow Studies by CB Model Approach using UPFC, International Conference on Electrical, Electronics and Robotics (ICEER), March 2014, Goa, India.
24. Sudarshana Reddy H.R. and Shiva Kumar HS., A Hybrid Particle Swarm Optimization for Reactive Power and Voltage Control in Electric Power Systems, National Conference on Exhibited Models of Project Innovations and Research in Engineering (NC-EMPIRE), March 2014, Miraj, India
Sudarshana Reddy H.R. and Swetha G.C., Voltage Stability Assessment in Power Network using Artificial Neural Network , International Journal of Advanced Research in Electrical , Electronics and Instrumentation Engineering, March 2014 (ISSN: 2320-3765), India.
25. Sudarshana Reddy H.R. and Suneetha S., Optimal Location of TCSC Device to Minimize the Losses, International Conference on Emerging Trends in Electrical, Electronics, Instrumentation and Computer engineering [ETEICE], March 2014, Raipur, India.
26. Sudarshana Reddy H.R. and Shreedharamurthy S.K., Kohonen Neural Network based Kannada Numerals Recognition System, International Journal of Scientific & Engineering Research, Volume 5, Issue 5, May-2014.
27. Sudarshana Reddy H.R. and Harish Kumar G., Load Flow Studies by CB Model Approach using UPFC, International journal of Systems and Technologies, ISSN 0974 – 2107, Vol.7, Issue 2, June 2014.
28. Sudarshana Reddy H.R. and Surya N.S., Reactive Power Compensation Capability & Active Power Transfer of PMSG Based Variable Speed WECS, International journal of Advance Research in Electrical & Electronics & Instrumentation Engineering, ISSN 2278 – 8875, Vol.4, Issue 5, May 2015.
29. Sudarshana Reddy H.R. and Uday Kumar G., Contingency Ranking in Modern Power System by Exact and Precise Method, International journal of Innovative Research in Electrical , Electronics , Instrumentation and Control Engineering, ISSN 2321 – 5526, Vol.3, Issue 5, May 2015.
30. Sudarshana Reddy H.R. and Rachana Raj K., Contingency Analysis to Detect Weak Links in the Network, International journal of Science Technology & Engineering, ISSN 2349 – 784X, Vol.1, Issue 11, May 2015.

9. LIST OF Ph.D. THESIS EVALUATED

- 1) “Digital Speech & Signal Processing Algorithms and Systems” by Basavaraj Ishtalingappa Pawate (Ref. No: KU/EB/D.Lit/4944/2010.), Kuvempu University dated 11/10/2010.
- 2) Design & Implementation of Neuro-Fuzzy based speed control of Induction Motor drive by space vector pulse width modulation for voltage source inverters” submitted by Mr.Ashok Kusagur , Jawaharlal Nehru Technological University, Hyderabad (Ref. No:P7/Ph.D/1133/5-5/AK/EEE/2011 dated 20/06/2011) dated 23/11/2011.
- 3) “Design of efficient authentication algorithm with Bio-metric data and its performance analysis” by Ms Hegde Chetana Laxminarayana, Bangalore University, Bangalore (Ref.

No:Ex.II/Ph.D/Regn/Com.Sci./HCL/2008 dated 24/01/2013) dated 14/02/2013.

- 4) "Efficient Middleware for Secure Services in Computer Communication Network" submitted by Chandrakant Naikodi, Bangalore University, Bangalore, (Ref. No:Ex.II/Ph.D/Regn/Com.Engg./NR/07-08 dated 24/05/2013) dated 11/07/2013.
- 5) " Application of GA and AI Techniques for Power System Stability with Voltage Source Converter based FACTS Controllers" by R.Thirumalaivasan, Jawaharlal Nehru Technological University, Hyderabad (Ref. No:P7/Ph.D/12177/11-1/TR/EEE/2012 dated 17/09/2014) dated 31/03/2014.
- 6) "Feasibility Analysis of DC Link Voltage In UPQC For Voltage Sag Mitigation Using Artificial Intelligence Techniques"by M.Vishnu Vardhan, Sri Venkateshwara University, Tirupati (Ref. No:B7(2)/Ph.D./EEE/16/2014 dated 14/07/2014) dated 04/08/2014.
- 7) "Quality of Service in Wireless Sensor Networks " submitted by Shivaprakash T, Bangalore University, Bangalore (Ref. No:Ex.II/Ph.D/Regn/CSEngg./NR/2010 dated 02/12/2014) dated 04/03/2015.
- 8) "Design and Performance Evaluation of Efficient Spatial Data Mining Algorithms" submitted by Bharathi Somashekhar, Bangalore University, Bangalore (Ref. No:Ex.II/Ph.D/REGN/COM.SCI/BS/2010) dated 06/06/2015.
- 9) Design of Steganography Algorithms using Spatial and Transform Domain Techniques for Secure Communication submitted by N Sathisha, Bangalore University, Bangalore dated 25/11/2015.

10. Sponsored Projects (Number & Amount)	<ul style="list-style-type: none"> • Received Rs. 20 lakhs for modernizing High Voltage Laboratory from Vision Group on Science and Technology from Government of Karnataka during 2013 • Involved in setting up of Modern Power System Simulation Laboratory under AICTE - MODROBS Scheme for which received a grant of Rs 5 lakhs in 2002.
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11. Professional Training Received / Summer / Winter Schools attended:

Year	Nature of Training	Duration	Organization where training was provided
2013	"Quality Initiatives in Technical and Higher Educational Institutions"	1 week	Engineering Staff College of India, Hyderabad
2011	"Management and Leadership Development Training course" under the Scheme National Programme for Training of Scientists and Technologists working in the Government Sector.	1 week	National Academy of Administration, Mussoorie, India

2009	"Mission 10X Workshop"	1 week	Conducted by Wipro, Bangalore held at UBDTCE, Davangere
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2009	"Mission 10X Learning Approach"	2 days	Conducted by Wipro, Bangalore held at GMIT, Davangere
2008	Short Term Course on Software Engineering as a Part of Indo-US Collaboration for Engineering Education (IUCEE)	1 week	Infosys Technologies, Mysore

12. Conferences / Seminars / Workshops attended:

Year	Conferences / Seminars /Workshops attended	Title of paper presented
2013	Workshop on Case Study Teaching Methodology at IBS, Bangalore	Participant
2013	Workshop on Faculty Development Programme organized by CSI and VTU, held at VTU, Belgaum	Participant
2013	National Seminar on Research Opportunities in Bio-Fuels organized by VTU, Belgaum	Participant
2012	International Conference on Emerging Research in Electronics, Computer Science and Technology at PES College of Engineering, Mandya	Chairperson for the Session
2011	National Conference on Soft Computing - 2011, Organized by VIT, Vellore	Statistical Classification with barcode based feature vectors for Numeral Recognition
2011	26 th Indian Engineering Congress organized by IEI, Karnataka State Centre, held at Bangalore	Participant
2010	Workshop on "Mathematics and its Applications in Engineering", held at BIET, Davangere	Participant
2009	Workshop on "Engineering Education: Past, Present and Future" held at UBDTCE, Davangere under World Bank Aided TEQIP	Participant
2009	Workshop on "Intellectual Property Rights", held at UBDTCE, Davangere under World Bank Aided TEQIP	Participant
2008	Workshop on "IPR, R&D and Funding Agencies", held at MCE, Hassan under World Bank Aided TEQIP	Participant

13. CONTINUING EDUCATION PROGRAMMES / SHORT TERM COURSES / WORKSHOPS / SEMINARS ETC. ORGANIZED:

Title of Programme	Period	Funding Agency
2009 - "Mission 10X Workshop" - Faculty Training Programme	1 week	Wipro, Bangalore
2008 - Workshop on "Placement Training Programme" for Students under Tribal Development Programme.	3 days	World Bank Aided TEQIP
2007 - Workshop on "Recent Trends in Power System Simulation".	2 days	World Bank Aided TEQIP

2007 - Workshop on "Artificial Neural Networks and its Applications".	2 days	World Bank Aided TEQIP
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14. PROFESSIONAL MEMBERSHIP AWARDS AND RECOGNITION:

a. **Awards:**

- ISTE 2006 National Award: **Rajaram Bapu Patil National Award "Promising Engineering Teacher Award"** for contribution to Technical Education (Colleges)

b. **Professional Membership:**

- F.I.E. - Fellow of the Institution of Engineers (India) (Incorporated by Royal Charter 1935)
- MISTE - Life Member of Indian Society for Technical Education, New Delhi.

15. EXPERIENCE IN ADMINISTRATIVE POSITION, CURRICULUM AND LAB DEVELOPMENT, STUDENT WELFARE, PROFESSIONAL AND OUTREACH ACTIVITIES:

a. **Administrative Experience:**

- Served as Dean (Administration) of the College between 2013 to 2014.
- Served as Chairman (HOD) of E & E Department during February 2011 to February 2013.
- Served as Governing Council Member (UGC Nominee) of PSG Technology, Coimbatore for the period 2011 to 2014.
- Serving as AICTE Expert Committee Member
- Serving as LIC Committee Member of VTU.
- Served as Edusat Coordinator.
- Contested and won the election for State Committee Member of Institution of Engineers (India), Karnataka State Centre, Bangalore for Sessions 2010-2011 and 2011-12 from Electrical Engineering Division.
- Served as Placement and Training Officer of the College during September 2007 to February 2009.
- Served as STEP Director of the College during 2010 to 2011.
- Served as coordinator of Internet Centre during 2004 to 2008.
- Served as NSS Officer of the College during 1999 to 2002
- Served as Sport Secretary of the College during 2004 to 2006.
- Served as Member of College Steering Committee Constituted by VTU.
- Served as Academic Advisory Committee Member for 26th Engineering Congress, held at Bangalore.
- Served in various committees for the implementation of TEQIP Project during 2006 to 2008.
- Served as Academic Advisory Council Member for School of Engineering of UBDTCE, during 2008.
- Served as Joint Honorary Secretary of Davangere Local Centre of the Institution of Engineers (India) during 2006 to 2008.
- Served as Committee Member of Davangere Local Centre of the Institution of Engineers (India) for six years.

b. **Professional/Exam Work:**

- i) Kuvempu University / Davangere University
- Served as Member of Faculty of Engineering during 2005 and 2010.
 - Served as Chairman, BOE for PG Programme during 2010.
 - Served as Member of PG Board of Studies during 2007 and 2008.
 - Served as Chairman, BOE for UG Programme during 2006.

- Served as Member for UG and PG BOE during 2000 to 2010.
 - Served as Member of Board of Appointment of Examiners.
 - Served as coordinator of D.Sc. Viva-voce exam committee during 2012.
- ii) Visvesvaraya Technological University
- Served as Member of BOE during 2011
- iii) Evaluated Nine Ph.D. thesis.
- iv) Served as BOS Member of Basaveshwara Engineering College (Autonomous), Bagalkot in 2011.

c. Curriculum and Laboratory Development:

- Involved in Curriculum Preparation for B.E and M.Tech in Kuvempu and Davangere University.
- Involved in Curriculum Development of CBCS for B.E in Kuvempu and Davangere University.
- Involved in setting up of Modern Power System Simulation Laboratory under AICTE - MODROBS Scheme.

d. Students Welfare:

- Presently in-charge of Boys' and Girls' Hostel of the College as Dean (Administration).
- Served as Organizing Secretary of Kuvempu University Athlete Meet during 2004.
- Serving as Mentor for students.
- Served as Proctor of the College Students' Association during 2011.
- Instrumental in Placing around 200 students through Placement and Training Office in the year 2007-08.
- Helping Socially and Economically backward students of the college through Alumni and Public Participation.

16. OTHER RELEVANT INFORMATION:

- 1) Instrumental in setting up of Exclusive Internet Center of the College under AICTE - Ernet Scheme along with assistance of the Kuvempu University during 2004.
- 2) Instrumental in setting up of separate placement and training office during 2008.
- 3) Served as Staff Association Secretary of the College during 2000 to 2002.
- 4) Serving as State Executive Committee Member of All Karnataka Engineering College Teachers' Association, Karnataka.
- 5) Delivered ten invited talks in National Seminars / Workshops / STTP's at various Colleges.

I hereby declare that the above information are true to the best of my knowledge and belief.

Date: 31.03.2016

Place: Davangere



Signature