


<b>Name of the Faculty</b>	<b>Aishwarya N Payannavar</b>			
<b>Faculty-ID</b>	2VXLVS0021160			
<b>Designation</b>	Assistant Professor(Adhoc)			
<b>Date of Joining</b>	03/02/2023			
<b>Gender</b>	Female			
<b>Appointment Type</b>	Adhoc			
<b>Institute Name</b>	VTU, Belagavi			
<b>Academic Profile</b>	<b>UG</b>	<b>PG</b>	<b>PhD</b>	
	ELECTRICAL AND ELECTRONICS ENGINEERING	M.Tech in VLSI Design and Embedded Systems	-	
<b>Total Experience in Years</b>	<b>Teaching</b>	<b>Industry</b>	<b>Research</b>	
	2.5	-	-	
<b>Research Profile</b>				
<b>Research areas</b>	-			
<b>Projects/Thesis (UG/PG/ Ph.D.)</b>	<b>PG</b>		<b>Ph.D.</b>	
	<b>Guided</b>	<b>On-going</b>	<b>Guided</b>	<b>On-going</b>
	-	-	-	-
<b>Papers Published in journals</b>	<b>National</b>		<b>International</b>	
	-		-	
<b>Papers Presented in Conferences/workshops</b>	<b>National</b>		<b>International</b>	
	-		-	
<b>Books Published / IPRs/ Patents</b>	-			
<b>H-index</b>	<b>Scopus</b>	<b>Google-Scholar</b>	<b>Other</b>	
	-	-	-	

Professional Profile	
Professional Memberships	IEEE MEMBER(2024)
Consultancy Projects(if any)	-
Awards(If any)	-
Grants fetched	-
Interaction with Professional Institutions/Bodies	-