

Dr.T C Thanuja

Prof. & Chairperson

Dept. of Electronics and Communication Engg.

Jnanasangama, Machhe,

Visvesvaraya Technological University,

Belagavi- 590018, Karnataka

Email: tcthanuja@vtu.ac.in



Faculty ID: 4011

Date of Joining: 06.01.2014

Appointment type: Permanent

Education:

- 2010- Ph. D in Electronics and Communication Engineering, Avinashilingam University for Women, Coimbatore, Tamilnadu
- 2000- M.E. - Electronics & Communication Engg. BMS College of Engineering, Bangalore, Bangalore University, Karnataka
- 1995- B.E. - Electronics & Communication Engg., Siddaganga Institute of Technology, Tumkur, Bangalore University, Karnataka

Professional Experience

- Worked as an Associate Professor in the dept. of Telecommunication Engg. at R V College of Engg., Bengaluru, Karnataka, from Jan 2011 to 31st Dec 2013
- Worked as Assistant Professor in the dept. of Telecommunication Engg. at R V College of Engg Bengaluru, Karnataka, from 29th Oct ,2007 to Dec 2010
- Worked as a Senior Lecturer in the dept. of Telecommunication Engg. at R V College of Engg Bengaluru, Karnataka, from 09th OCT, 2005 to 28th Oct, 2007
- Worked as Lecturer in the dept. of Telecommunication Engg. at R V College of Engg. Bengaluru, Karnataka, from 09th OCT, 2000 to 09th OCT, 2005.
- Worked as a lecturer in P V P Polytechnic, Bangalore, 560 059, Karnataka from September, 1996 to September 2000.

ORCID Account: 0000-0002-1065-4556

Scopus ID: 26649284900

Publications :

International journals:

1. Thanuja T C, Dr R Nagaraj, “Schemes for Evaluating Signal Processing Properties of Audio Watermarking”, International Journal Computer Science Network Security (IJCSNS), VOL.8 No.7, July 2008. pp. 242-246. ISSN 1738-7906.
2. Thanuja T C, Uttara Kumari M, “Swift and Precise Reversible Watermarking Scheme for Medical Image Authentication”, International Journal of Computational Intelligence Research (IJCIR), Vol 5, No.1, 2009.
3. Shambulinga M, Dr. T. C. Thanuja, Ramanathan N, “Tone Mapping of HDR Image for LDR Display Using Guided Image Filter”, International Journal of Scientific and Engineering Research (IJSER), Volume 4, Issue6, June 2013, pp3034-3037 (ISSN 2229-5518).
4. Arpitha H, T. C Thanuja “Secret-Sharing-Based Method for Watermarking of Images with Data Repair Capability”, International Journal of computers and communications, Vol.2, issue 1, 2013, pp.24-32, ISSN 2319-8869.
5. Thanuja TC, Kavya M P “Design and Power Analysis of Memory System using Conventional 6T, Sleepy Stack 8T and Single Ended 6T SRAM Cell” International Journal of Computer Applications , pp 0975 – 8887, 2015.
6. Thanuja TC, Kavya M P , “Development of Prototype for Ethernet port with ARM Cortex-M3 Processor for Web Application” International Journal of Engineering Research & Technology (IJERT), ISSN: 2278-0181,Vol. 4 Issue 09, pp 539-544, September-2015
7. Nayina Ramapur, Sujata Pujari, Dr.T C.Thanuja, “Design of Block Level Edge Detector using Spatial Filter”, IJSRD - International Journal for Scientific Research & Development| Vol. 3, Issue 06, 2015 | ISSN (online): 2321-0613.
8. Nayina Ramapur, Sujata Pujari, Dr.T C.Thanuja, “Design of High Speed All Digital Phase Lock Loop for FM Application”, IJSRD - International Journal for Scientific Research & Development| Vol. 3, Issue 06, 2015 | ISSN (online): 2321-0613
9. Ashwini Chougala, T.C. Thanuja, Usha S “Design Of High Throughput And High Speed Hypercut Packet Classification”, International Journal Of Vlsi And Embedded Systems- Vol 06, Article 08622; August 2015 ISSN: 2249 – 6556, pp1590-1594
10. Prapulla S B, Dr. Shoba G, Dr. Thanuja T C, “Smart Refrigerator Using Internet Of Things” Journal of Multidisciplinary Engineering Science and Technology (JMEST) ISSN: 3159-0040 Vol. 2 Issue 7, pp 1795-1801, July – 2015.
11. Thanuja T C, Kavya M P, “Design and Power analysis of memory system using conventional 6T, sleepy stack 8T and single ended 6T SRAM cell” International JI. Of Computer Applications, pp.0975-8887, Aug 2015.

12. Nishant Angadi, Dr. T.C. Thanuja, Kavya M.P, "Prototype Development for USB 2.0 with ARM Cortex-M3 Processor for Data Access", International Journal of VLSI and Embedded Systems.
13. J K Ravan, Dr. T C Thanuja, "Watermarking techniques for Security", International journal of Advanced technology in engineering and science, Vol no. 4, issue no. 1, March 2016, ISSN 2348-7550
14. Sagar Latti, T C Thanuja, "Razor Based Low-Power Multiplier with Variable Latency Design", International Journal of Science and Research (IJSR) ISSN (Online): 2319-7064 Index Copernicus Value (2013): 6.14 | Impact Factor (2015): 6.391, Volume 5 Issue 5, May 2016, pp1346-1349.
15. Mahadevappa Mugalihal, Dr. T. C. Thanuja, "Design and Implementation of an Efficient 32×32 Bit Vedic Multiplier Using Kogge Stone Adder", International Journal of Advanced Research in Computer Science and Software Engineering, Volume 6, Issue 6, June 2016 ISSN: 2277 128X.
16. K Vinod Kumar & Dr. T.C Thanuja, "Design and Analysis of Frequency Synthesizer PLL Using 45nm CMOS Technology" Imperial Journal of interdisciplinary research, Vol-2, Issue-9, 2016 ISSN: 2454-1362, pp-85-91, impact factor-3.75.
17. Kanchan, T C Thanuja, "FPGA Implementation of CORDIC for FFT Applications" International journal of innovative research in computer and communication engineering, ISSN:2320-9801, Impact factor:6.577, Vol. 4, Issue 6, June 2016.pp. 10313- 10318
18. Prapulla S B, Dr. Shobha G, Dr. Tanuja T C, Ensuring Security In An Energy Efficient LEACH, IJARSE, Vol. No.3, Issue No.8, August 2017, ISSN-2319-8354(E) pp-88-101.
19. Shashidhar P K, Dr. Thanuja T C, "Survey on Energy Efficient Routing Issues in IOMT", International Journal for Research in Applied Science & Engineering Technology (IJRASET), ISSN: 2321-9653; IC Value: 45.98; SJ Impact Factor:6.88, Volume 5 Issue IX, September 2017, pp 834-838.
20. Dr.T.C.Thanuja, Vakare Suman R, "Automated Fare Collection System Using Arm", International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering(IJAREEIE), ISSN:2320-3765, Vol.6, Impact Factor:5.621 Issue 6,June 2017, pp 4535- 4542.
21. Tejaswini A Mahajan, Mahesh Neelagar, T C Thanuja, "Design and Simulation of a Multiport Memory Controller for Communication between Master and Slave Port", International Journal Of Science & Research (IJSR), ISSN:2319-7064,Vol.6, Issue 8, , Index Copernicus Value (2015): 78.96 , Impact Factor 2014:5.611, 2015:6.391, August 2017, pp- 1086- 1089.
22. Dr.T.C.Thanuja, Shruti B Haibatti, "IoT Based Smart Village as a Model for Swatch Bharat with a Web App Using Raspberry Pi", International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 6, Issue 8, August 2017, ISSN (Online): 2278 – 8875, pp 6195-6201. IF 5.621.

23. M S Sudha, T C Thanuja, "A robust Image Watermarking Technique using DTCWT and PCA", International J. Of Applied Engineering Research Vol. 12, No.19, pp. 8252-8256, 2017.
24. T C Thanuja, Akshata, "Universal Verification Methodology Based Verification Environment for PCIE Data Link Layer", Vol.4, issue 07, July 2017, IF 5.1891, pp.2068-2072.
25. Mrs. Sudha M S, Dr. Thanuja T C, "FPGA Implementation of DTCWT and PCA Based Watermarking Technique", International Journal of New Innovations in Engineering and Technology Volume 10 Issue 1 February 2019, 039 ISSN: 2319-6319 pp 039-046.
26. Deepika Patil, T. C. Thanuja and B. C. Melinamath, " Air Pollution Monitoring System Using Wireless Sensor Network (WSN)" © Springer Nature Singapore Pte Ltd. 2019 V. E. Balas et al. (eds.), Data Management, Analytics and Innovation, Advances in Intelligent Systems and Computing 808, https://doi.org/10.1007/978-981-13-1402-5_30, pp 391-400 .

International Conferences:

1. Thanuja T C, Nagaraj R, Uttara Kumari M, "Reversible Data Hiding using Increased Peak Histogram", IEEE sponsored 11th International Conference on Computer and Information Technology (ICCIT 2008) & Workshops, Dec. 24-27, 2008, pp.44-47, Khulna, Bangladesh.
2. Thanuja T C, Uttara Kumari M, "Invertible Data Hiding in Color Images using Multiple Peaks Histogram", 2009 International conference on Signal Processing Systems(ICSPS 2009), May 15-17, 2009, Singapore.
3. Thanuja T C and Uttara Kumari M, "Reversible data hiding in gray scale images using multi-level embedding", International conference on Mechanical and Electronics Engineering, 24-27 July 2009, Chennai, India.
4. Uttara Kumari M, Thanuja T C, Harsha Kumar, D Chinmaya, E Jayanath and Ganesh P N, "Watermarking using integer DCT and increased peak histogram technique", International conference on Mechanical and Electronics Engineering, 24-27 July 2009, Chennai, India.
5. Thanuja T C, Uttara Kumari M, Harsha V B, Mahesh P, "Robust video watermarking scheme for copyright protection", International Conference on Signal and Image Processing (ICSIP), 12-14 Aug. 2009, Mysore.
6. Uttarakumari, Thanuja. T.C, Vinay J, kavya N Bhushan, Naren S Vasand, "Watermarking Using Modulo Algorithm for Gray Images" International Conference on Singal and Image Processing, ICSIP 2010, 15-17 December 2010, Page No. 343-348, Chennai, India.
7. Thanuja.T.C, P.Nagaraju, Vinay.J, Kavya.N.Bhushan, Naren S Vasand, "A Robust Modulo Watermarking Algorithm for Colour Images", International Conference on Emerging Trends in Engineering , 4-5 May 2011, Nitte India
8. Thanuja.T.C, P.Nagaraju, Vinay.J, Kavya.N.Bhushan, Naren S Vasand, "Hardware Implementation of Robust Modulo Watermarking Algorithm", International Confernece on Informaiton Systems and Technology, 16-17 May 2011, Thrissur, India.
9. Arpitha H, T. C Thanuja "Secret-Sharing-Based Method for Watermarking of Images with Data Repair Capability", International conference recent trends computer science and engineering, 26th May 2013.

10. Arun S Amingad, Dr K. Sreelakshmi, Dr T. C. Thanuja, "Prediction Error based Data Hiding in P-Frames of compressed Video", 2nd International Conference on Recent Trends in Computer Science and Engineering (ICRTCS)", 26 May 2013, pp 186-189.
11. Sudha M S, Dr.Thanuja T C, "Digital Image Authentication (Dia) - A Survey", Proceedings of IRF International Conference, Bangalore 23rd March-2014, ISBN: 978-93-82702-68-9
12. Dr. T C Thanuja, Sujatha Pujari, Nayina Rampuri, "Design of Low Power SRAM cell", First international conference on large area and flexible microelectronics, Dec 18-20, 2014, Bangalore, pp-36.
13. J K Ravan, Dr. T C Thanuja, "Watermarking techniques for Security", International conference on recent innovations in engineering and management (ICRIEM-2016, on 23rd March 2016 at BIMAT, Kholapur, Maharastra, India, ISBN:978-81-932074-5-1.
14. Prapulla S B, Jayanth Chandra, Madhwesh B, Mudakavi, Shobha G, Thanuja T C, "Multi Mobile Agent Itinerary planning using Farthest Node First Nearest Node Next (FNFNNN) Technique", [International Conference on Computation System and Information Technology for Sustainable Solutions \(CSITSS\)](#), Bengaluru 2016, PP: 105 – 111.
15. Jitendra Ravan, Dr. Thanuja, "Image forgery Detection against Forensic Image Digital Tampering", 2018 IEEE 8th International Conference on Computational Advances in Bio and medical Sciences (ICCABS) Oct. 18-20, 2018, Stan Fulton Building, University of Nevada, Las Vegas, Nevada 978-1-5386-7709-4/18/\$31.00 c 2018 IEEE, pp270-276.

National Publications:

16. Thanuja T C, Harsha Kumar, D Chinmaya, E Jayanath and Ganesh P N "Watermarking using LSB Replacement and Run-Length Coding Techniques", Indian Conference on Computer Vision, Graphics, Image & Video Processing, March 13-14, 2009, Nagpur, India, pp. 17-19.
17. Thanuja T C, Uttara Kumari M, "Multiple Peak Histogram for High Embedding Capacity", Indian Conference on Computer Vision, Graphics, Image & Video Processing, March 13-14, 2009, Nagpur, India, pp. 20-23.
18. Uttarakumari, Thanuja. T.C, Vinay J, kavya N Bhushan, Naren S Vasand," Watermarking using of Interger DCT & Increased peak Histogram Technique of color Images", National conference on VLSI and Multimedia, Oct,2009, Page No. 1123-243,Bangalore.
19. Nagesh S Tikars, T.C.Thanuja,"Plug-in-development for automation of the test script generation using model-based testing, National Conference on Recent Advnnces in Electronics & Communication Engg. 21st May 2010, Bangalore.
20. T C Thanuja, Nadella Mohan, "Echo cancellation enhancement for VOIP" National Conference on Signal Processing & Communication, 17-18, May 2012.
21. Mr. Arun Amingad, Dr. K Sreelakshmi, Dr. T.C. Thanuja, "Prediction Error Based Data Hiding in Motion Vectors of Compressed Video", National Conference on Recent Trends in Communication and Networking NCRTCN-2013, Department Of TCE, DBIT, Bangalore-74 , pp 20-22, 27th, 28th March 2013.
22. Shambulinga M, Dr. T. C Thanuja, N Ramanathan, " HDR Image on LDR Display Using Tone Mapping and Guided Image Filtering," National conference on Recent Advances in

Electronics and Communication Engineering, RNSIT, in association with IMAPS & IETE on 3rd May, 2013, Bangalore.

23. Thanuja T C, Kavya M P, “Design and Power analysis of memory system using conventional 6T, sleepy stack 8T and single ended 6T SRAM cell” National conference on power systems and industrial automation, GSSSIT & Schneider, Mysore, 22nd Aug 2015.

Guest Lectures Delivered

- Motivational talk on Incubation and entrepreneurship at Marata Mandal College of Engineering, Belagavi, 2019
- Delivered a lecture on “Applications of Wireless Nanosensor network in Military, Defence and spacecraft” in Two day National level seminar on 16th Feb, 2017 at Jain College of Engineering, Belagavi
- Delivered a lecture on “Embedded system design using FPGA” in preconference workshop of International conference on “Recent innovations in engineering” at HIT, Nidasoshi on 07.10.2016.
- Member of panel discussion in Two days National conference on “Make in India an opportunity for emerging engineering Technologies” from 5- 6th Feb 2016 at SGBIT, Belagavi.
- Delivered a lecture on “Applications of Image Processing” on 24.09.2008 at the Faculty of Engineering, Avinashilingam University, Coimbatore.
- Delivered a lecture on “System Design using VHDL” on 04.07.2007 at the Faculty of Engineering, Avinashilingam University, Coimbatore.
- Resource person in CEP programs for DRDO employees in July 2004 , July 2005, Jan 2006, Feb 2006, July 2006, October 2006, June 2007.

Academic Research awards

- Recognized for academic excellence on September 9th 2008, by ISTE-RVCE Chapter, R V College of Engineering, Bengaluru.
- Honored for publishing papers in International journals on 26th January 2010, by RSS Trust Bengaluru

Administrative:

University level/ Institution level:

- Member of Board of Studies VTU- UG and PG courses-Electronics board from 2015
- Chairperson of Women cell VTU, Belagavi from 2014

- Member of Scrutiny Committee for “Collaborative Research Funding Scheme” under TEQIP 1.3 at VTU
- Academic Council member of R V College of Engineering, Bengaluru from 2017
- BoS member ECE, Basaveshwar Engineering College, Bagalkot for an year 2017-18
- Warden for Girls hostel at VTU Belagavi campus from 2015 to 2019 Member of editorial Board for VTU news letters from 11 Oct 2017
- Member of Board of Examiner for UG and PG for the academic year-2014-15
- BOE member for Bangalore University, M.Sc. Electronics Science 2011-12, 2017-18
- Panel member to Interview and Scrutinize the Ph. D application during the year 2014-16
- Custodian for VRAT Exam 2016-17
- Subject expert for selection of teaching staff for various affiliated and Autonomous colleges under VTU.
- PhD registration expert committee member in Electrical Engineering Science for the academic year 2014-15/2015-16 /2016-17
- VTU academic organization and administrative review committee member

Administrative Experience at RVCE:

- M/S Pace India MoU department coordinator 2009-10, 2010-11, 2013-14.
- Cultural Activities Secretary during the year 2009-10. During this period
 - organized National level techno-cultural fest “8th mile” 2010,
 - Organized India south zone Quiz competition “Swedish nobel week quiz” 2010 & 2011
 - Organized an International debate tournament under the debate club DEBSOC for the first time in the college -2010.
- Co-ordinator for department level technical fest connect 2008,
- Active member in the department BoS – (2007 to 2013).
- Co-ordinator for Altera-achieva MoU for two years- First year 10 projects groups were participated in the national level project competition conducted by Altera, out of 10 groups 4 groups were selected for finals. In the second year 4 groups were participated and one group was selected for finals.
- Member of TEQIP phase II finance committee at the college level (2010-2013)
- Project coordinator and Project evaluation committee member in the department
- Coordinator-Seminar on new technologies

- Part of a PG and UG NBA committee in the department
- Part of the assessment committee for evaluation of human skills and social service (HSS).
- National Conference 2012 coordinator
- Department level TEQIP Equity Assurance Plan committee member.
- Active member in the department compliance committee.

Professional Affiliation

- Life member of ISTE – LM 32205
- Life member of IETE – AM 149779
- Life member of IACSIT- 80331904
- Life member IMAPS-LM 419

Research Projects:

Projects Executed:

- Co-Investigator for a funded Major Research Project on "Growth and characterization of novel Nano cluster carbon thin film transistor at room temperature", Dept. of Telecommunication Engineering, R V College of Engineering, funded by UGC, for 3 years from 2011 – 2014. Sanction No: 39-884/2010 (SR)
- Worked as Co-investigator of CoE under TEQIP, “Center for Large area microelectronics enabled devices, sensors and RF & Terra hertz based Communication Systems” R V College of Engineering, Bengaluru, Karnataka.

Project Ongoing:

- VGST sponsored GRD-547, No.KSTePS/VGST-KFIST(L2)/2017/269KFIST- LEVEL-2 project on “VTU Software Defined Radio Lab” sanctioned on 21.11.2017 for 40.0Lakhs

Guidance:

UG: 20 Guided

PG: 40 Guided; 02(Ongoing)

PhD: 08 (Ongoing)

Doctoral Students (Ph.D.):

Sl. No.	Name of the candidate	Title of research	Co-supervisor	Year of Registration and mode and dept.	Status
1.	Mrs. Prapulla S B	Secured multimobile agent wireless sensor networks’	-	March 2014/Part Time, CSE	Ongoing

		with improved QoS			
2.	Mrs Apurva D Katre	Optimisation techniques for spectrum sensing in Cognitive Radio Network	-	Jan 2015/ Part Time ECE	Ongoing
3.	Mr. Mahesh Vasanth Mahendrakar	Intelligent Traffic Smoothing	-	Dec 2015/ Part Time ECE	Ongoing
4.	Mr. Jitendra K Ravan	Hybrid watermarking techniques for multimedia security	-	Dec 2015/ Part Time ECE	Ongoing
5.	Mrs.Chetana D M	Design and Fabrication of Metal Oxide based Gas Sensors	Dr. H M Mahesh, Professor, Dept. of Electronic Science, Bangalore University	March 2017 / Full Time, ECE	Ongoing
6.	Mrs. Shashidhara PK	Energy efficient routing over Multimedia Internet of Things	-	March 2017 / Part Time, ECE	Ongoing
7.	Mrs. Deepika D Patil	Air quality prediction and analysis for smart city	Dr B C Melinamath, SCOE, Pandrapur	March 2017 / Part Time, ECE	Ongoing
8.	Mrs. Sudha M S	Implementation of Stegnographic Techniques for Multimedia Security	-	2012, Part Time, ECE	Thesis submitted

National/ International Conferences/ Symposium Organised

- Two day national conference on “Advances in Information and Communication Technology” in R V College of Engineering on 17th & 18th May 2013 under TEQIP.
- TEQIP-II national conference on “Advances in information and communication technology” at RVCE, from 17-18th May, 2013.
- National conference on “Signal processing & communication” at RVCE from 17-18th May, 2012
- One day national conference on “Communication Technologies for global connectivity” in R V College of Engineering on 17th May 2011.
- Two day International symposium on “Research methodologies”, in R V College of Engineering from 2 & 3 June, 2008.
- Two day national conference on “Next Generation Wireless Networks”, in the department of Telecommunication Engineering, R V C E, Bangalore, from May 16-17, 2008.
- A Two day National conference on “Intelligent Models for Data Processing and Management” from May 2-3, 2008, at R V College of Engineering.

Training programs organised

- Organised an expert lecture on “Industrial IOT” by Mr. Ganesg Shankar, Managing Director, Fluxgen Engineering Technologies Pvt. Ltd., Bengaluru on 20.12.2017 from 10.30 am to 12.30 pm at Dept. of VLSI Design & Embedded Systems, Center for P G Studies, Belagavi.
- Organised an expert lecture on “Carrier Oppurtunities in the core area of VLSI” by Mr. Kumaraswamy”, Senior Technical Manager, Trident Tech Labs, Bengaluru on 09.12.2017 from 10.00 am to 12.00 pm at Dept. of VLSI Design & Embedded Systems, Center for P G Studies, Belagavi.
- Organised one day seminar on “Wolfram Mathematica for Education & Research” at VTU Belagavi, on 23rd May 2017.
- Coordinator for organizing a symposium on “Women empowerment and present day challenges for women” in association with KSCW, Govt. of Karnataka on 16th May, 2017.
- Member for organizing the state level science exhibition cum competition on “Science and technological solutions for specially abled persons” on 10th March 2017 at VTU, Belagavi.
- Organised International Women’s day at VTU on 08.03.2017, ISRO scientist were felicitated.
- Coordinated an event (Patriotic song competition) in view of Swami Vivekananda Jayanthi celebration organized by VTU on 14.01.2016
- Chairmen of Reception committee in two day National seminar on “Library consortia in digital age: opportunities and challenges” organized by VTU, Belagavi on 1st and 2nd Dec 2015
- Talk on EDP Awareness programme in the CPG, VTU on 19.11.2015
- Workshop on ARM Cortex Microcontrollers
- Coordinated Seminar on Exploring, Engineering and supporting research at academic institution for new innovations in Science and Technology on 7th March 2015.
- Two days Faculty Development Program on ARM Cortex M4 “NUedu-SDK-Nuc472 ARM Cortex M4” from 21st – 22nd Nov, 2014, Belagavi
- Training on Cadence tool on 12th Sep, 2014.
- Technical Talk on Advances in Embedded Systems by Dr. Priyatam Kumar, BVBCET, Hubli, on 04th Sep, 2014
- Training in Xilinx Tool by Mr. Bikas from Coreel Technology, from 12th – 14th Aug, 2014.
- Training in Cadence, by cadance from 8-9th April 2014.
- Technical Talk Social Communications by Dr. D G Kulkarni organized by Department of VLSI, VTU, Belagavi, on 19th March 2014
- Technical Talk on Presentation skills by Dr. D.H. Rao organized by Department of VLSI, VTU, Belagavi, on 18th March 2014.
- Workshop on Robo Fest by Anil Kumar Dogra, Revert technology organized by Department of VLSI & ES in association with IIT Roorkee and Revert Technology from 14-15th Feb, 2014.
- Three days workshop on “Advanced trends in Image Processing and its applications [ATIP-13]” from 18-20, July 2013, at RVCE under TEQIP.
- One day workshop on “Hands on training on VHDL-AMS” in association Trident Technologies on 28th, June 2011 in the Dept. of Telecommunication Engg

- Organized a two day workshop on “VHDL-AMS” in co-ordination with M/S Mentorgraphics on 28th & 30th of March, 2010 in the Dept. of Telecommunication Engg.
- Organized a workshop on “VLSI & Embedded System Design” in coordination with ALTERA&ACHIEVA in RVCE on 10th & 11th Feb 2006.
- Involved in organizing the “Computer Awareness Program” for the supporting faculty of RVCE on 13th & 20th Oct 2001.
- Involved in organizing CEP for LRDE employees.

AICTE/ ISTE/VGST Sponsored STTPS Attended

- IEEE workshop on driving innovation with IPR and research on 17th sept. 2018 at VTU Belagavi
- VGST sponsored “Cognition and Big Data Analytics” from 24th-27th July 2018, organized by CSE Dept., PG studies, VTU, Belagavi.
- VGST sponsored “System Verilog” from 24th-27th April 2018, organized by VLSI Design and Embedded Systems Dept., PG studies, VTU, Belagavi
- Attended VGST sponsored “Trends in computing Research from 15-18th June 2015, organized by CSE, PG studies, VTU, Belagavi.
- Attended IEEE international conference in VLSI at Bangalore organized by IEEE, on 7th Jan 2015
- Attended VTU-VGST sponsored FDP on “Quality enhancement in teaching and research methodology to bridge the gap between academics and industry” from 27th-30th May 2014 at VTU, belagavi.
- Course on “Teaching Engineering Using Matlab & Simulink” as a part of Indo-US Engineering Faculty Leafership Institute Workshops (IUCEE) from 12th July to 14th July, 2011 at R V College of Engineering, Bangalore.
- SDP on “Low power VLSI Design & its implementation” on 29th June to 11th July 2009. at Dept. of ECE, RVCE, Bangalore.
- ISTE sponsored STTP on “VLSI Design, Development & Testing” from 20th – 24th March 2007, at SJGIT, Chikballapur.
- ISTE & SFPU sponsored One week STTP on “Emerging Trends in VLSI technologies” from 18th-23rd Dec, 2006, at Dr. AIT, Bangalore.
- ISTE and TEQUIP sponsored STTP on “Recent trends in advanced communication engineering” from 20th – 25th March 2006 at Dr. AIT Bangalore

Peer-Review Activities

- Journal of Systems and Software- Elsevier Journal with Impact factor 1.277
- Imaging Science Journal – IEE Journal
- Journal of Engineering and Computer Innovations
- Sadhana - Academy Proceedings in Engineering Science