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R & D Coordinator

Convener/Program Coordinator

Dr. (Smt.) Shobha R. Savanur
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REGISTRATION FORM

VGST Sponsored One Week
Faculty Development Programme on
" **Design & Implementation of Hardware Accelerators and Protocol on FPGA"**

Date : 13th to 17th December 2018

Name :

Institution :

Is Institute is approved by AICTE?(Yes/No) :

Designation :

Department :

Educational Qualification :

Specialization :

Address :

Contact No :

Email Id :

Accommodation required(Yes/No) :

The given information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the course for the entire duration.
Place:
Date:

Signature of the Applicant   *Signature with Date*
                                 HOD/Principal

BLDEA's
Centenary Year-2010

B.L.D.E Association's

V.P.Dr. P.G.Halakatti College of Engineering & Technology, Vijayapura-586103

(Affiliated to VTU, Belagavi, Approved by Govt. of Karnataka & AICTE, New Delhi)
(NAAC Accreditated)
NBA Accreditated Branches, UG-EEE, CSE, ME,CV
(Validity 01.07.2018 to 30.06.2021)

Department of Electrical and Electronics Engineering
(Accreditated by NBA New Delhi)

KSTePS

VGST Sponsored One Week
Faculty Development Programme
on
"Design & Implementation of Hardware Accelerators and Protocol on FPGA"
(13th to 17th December 2018)

Organized by
Department of Electrical and Electronics Engineering

B.L.D.E Association's
V.P.Dr. P.G.Halakatti College of Engineering & Technology, Vijayapura

BLDEA's V. P. Dr. P. G. Halakatti College of Engineering and Technology is one of the premier institutions of Northern Karnataka region with an enormous campus spread across 14 acres of land in the historical city of Vijayapura. The esteemed institution was established in the year 1980 under the umbrella of BLDE Association. The Institute is affiliated to Visveswaraya Technological University, Belagavi and is approved by AICTE, New Delhi. Five programs have been accredited twice by National Board of Accreditation (NBA), New Delhi, during the year 2005, 2015 and four programs have been reaccredited during the year 2018. Recently, Institute is also accredited by National Assessment and Accreditation Council (NAAC) with B+ grade. The Institute has 8 under graduate programs - Civil Engineering, Mechanical Engineering, Electrical and Electronics Engineering, Computer Science and Engineering, Electronics and Communication Engineering, Information Science and Engineering Architecture, Automobile Engineering. The Institute also has 5 M.tech. programs in Structural Engineering, Machine Design, Computer Science, Digital Communication and Networking, Microelectronics & control Systems. The institute also has MBA and MCA programs

Department of Electrical and Electronics Engineering

Electrical Engineering Department is one of the basic engineering departments established in the year 1980.

The department offers Four years graduate (B.E) programme, also one Post Graduate programme in Microelectronics and Control System (EMS). The department is thrice NBA Accreditated for 3 years (2005-2008, 2015-2018, 2018-2021). The department has competent faculty resource and well established laboratories. The department aims to promote academic and professional excellence in all of its endeavors and develop its member's capabilities enabling them to be socially responsible engineers and contribute significantly to the development of mankind.

About The Faculty Development Programme

An intensive one-week training programme is being organized for faculty of engineering and technological institutions. The purpose of this programme is to give a progressive awareness to technology enabled teaching-learning practice to engineering educators with emphasis on Hardware accelerators and protocols on FPGA and DSP Applications.

Objectives of the Programme

The main objectives of organizing this programme are
- To impart design experience of complex digital systems
- To give hands on experience on CAD tools & FPGA Kits
- To design digital building blocks like arithmetic circuits, processing elements & memories.
- To understand FPGA architecture & timing issues.

Expected Outcomes of the Programme

- To improve competent dimensions of the faculty in the field of FPGA Design.
- Extensive deliberations for the design and implementation of hardware accelerators and protocol implementation on FPGA platform.

RESOURCE PERSONS

- **Dr. Niranjan U. C. :** Director of Research & Training, Manipal Dot Net Pvt. Ltd., Indian Institute of Science, Bengaluru.
- **Mr. Chetan S. Deokar :** Lead research Engineer, Seimens Corporation Research and Tech. Ltd., Bengaluru.
- **Mr. Sachin S Munji :** Design Engineer (VLSI), Sai Tektronix Pvt. Ltd. Bengaluru.

Eligibility

Faculty members from academic Institutions (Technical Colleges / universities / Research Organisations) and professional from industries, PG Students are eligible to attend the FDP programme. The number of seats is limited to 50 and selection is based on First Come First Serve basis.

Free boarding & Lodging facility will be provided (as per availability) to eligible outstation participants for AICTE approved Institutions and T.A/D.A will be reimbursed as per norms.

REGISTRATION

No Registration Fee

Last date of receipt of registration form is 8th December 2018.
- The selected participants will be intimated through mail by 10th December 2018.

- Candidates are required to email their registration form duly signed by principal/Head of the department.