

**AICTE-VTU Joint Training Programme for  
Teachers from AICTE Approved & VTU  
Affiliated Technical Institutions**

**One Week Training Programme for Teachers  
On**

**“An Overview of Teaching Techniques in  
Microcontroller and Embedded Systems”**

**(20<sup>th</sup> - 24<sup>th</sup> Jan 2020)**

*(A Training Programme for faculty Members of VTU  
Affiliated Institutions)*

**REGISTRATION FORM**

Name:.....

Designation & College:.....

Email ID:.....

Mobile:.....

**Declaration by the Applicant**

If I selected, I agree to abide by the rules and regulations of the  
Training Programme of VTU-HRDC and shall attend all the  
sessions compulsorily.

Date: Signature of Application

Recommended and Forwarded

Signature of the Head of the Institution (With Seal)

**CHIEF PATRON**

**Dr. Karisiddappa**

Hon'ble Vice Chancellor, VTU, Belagavi

**PATRONS**

**Dr. Anand S Deshpande**

Registrar, VTU, Belagavi

**Dr. Satish Annigeri**

Registrar (Evaluation), VTU, Belagavi

**Mrs. A. Sapna**

Finance Officer, VTU, Belagavi

**CONVENERS**

**Dr. N Chikkanna**

Coordinator, VTU-CPGS, Muddenahalli

**Dr. Shiva Murthy G**

Associate Professor & Programme Coordinator, Dept. of CSE  
VTU-CPGS, Muddenahalli

**ORGANISING COMMITTEE**

**Dr. G S Venkatesh** Professor, Dept. of CAE  
**Dr. Indumathi T S**, Professor, Dept. of DECS  
**Dr. Dinesh Rangappa**, Professor & Prog. Coordinator, Dept. of NT  
**Dr. Sarika Raga**, Assoc. Prof. & Prog. Coordinator, Dept. of DECS  
**Dr. Pankajakshi R** Prog. Coordinator, Dept. of MBA  
**Dr. Basawaraj**, Associate Professor, Dept. of APT  
**Dr. Thirtha Prasad H.P**, Associate Professor, Dept. of CAE  
**Dr. H.H. Ramesha**, Associate Prof., Dept. of MBA  
**Dr. Kanta D Devanagavi**, Asst. Professor, Dept. of MCA  
**Mr. Yadhunaik B H**, Asst. Professor, Dept. of MCA  
**Mrs. Renuka Malge**, Asst. Professor, Dept. of MCA  
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**Mr. Sanjeev G Palekar**, Asst. Professor, Dept. of APT  
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**Mrs. Veenarani K**, Asst. Professor, Dept. of MBA  
**Mr. Vinod Kumar G**, Sr. Asst. VTU, Muddenahalli

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**Organized By:**

**VTU- Human Resource Development Cell (HRDC)  
Visvesvaraya Technological University  
Centre for Post Graduate Studies  
Bengaluru Region, Muddenahalli  
Chikkaballapur-562101**

**Venue:**

**Seminar Hall  
VTU Centre for Post Graduate Studies,  
Bengaluru Region, Muddenahalli  
Chikkaballapur-562101**

### About the VTU

Visvesvaraya Technological University (VTU) named after Bharat Ratna Dr. Sir M. Visvesvaraya established on 1st April 1998, as per the VTU Act 1994 of the Government of Karnataka. University has been established by the Government of Karnataka in order to promote planned and sustainable development of technical education has its Head Office located in Belagavi in the state of Karnataka. Reckoning the fact that, University is a seat of confluence of knowledge and as a technological University, Visvesvaraya Technological University does not restrict itself only to curricular activities but emphasizes on the overall growth of knowledge in society and hence its campus in Belagavi is named aptly as “Jnana Sangama”. The University has jurisdiction over the whole state of Karnataka. VTU has established four regional centers at Bengaluru, Mysuru, Kalburgi and Belagavi for administrative convenience. VTU is one of the prestigious and largest technological Universities in the India having affiliation of 220 Engineering colleges and 20 Autonomous Colleges and one Constituent College.

VTU is imparting technical education to more than 4.5 lakh students under its 35 Undergraduate & 94 Postgraduate Programs. VTU is offering Doctoral programs in 865 departments in various affiliated Colleges which have been recognized as research centers. Around 5000 Research Scholars are perusing for Doctoral and M. Sc. (Engg.) programmes. VTU has established full-pledged Centers for Post Graduate Studies at Muddenahalli, Kalburgi, Belagavi and Mysuru.

### About AICTE, New Delhi

All India Council for Technical Education (AICTE) was set up in November 1945 as a national-level apex advisory body to conduct a survey on the facilities available for technical education and to promote development in the country in a coordinated and integrated manner. And to ensure the same, as stipulated in the National Policy of Education (1986), AICTE was vested with:

- Statutory authority for planning, formulation, and maintenance of norms & standards
- Quality assurance through accreditation
- Funding in priority areas, monitoring, and evaluation
- Maintaining parity of certification & awards

### About the VTU PG Centre, Muddenahalli

The Visvesvaraya Technological University, Post Graduate Center, Bengaluru Region, Muddenahalli has 200 acres campus located at Muddenahalli, Chikkaballapura (Dist). Muddenahalli is the birth place of great legend Bharata Ratna Sir M. Visvesvaraya. It has all necessary infrastructure required for imparting quality education through its well-structured administrative and academic staff for overall development of student’s personality. Center for PG Studies has five full time post graduate programmes in M.Tech, MBA and MCA. All the departments have well qualified faculty.

### PROGRAMME OBJECTIVES:

The objective of the AICTE – VTU Training Programme is to conduct training for the faculty members to prepare them for being successful in a knowledge society and fulfill the following important Teaching Techniques in subject.

- Understand the architecture and programming of X86 Microcontroller
- Familiarize the Microcontroller interfaces and its programming
- Familiarize and strengthen the knowledge on the ARM7 based microcontrollers architecture and programming

### PROGRAMME OUTCOME:

- To refresh the fundamental knowledge on an overview of X86 Microcontroller architecture and ARM7 based microcontrollers Embedded Systems
- To enhance the microcontroller and its interfaces programming skills.
- To enrich ARM7 based microcontrollers programming skills
- To enable and develop ARM7 based microcontrollers Applications

### Resource Persons:

Renowned Experts from reputed Institutions/Industry and Senior Faculty Members of VTU.

**Eligibility:** Teachers from AICTE Approved and VTU Affiliated Technical Institutes who are handling “Microcontroller and Embedded Systems” subject of UG & PG programmes are eligible to participate.

**Registration Fee: No Registration Fee**

**Accommodation & Food:** Free accommodation and food will be provided for participants.

**TA will be provided to the outstation Participants as per the norms of the programme on production of Bus/Train Ticket.**

### How to Apply:

Interested and eligible candidates may apply by submitting the hard copy of the filled in registration format given in this brochure to **The Coordinator, VTU Center for PG Studies, Muddenahalli** and also by sending the scanned copy of the filled in registration form by e-mail the [shivamurthy@vtu.ac.in](mailto:shivamurthy@vtu.ac.in) on or before **13<sup>th</sup> Jan 2020**.

### Selection of the Participants:

AICTE-VTU Training Programme will be of FIVE Days residential programme with minimum 40 participants. Selection will be on first cum first served basis. Selected candidates will be intimated by e-mail on **16<sup>th</sup> Jan 2020**.

### Contact for Information:

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