

AGENGA/SCHEDULE

DATE	Timings	DAY	TOPICS PLANNED	Resource Person
30/07/2020	10:00 To 10:15	Thursday	Introduction to the FDP and the Host Institute, NMIT	Dr Ramachandra A C HoD, Dept. of ECE, NMIT, Bangalore
	10:15 To 11:45		VLSI Design Flow	Dr S L Pinjare, Visiting Professor, Dept. of ECE, NMIT, Bangalore
31/07/2020	09:45 To 12:00	Friday	Spice net listing Simulation through command line Demo to complex IP in IMESSAM ecosystem.	Saikrishna, Inohmic Technologies, Bangalore
01/08/2020	09:45 To 12:00	Saturday	Fabrication basics , Inverter GDS with DRC/LVS	P V Reddy, Inohmic Technologies, Bangalore
02/08/2020	09:45 To 12:00	Sunday	RTL basics, FIFO hands on Memory, Sequence detector	Venkata , Versha, Inohmic Technologies, Bangalore
03/08/2020	09:45 To 12:00	Monday	PD flow, Synthesis, STA	P Swaroop, Inohmic Technologies, Bangalore