



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ

ವಿಟಿಯು ಅಧಿನಿಯಮ ೧೯೯೪ ರ ಅಡಿಯಲ್ಲಿ ಸರ್ಕಾರದಿಂದ ಸ್ಥಾಪಿತವಾದ ರಾಜ್ಯ ವಿಶ್ವವಿದ್ಯಾಲಯ

VISVESVARAYA TECHNOLOGICAL UNIVERSITY

State University of Government of Karnataka Established as per the VTU Act, 1994 "JnanaSangama" Belagavi-590018, Karnataka, India

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REGISTRAR

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REF: VTU/BGM/Aca-Cirs/SA/2023-24/4036

DATE: 11 NOV 2023

NOTIFICATION

Sub: Subject (Course) Equivalence between the 1st semester's syllabus of program common to affiliated college and university department - reg..

Ref: 1. Equivalence Committee meeting held on 29-00-2023

2. Resolution No. 2.2.2 of the 174th Executive Council meeting, dt:7-10-2023

With reference to the above, **Subject (Course) Equivalence between the 1st semester's syllabus of program common to affiliated college and university department** is notified as under:

- The VTU students who are shifting from VTU Autonomous syllabus to affiliated Syllabus have to study the additional subjects before the award of Degree. These subjects will not be considered for vertical progression.
- The students will be considered for award Ranks, if they satisfy all other applicable regulations
- The **Subject (Course) Equivalent is enclosed**

It is the complete responsibility of the department/s to appraise both student and parent about the change of scheme.

These students are also to be hand helded by the department/s while registering on student registration portal and also in filing examination applications. The departments are liable for any laps in this regard.

The contents of this notification may be brought to the notice of all the concerned.

Encl: As above

To,
The Chairpersons / Programme Co-ordinators of VTU Departments

Copy to:

- The Hon'ble Vice Chancellor, through the Secretary to VC, VTU Belgaum, for kind information
- The Registrar (Evaluation), VTU Belgaum, for information.
- The Regional Directors of VTU Regional Offices for information and needful.
- The Special Officers and Case Workers of Academic Section, VTU Belgaum, for information and needful.

BY ORDER,
R. S. 05/11/23 K-E
REGISTRAR
A. S. 7

M.Tech., University department Syllabus Equivalence to Affiliated College Syllabus for the same program at 1st-semester level

01.	M.Tech., in Computer Science & Engineering	<ul style="list-style-type: none"> Additional subject /course to study and pass before the completion of the degree is 22SCS14- Internet of Things and Applications (3 credits) The subject 22SCS22 AI&ML in 2nd semester was exempted from the study and pass as the students have already studied and passed a similar subject in 1st semester (22VSC15- AI&ML)
02	M.Tech., Design	<ul style="list-style-type: none"> Additional subject/course to study and pass before the completion of the degree is 22MMD14-Signal Analysis and Condition Monitoring (3 credits). The 22MMD326- Non-Destructive Evaluation is exempted from the study and pass against the studied subject 22UMD14- Non-Destructive Evaluation
03	Master of Computer Application(MCA)	<ul style="list-style-type: none"> Additional subject/course to study and pass before the completion of the degree is 22MCA15- Design and Analysis of Algorithms. 22MCA23-Software Engineering, and 22VMC15-Software Engineering they are equivalented for credits, however, syllabus they differ slightly. Hence if the students have a backlog in the subject 22VMC15 Software Engineering, chance (as error) must be given to the students of the University department to appear and pass this course.
04	M.Tech., in Artificial Intelligence and Data Science (AIDS)	<ul style="list-style-type: none"> The students have to study and pass the additional subjects 22SAD14 Human Computer Interface and 22SAD15-Advanced Data Structure before the completion of the program, however, these subjects are not considered for vertical progression. 22SAD232- Business Intelligence and Analytics is exempted from the study and passes against 22VSA13, provided if the students opt for 22SAD232 as an elective subject. If the students have failed in 22VSA13 subject then they have to take 22SAD232 in 2nd semester and pass 22SAD325- Pattern Recognition is exempted from the study and passes against 22VSA14, provided if the students opt for 22SAD325 as an elective subject. If the students have failed in 22VSA14 subject then they have to take 22SAD325 in 3rd semester and pass 22SAD14-Human Computer Interface and 22SAD15 Advanced Data Structures these two courses/subjects were planned to be conducted as Practical sessions to enhance the skill components in the Departmental syllabus as additional courses with both CIE(50 marks) and SEE(50 marks). These courses shall be conducted before completion of 4th semester and marks scored and are considered against these two courses of affiliated syllabus and with same credits
05	M.Tech., in VLSI Design and Embedded System	<ul style="list-style-type: none"> Additional subject to study and pass before completion of the degree is 22LVS12ASCI Design. 22LVI12 Advanced Digital Design and Verilog HDL (4 credits) will not be considered for credit. The University Department shall issue the course completion certificate for the subject (Course) 22LVI12 Advanced Digital Design with Verilog HDL (4 credits) (IPCC) which will help the students in their career

General Instructions

22AUD19- BoS recommended Online Courses before the completion of the degree. However, these subjects will not be considered for vertical progression.

The students are eligible for the award of rank as per the governing regulation of the program



